

野火_RK3566_LUBANCAT_ZERO_WIFI_原理图

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历史版本

版本号	日期	设计	描述
V0.1	2022-04-18	ghs	初始版本
V0.2	2022-07-19	ghs	修正mini HDMI引脚定义，mipi csi添加两对数据线
V1.0	2022-12-13	ghs	删除TF卡检测引脚，将其直接接地，降低因氧化引起的不启动问题

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Title
野火_RK3566_LUBANCAT_ZERO_WIFI_原理图

Size
A3

Document Number
Historic Version

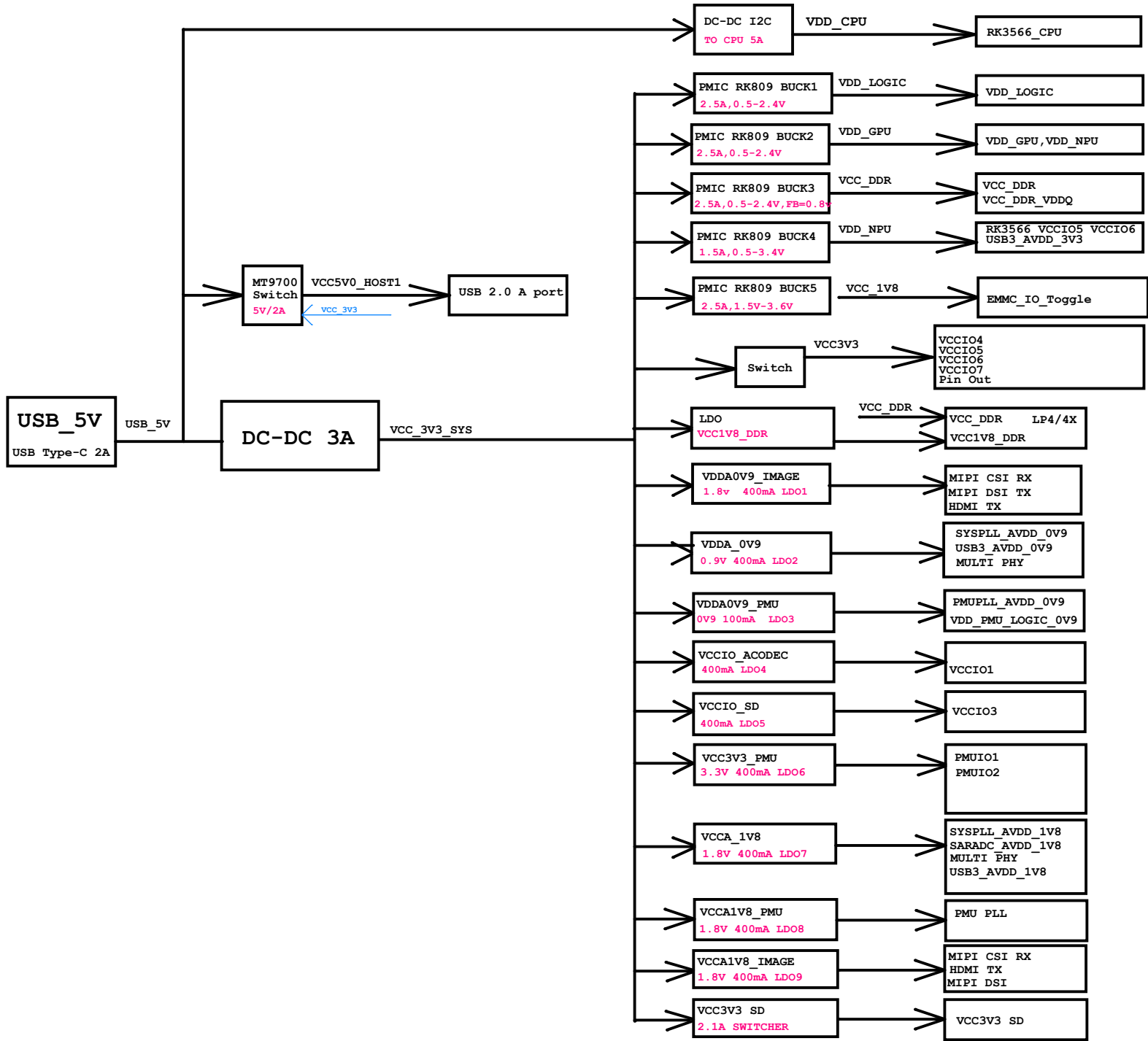
Rev
V1.0

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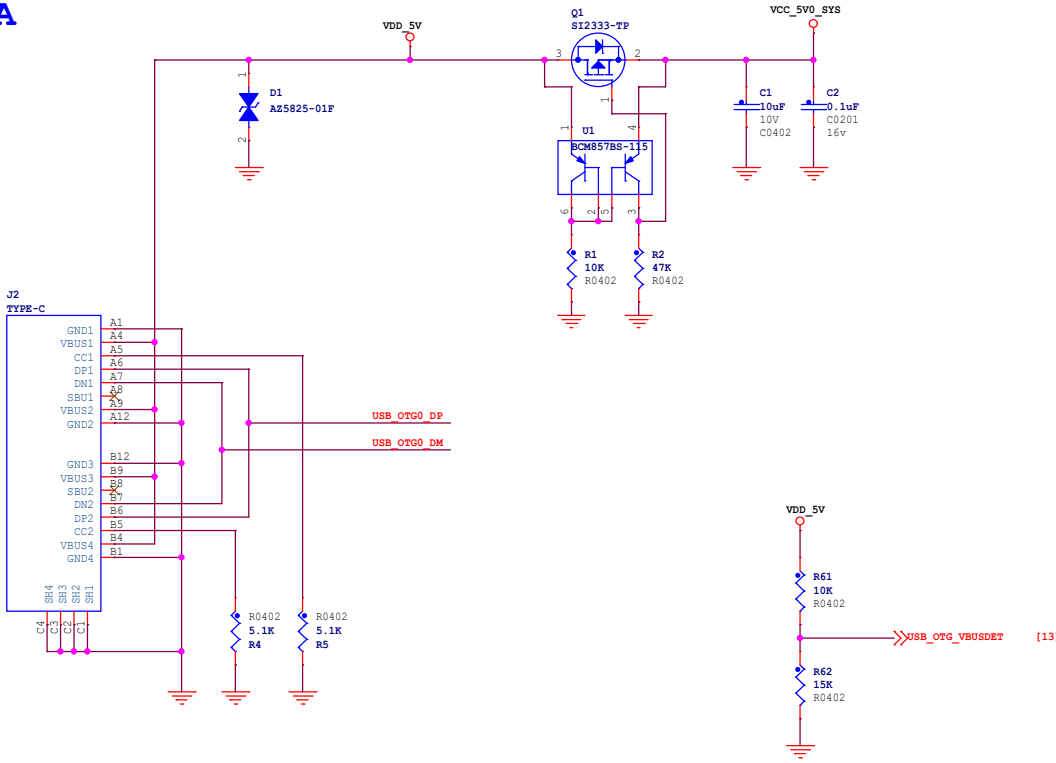
版本号	日期	设计	描述
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<p>Title</p> <p>野火_RK3566_LUBANCAT_ZERO_WIFI_原理图</p>			
Size A3	Document Number Historic Version	Rev V1.0	
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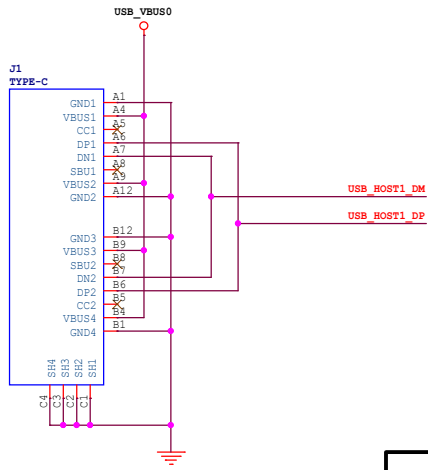
POWER DIAGRAM



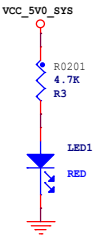
POWER IN
OTG Download
5V 3A



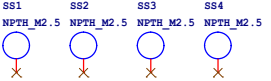
USB 2.0 HOST x1



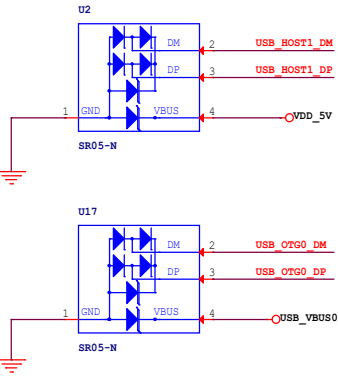
PWR_LED



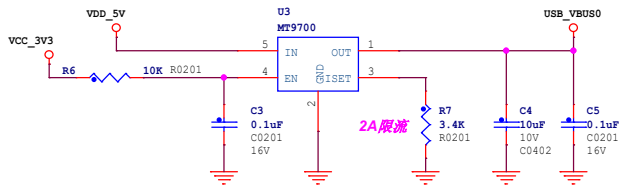
机械定位孔
4*M2.5



USB ESD



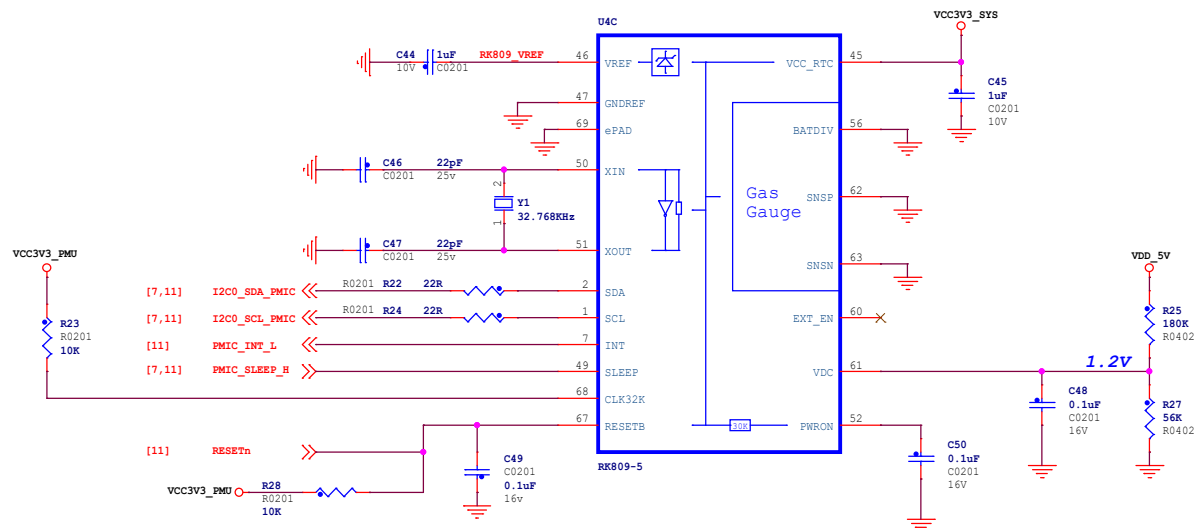
USB POWER



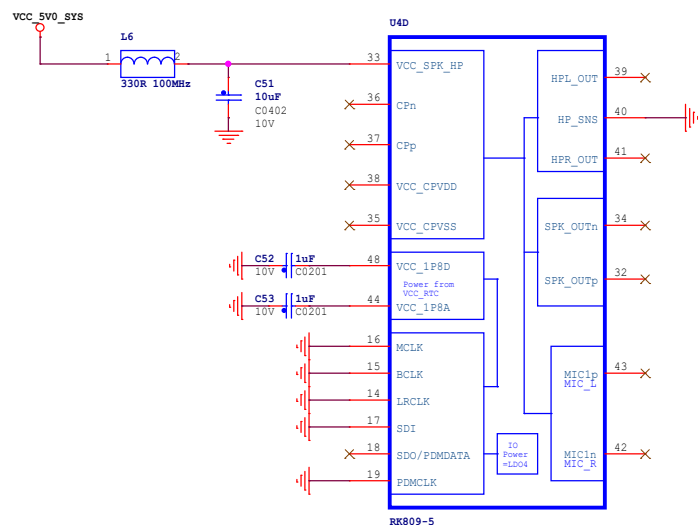
- USB_HOST1_DM >>> USB_HOST1_DM [13]
- USB_HOST1_DP >>> USB_HOST1_DP [13]
- USB_OTG0_DP >>> USB_OTG0_DP [13]
- USB_OTG0_DM >>> USB_OTG0_DM [13]

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A3	TYPE_C_POWER/OTG	V1.0
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PMIC RK809 Managerment

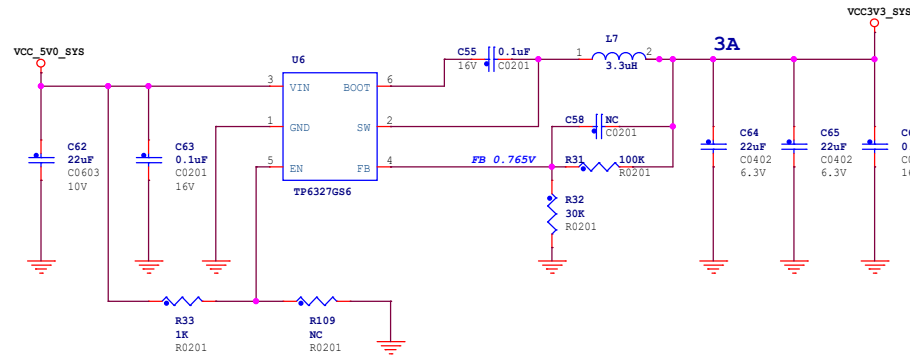


PMIC RK809 CODEC

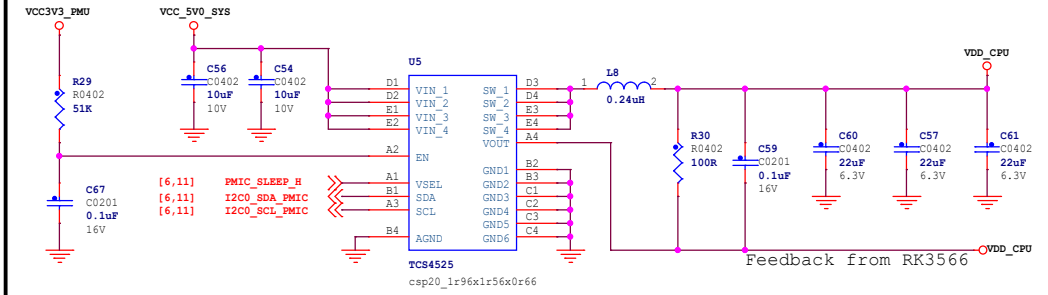


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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
Size	Document Number	Rev
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Date:	Friday, July 14, 2023	Sheet 6 of 22

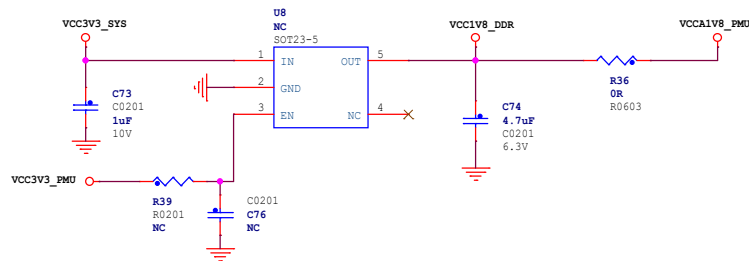
SYS_Power



RK3566 CPU Power VDD_CPU_EXT

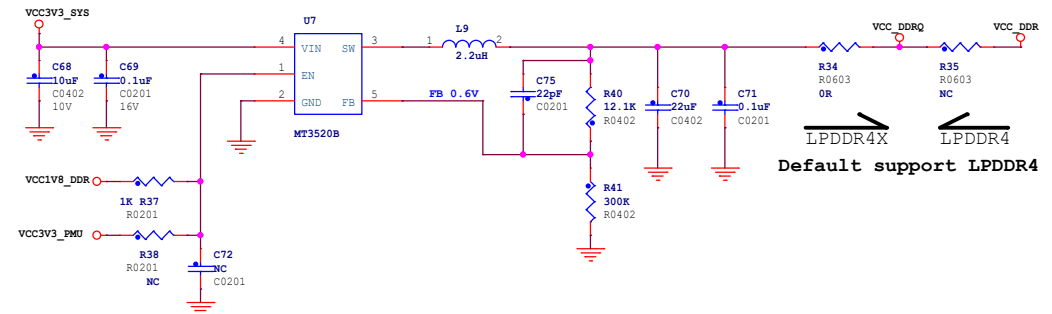


Auxiliary power supply LPDDR4_VCC1V8_DDR



LPDDR4X Power

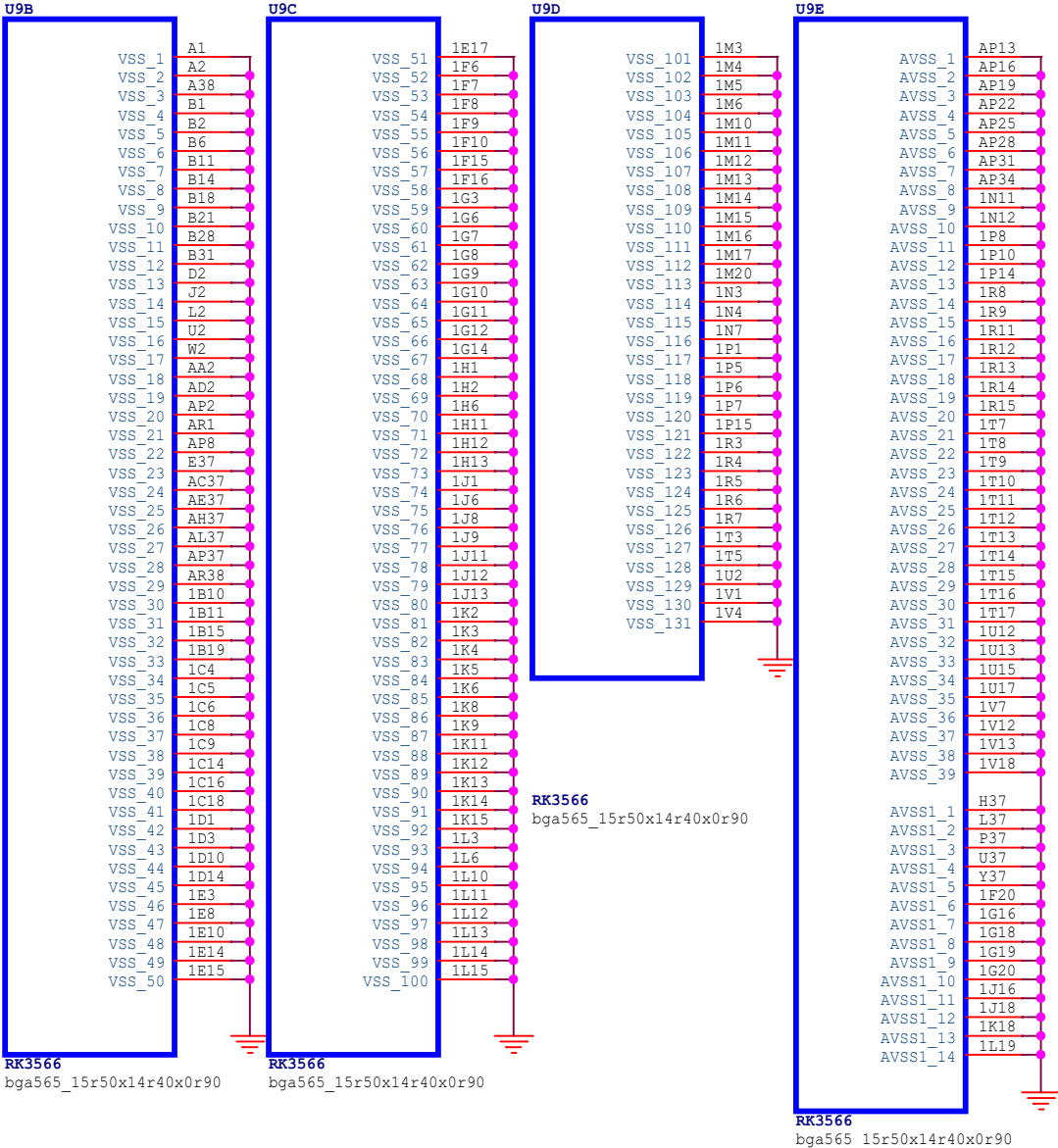
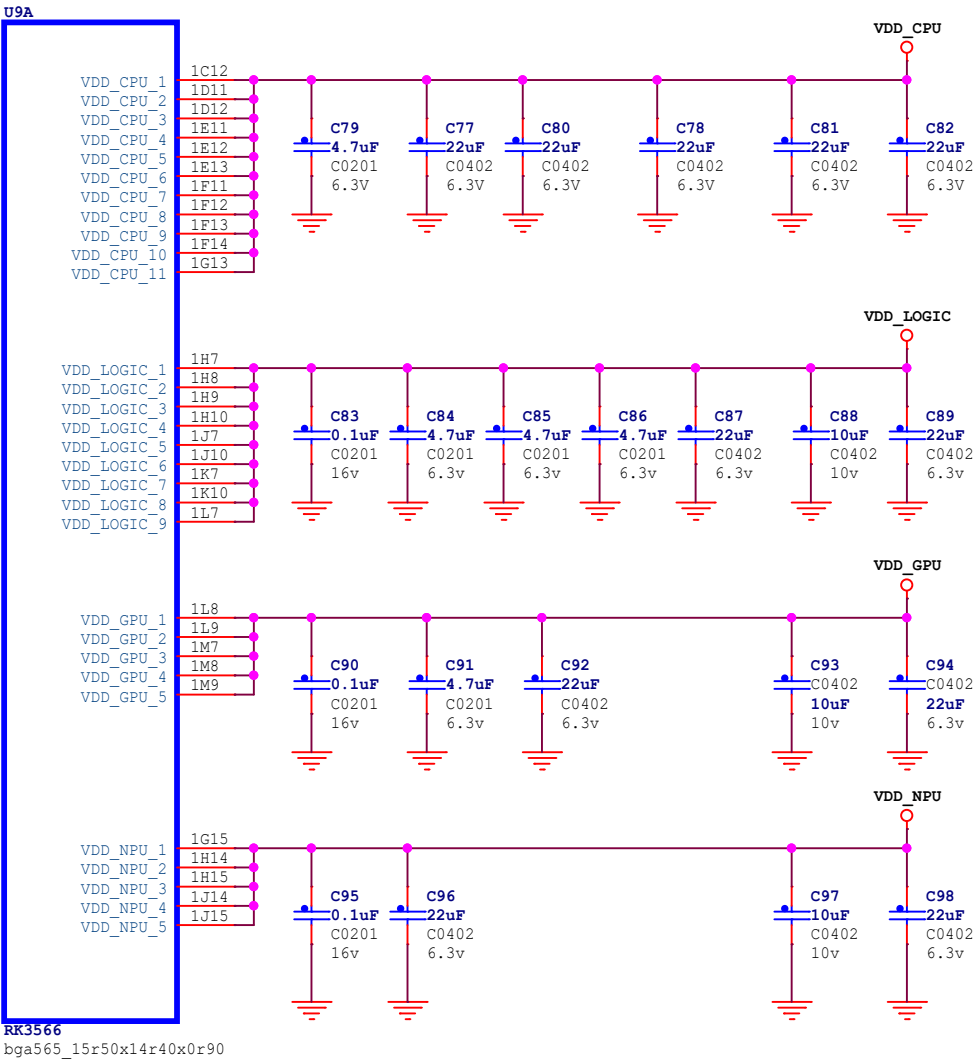
output: 0.6V



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Title 野火_RK3566_LUBANCAT_2ZERO_WIFI_原理图		
Size A3	Document Number Power_other	Rev V1.0
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RK3566_ABCDE

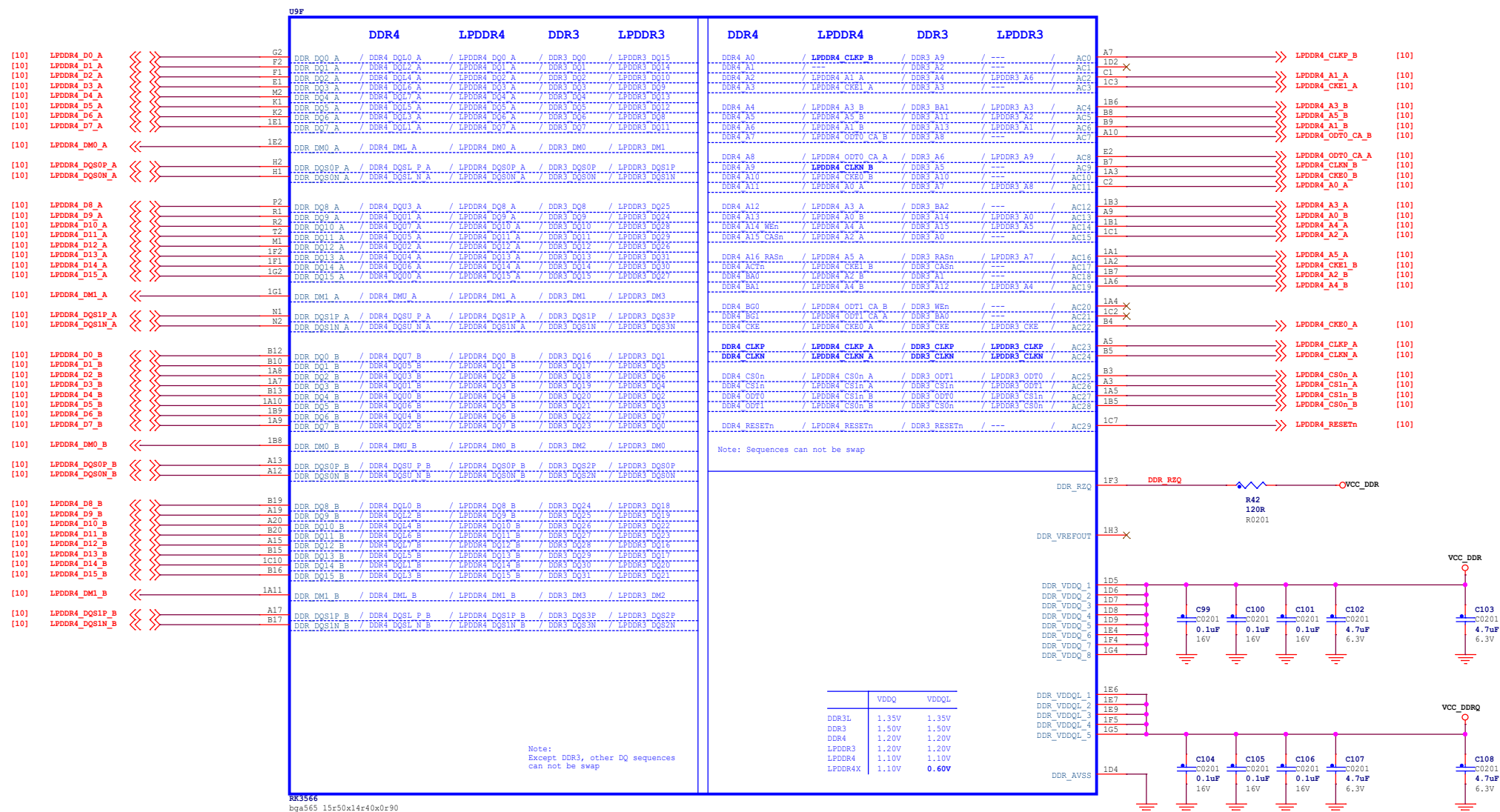
(Power&GND)



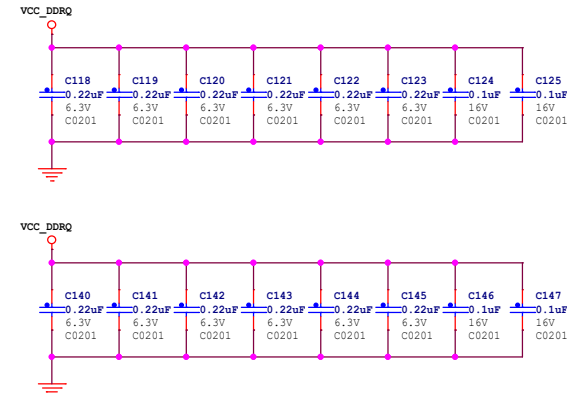
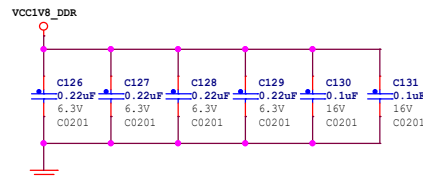
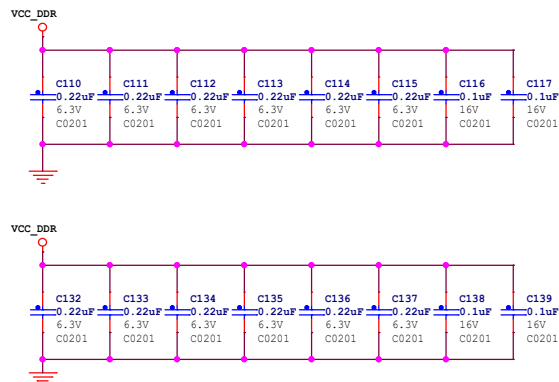
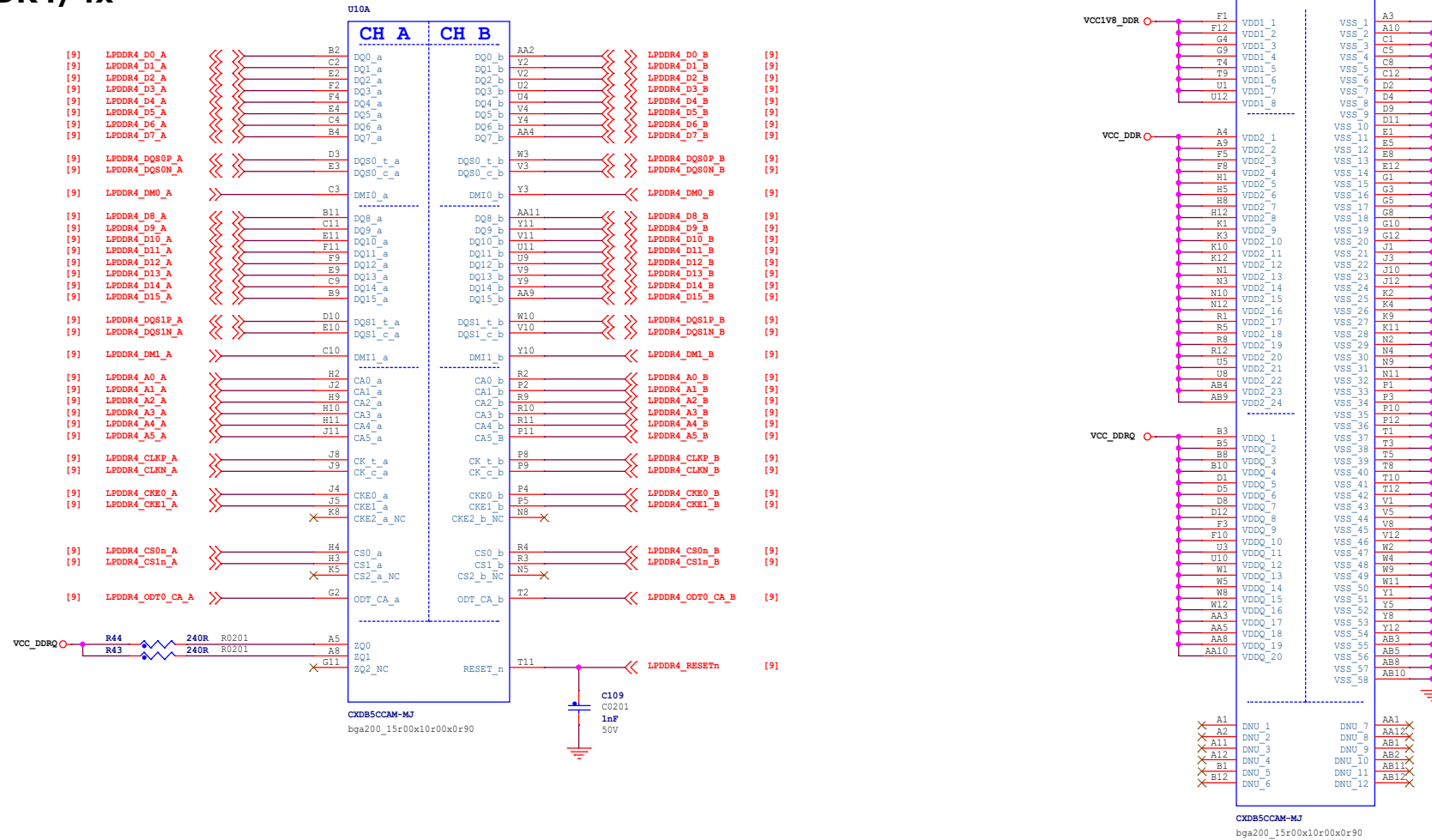
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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A4	Document Number RK3566_Power/GND	Rev V1.0
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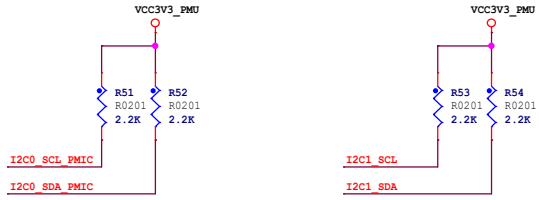
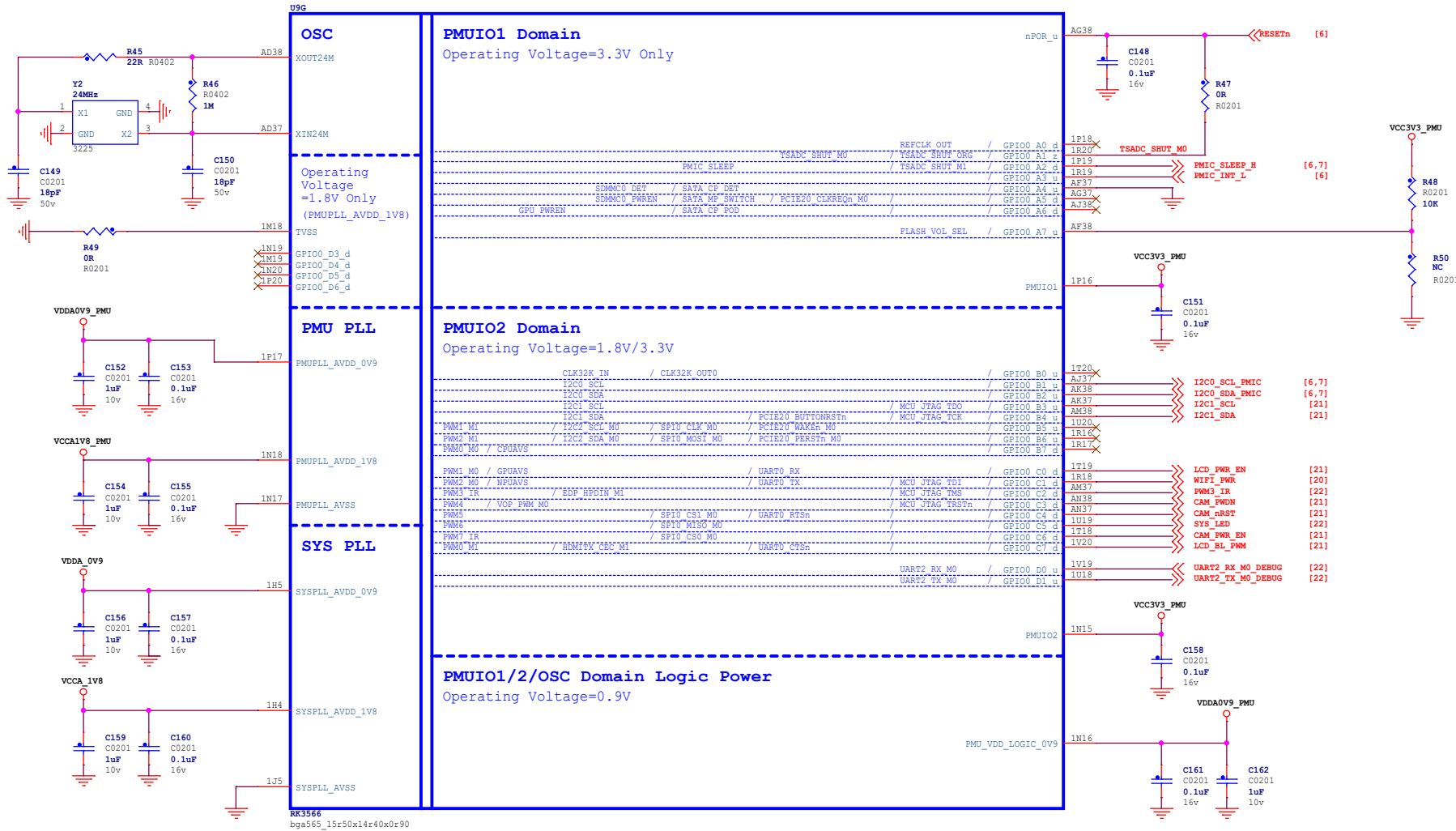
RK3566 F (DDR PHY)



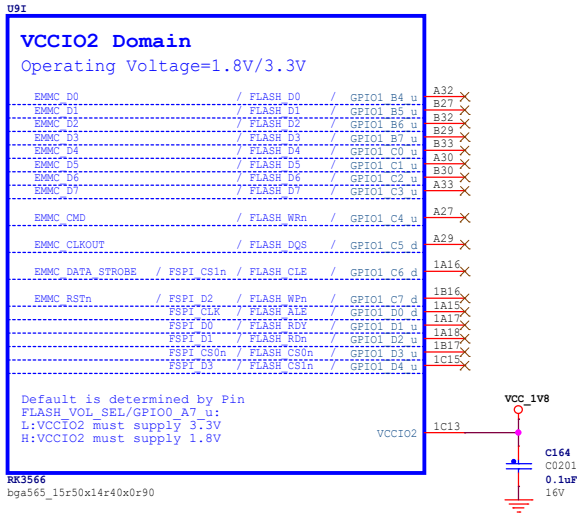
LPDDR4/4x



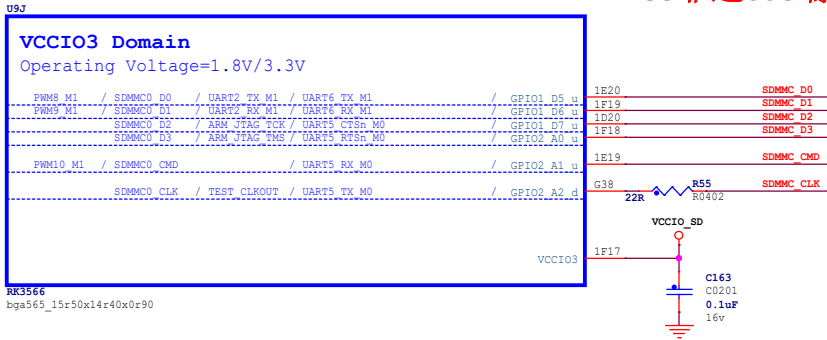
RK3566_G (OSC/PLL/PMUIO1/2)



RK3566_I (VCCIO2 Domain)
EMMC_IO

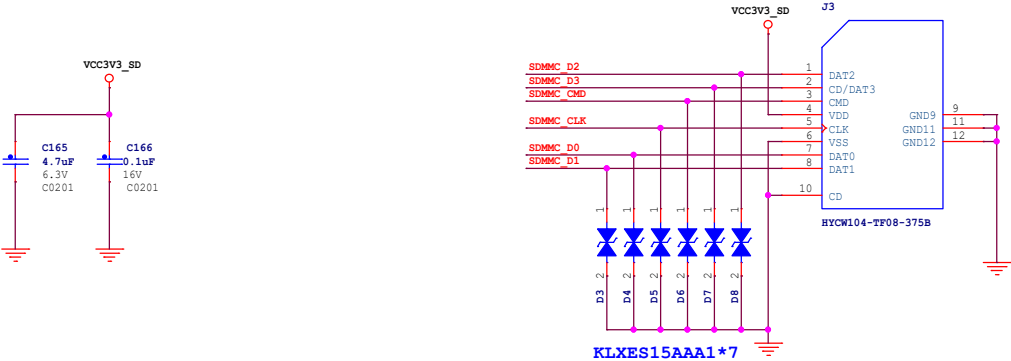


RK3566_J (VCCIO3 Domain)
SDMMC_IO



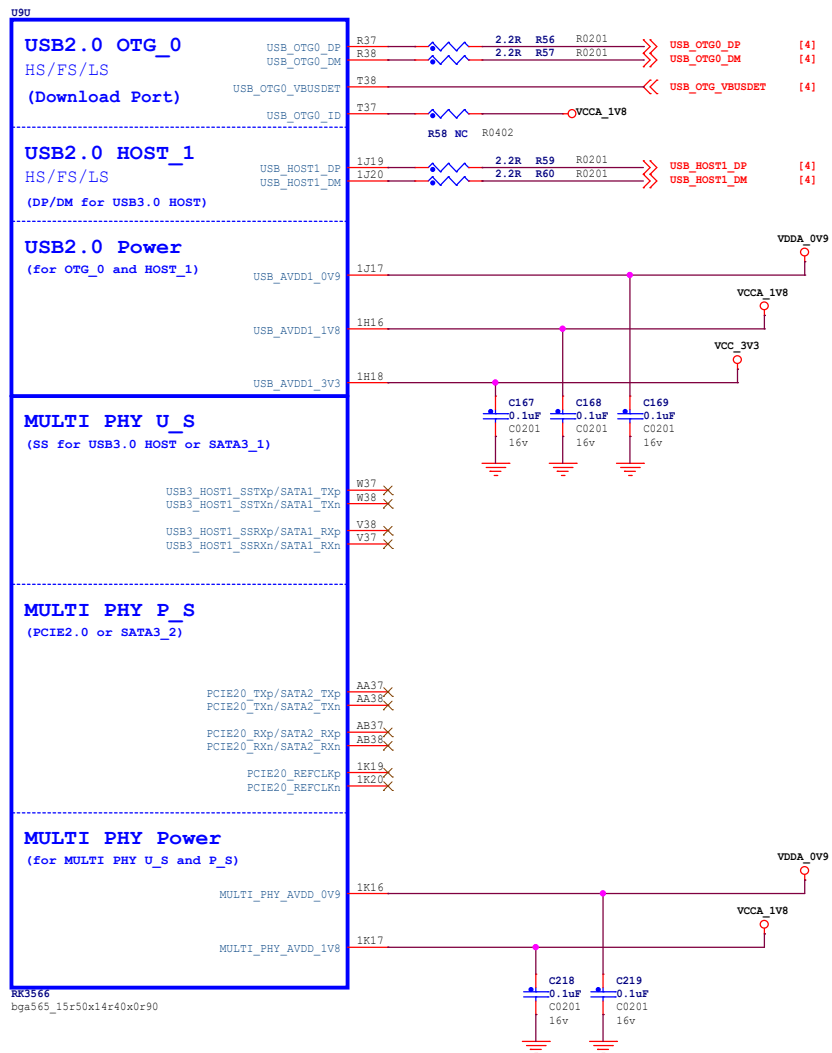
R55靠近SOC端放置

TF_CARD

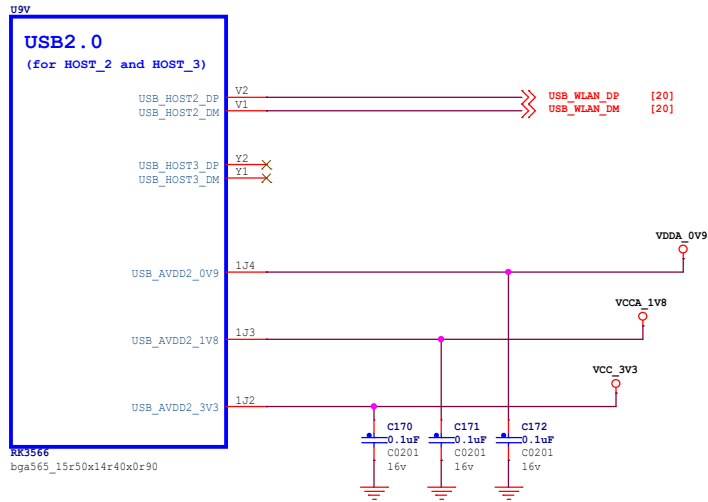


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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
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A3	RK3566_EMMC/SD Controller	V1.0
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RK3566_U (USB3.0/SATA/PCIE2.0 x1)

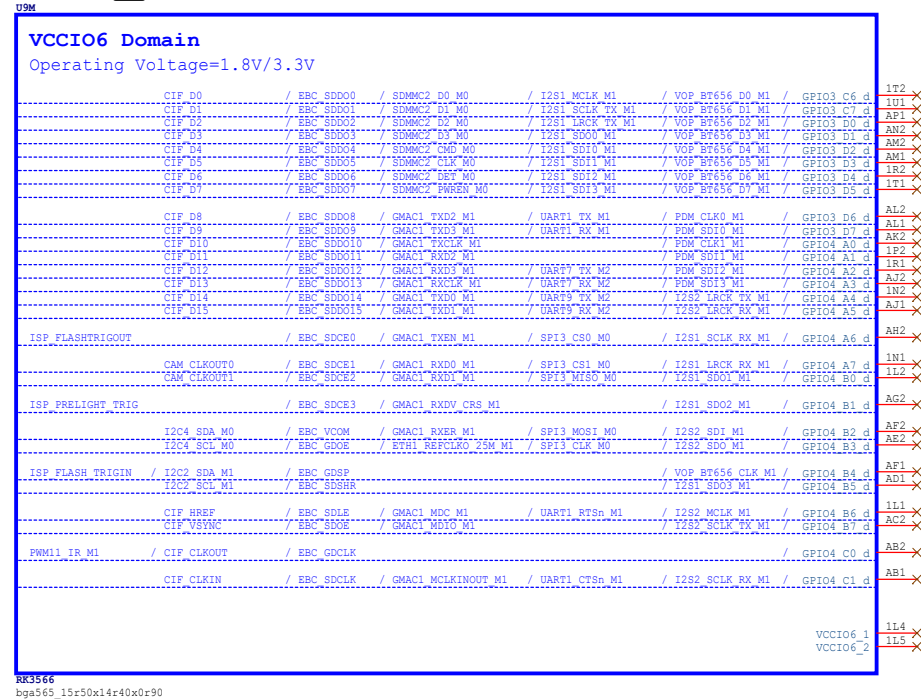


RK3566_V (USB2.0 HOST)

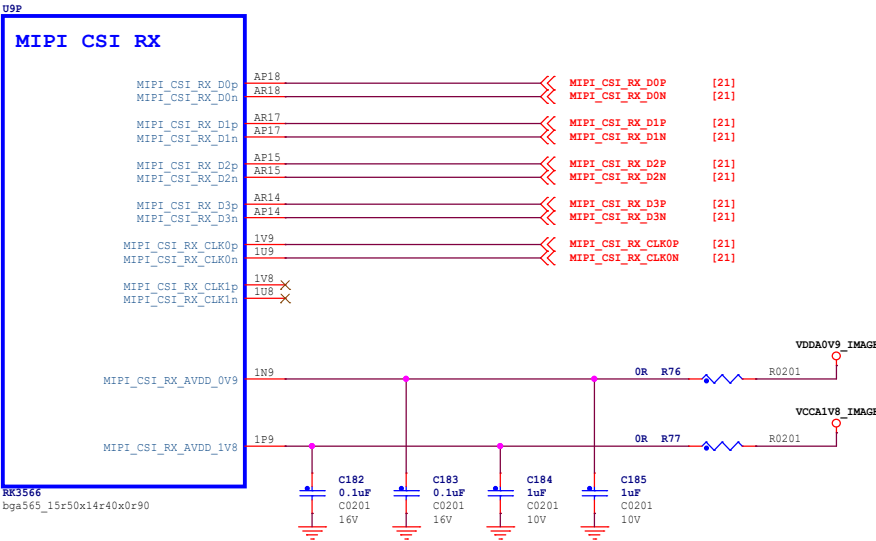


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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
Size	Document Number	Rev
A3	RK3566_USB/PCIE/SATA_PHY	V1.0
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RK3566_M(VCCIO6 Domain)
GMAC_Controller



RK3566_P(MIPI_CSI_RX)



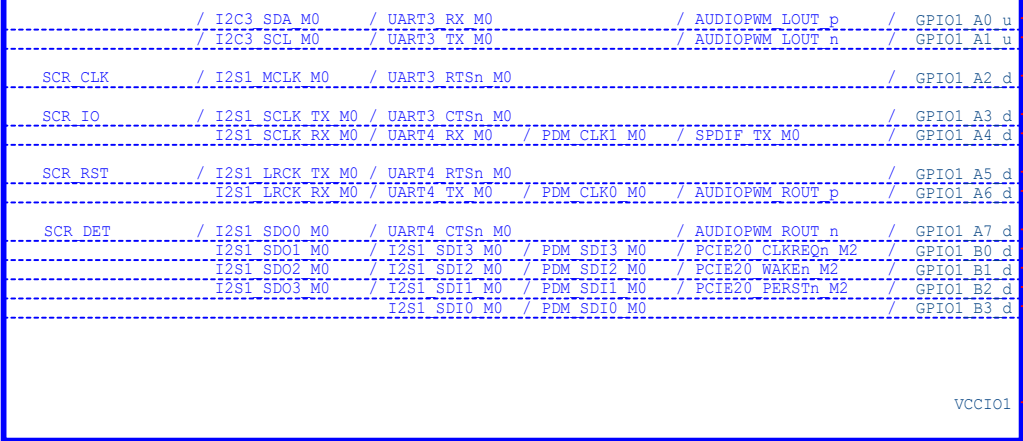
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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
Size	Document Number	Rev
A3	MIPI_CSI/GMAC_Controller	V1.0
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RK3566_H (VCCIO1 Domain)

U9H

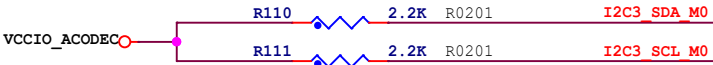
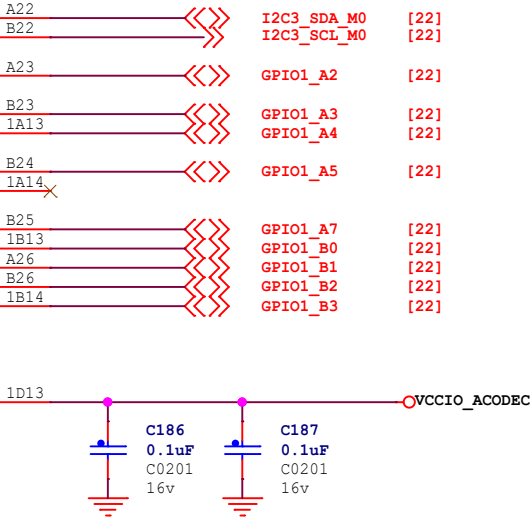
VCCIO1 Domain

Operating Voltage=1.8V/3.3V



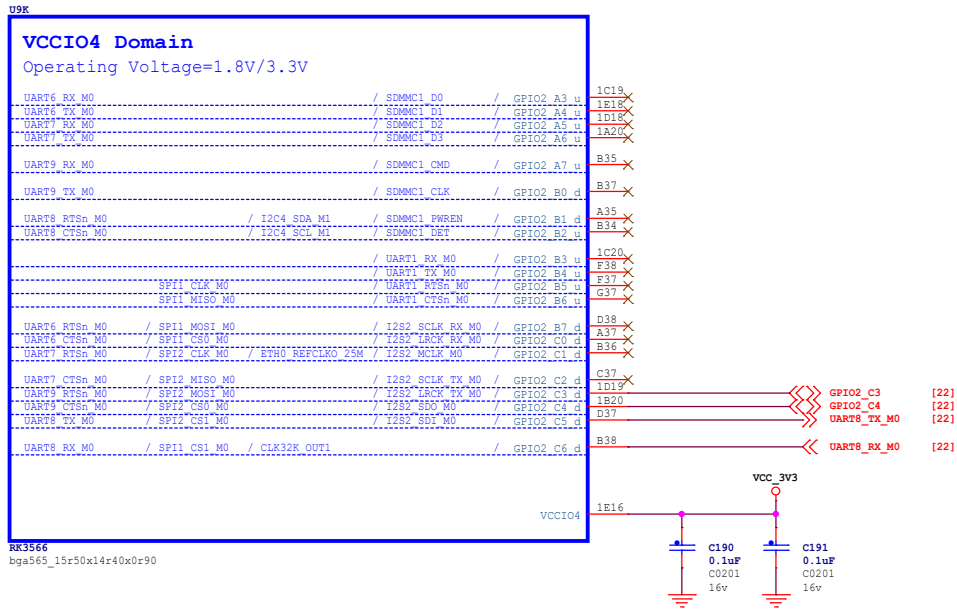
RK3566
bga565_15r50x14r40x0r90

VCCIO1

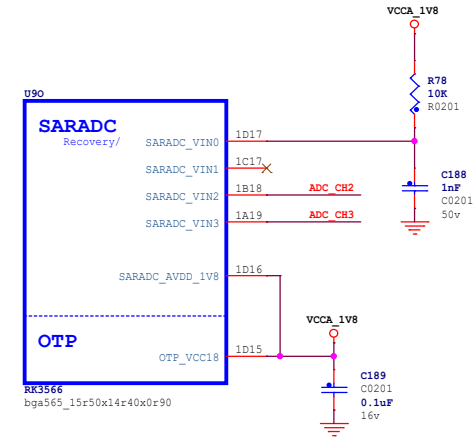


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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A4	Document Number RK3566_Audio Interface	Rev V1.0
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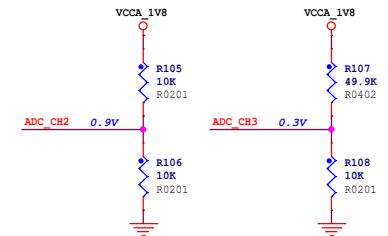
RK3566_K (VCCIO4 Domain)



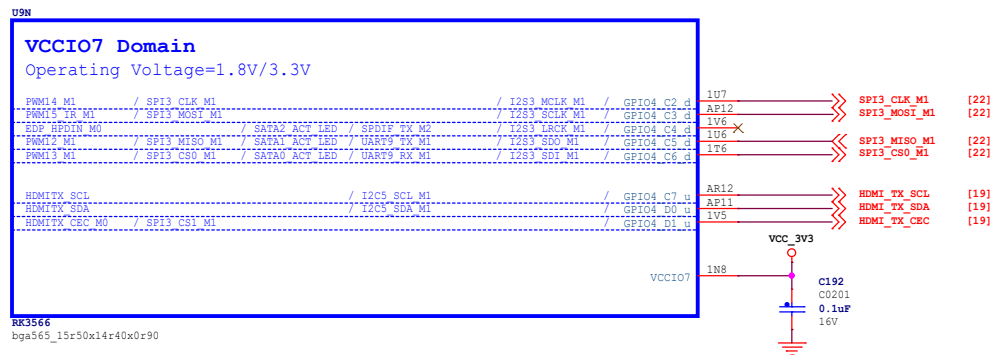
RK3566_O (SARADC/OTP)



BOARD_ID



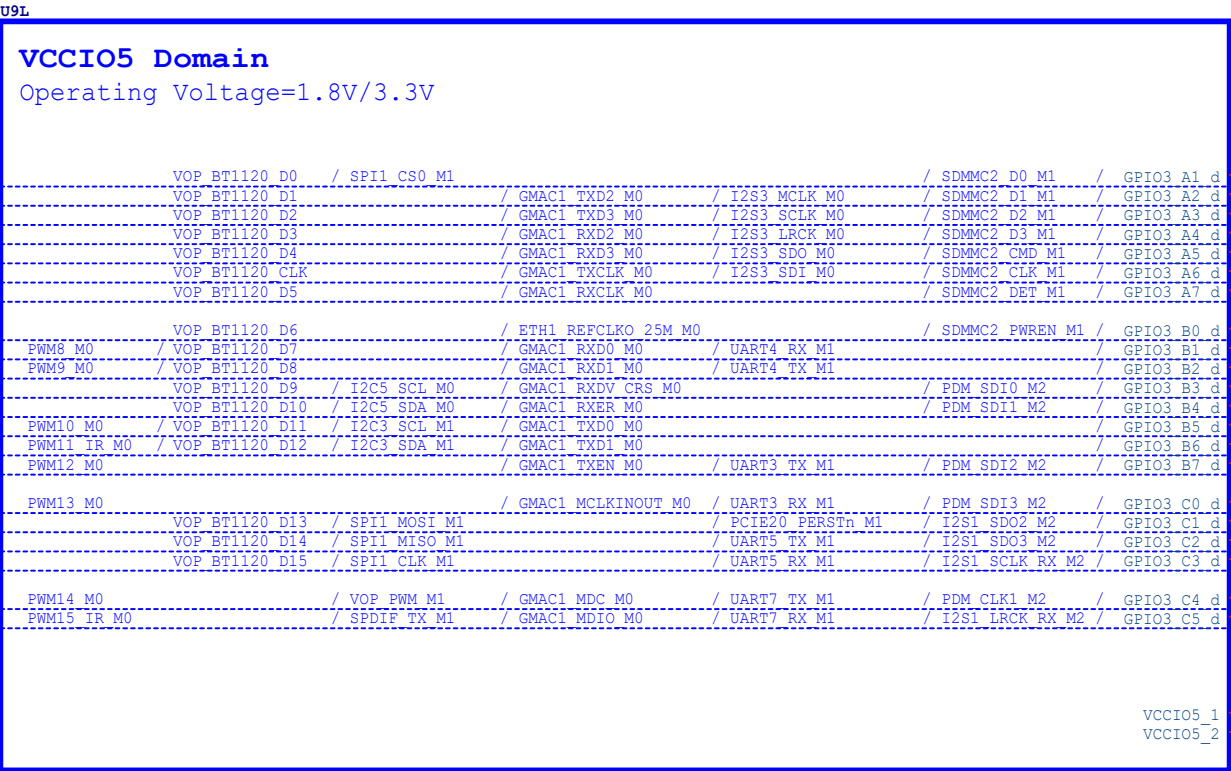
RK3566_N (VCCIO7 Domain)



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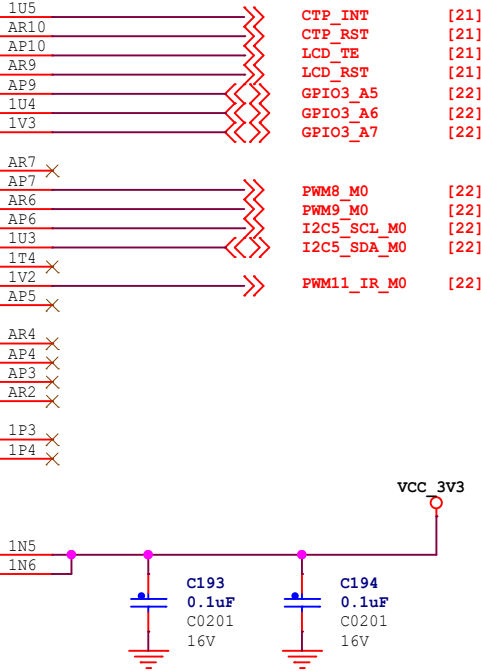
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Size A3	Document Number RK3566_SARADC/GPIO	Rev V1.0
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RK3566_L (VCCIO5 Domain)

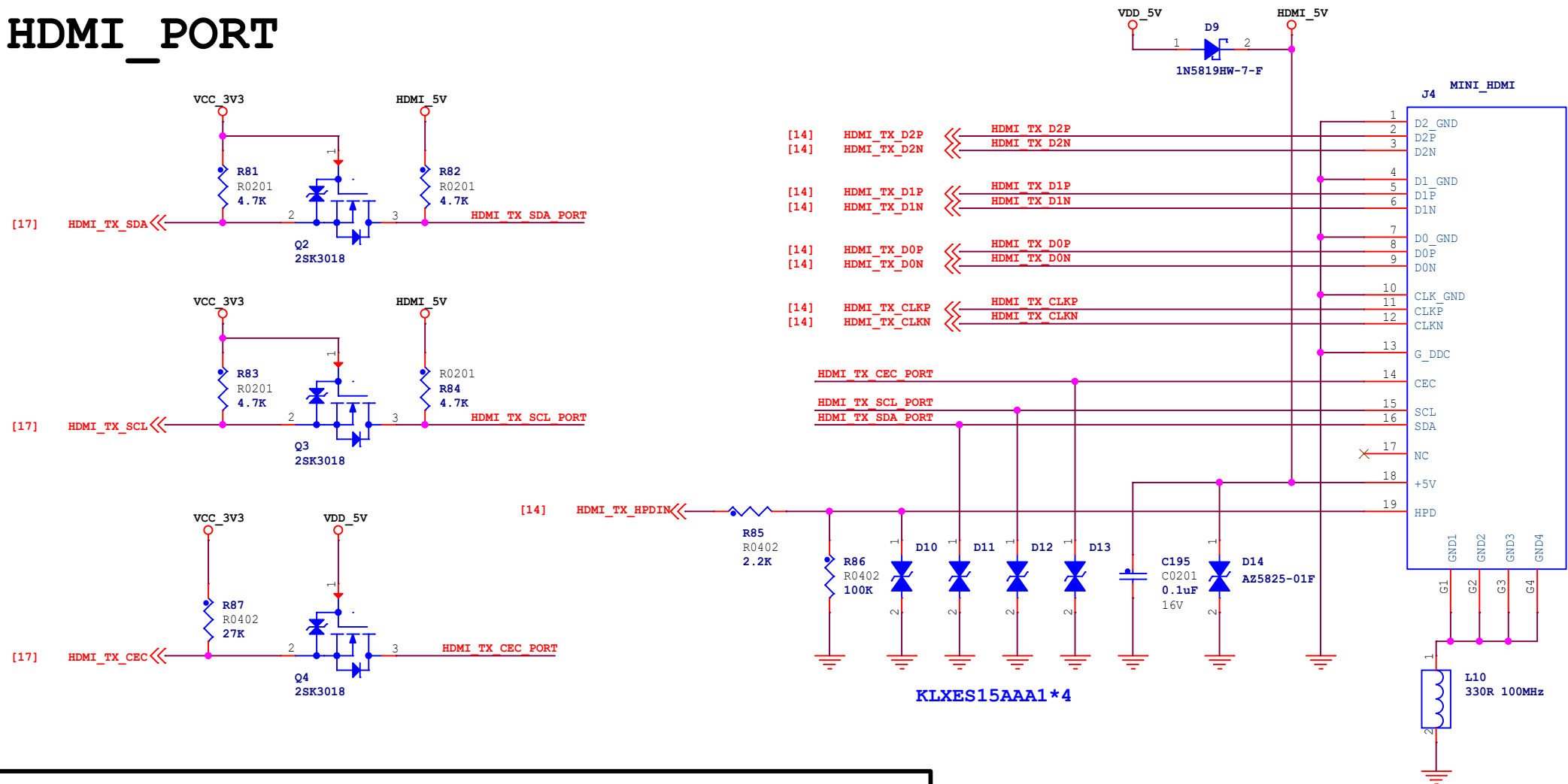


RK3566

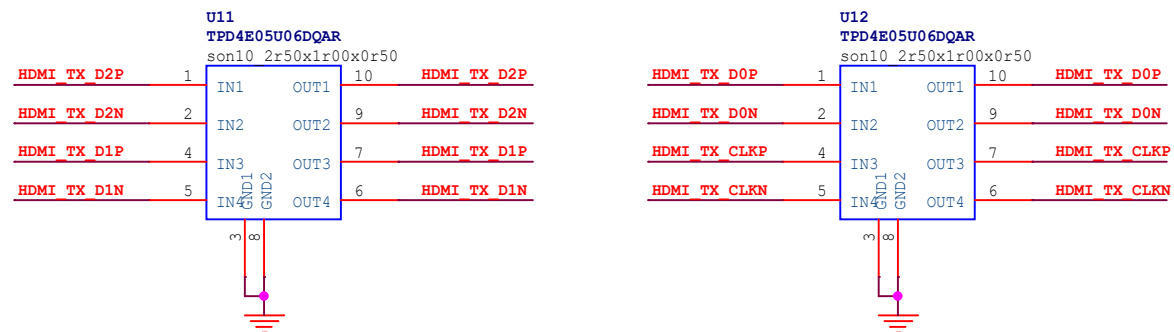
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HDMI_PORT



ESD



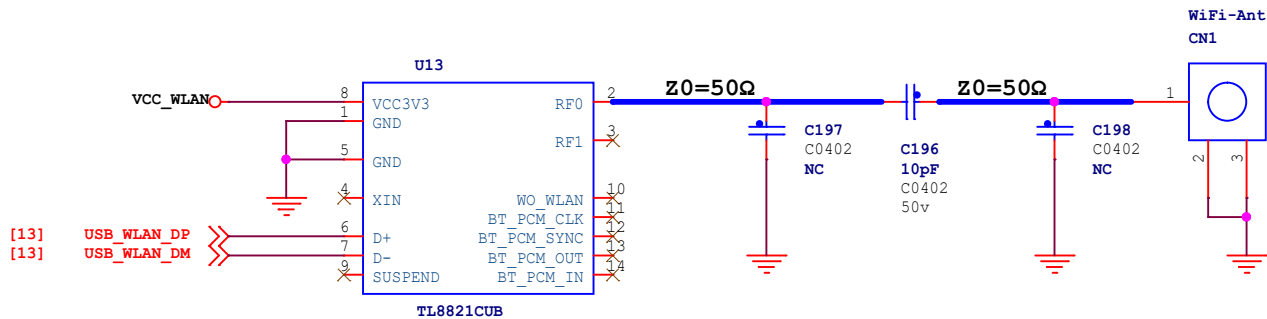
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Title	野火 RK3566 LUBANCAT ZERO WIFI 原理图
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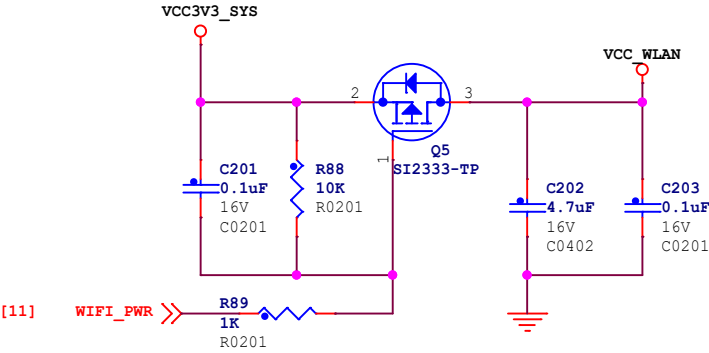
Size	Document Number	Rev
A4	HDMI PORT	V1.0

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USB_WLAN

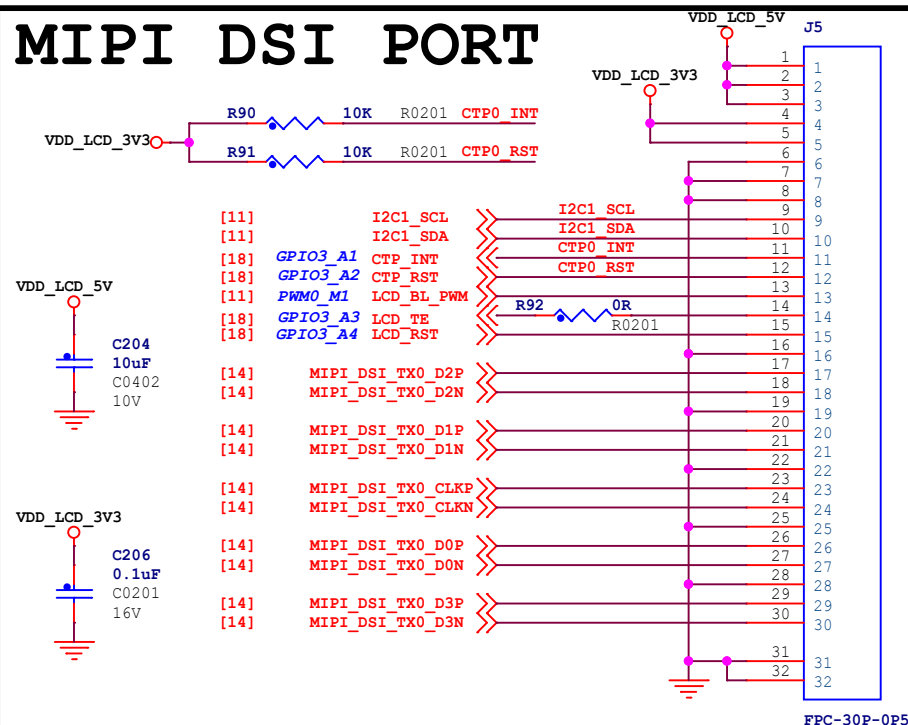


WLAN_Power

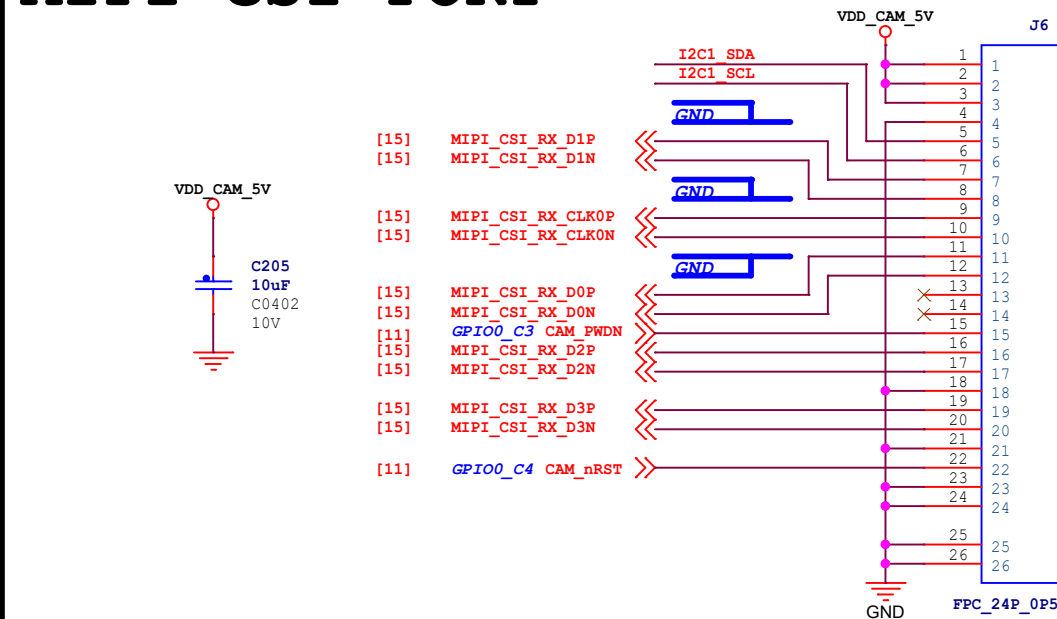


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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
Size A4	Document Number USB_WLAN	Rev V1.0
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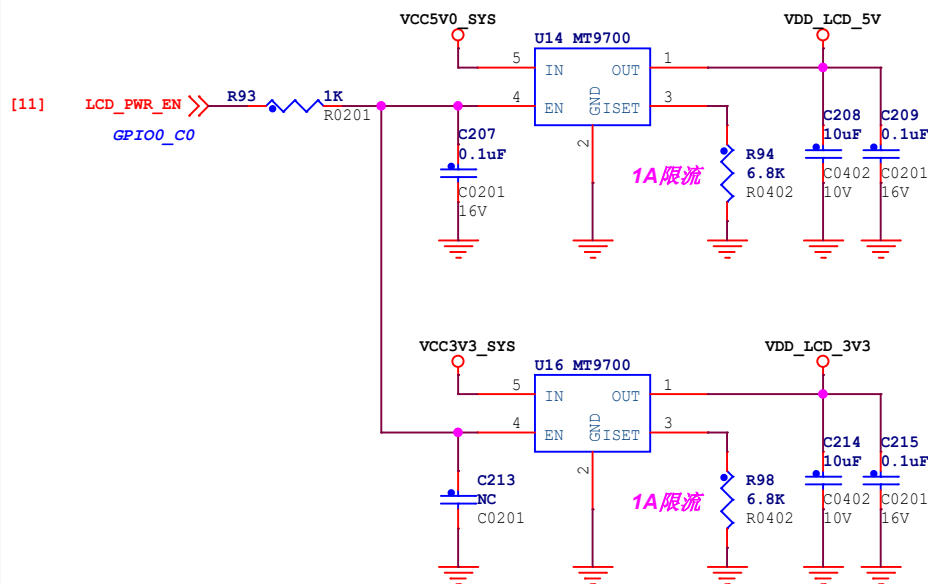
MIPI DSI PORT



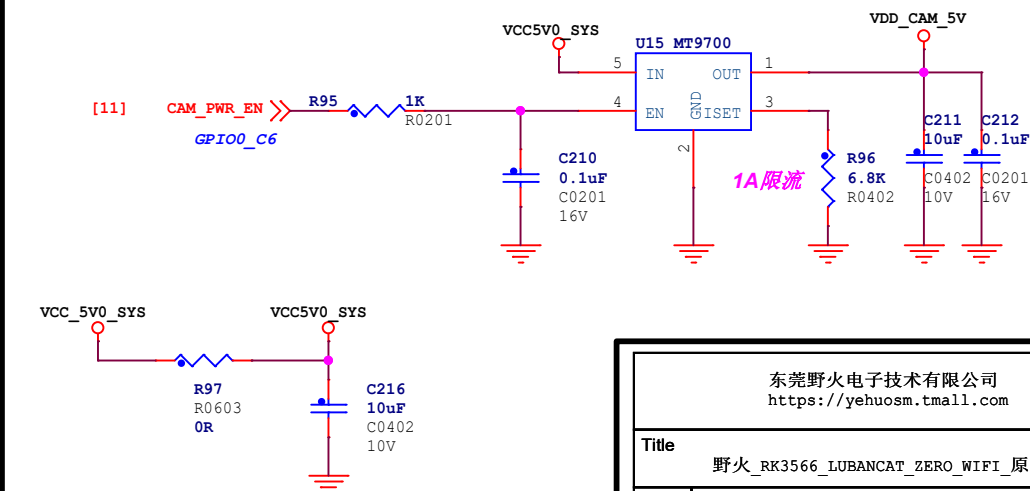
MIPI CSI PORT



LCD Power



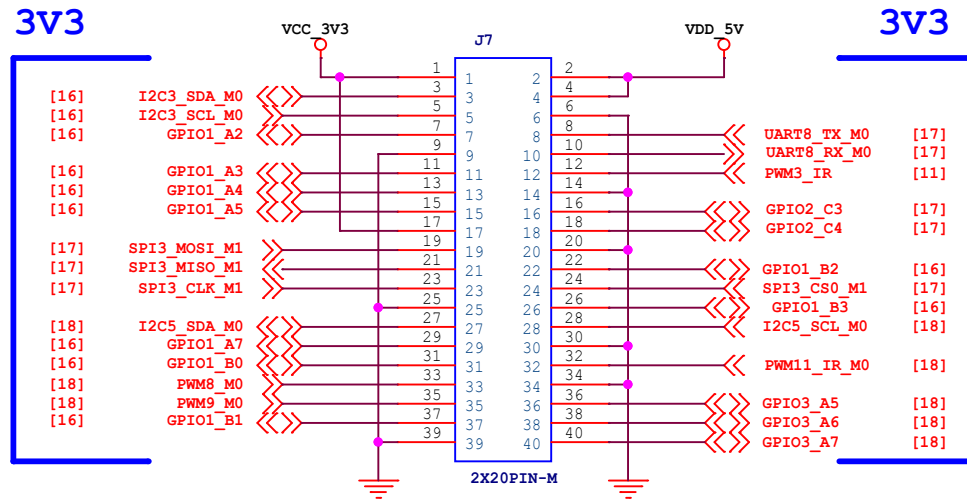
MIPI CSI Power



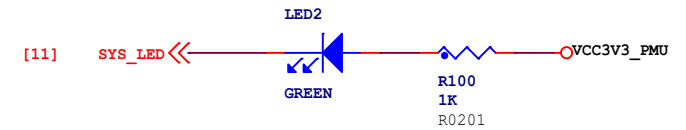
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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A4	Document Number MIPI DSI/MIPI CSI_PORT	Rev V1.0
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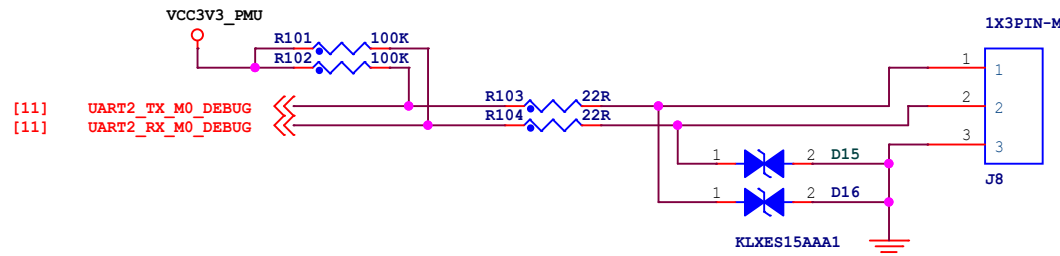
Pin Out



SYS_LED



Debug UART2



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