

野火_LubanCat_RV06_CS 核心板

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历史版本

版本号	日期	设计	描述
V0R1	2024-06-06	ghs	初始版本
V1R0	2024-07-19	ghs	正式版本

D

C

B

A

D

C

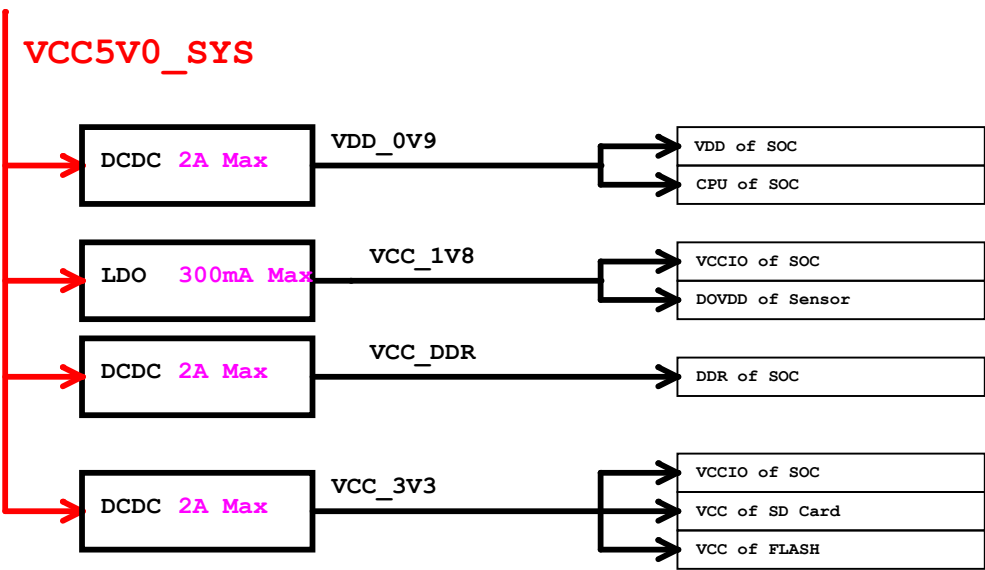
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A

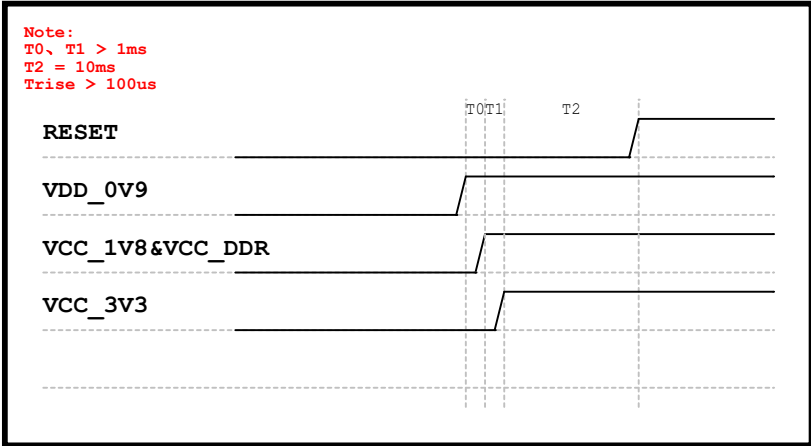
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POWER DIAGRAM

BOT POWER IN
5V/2A

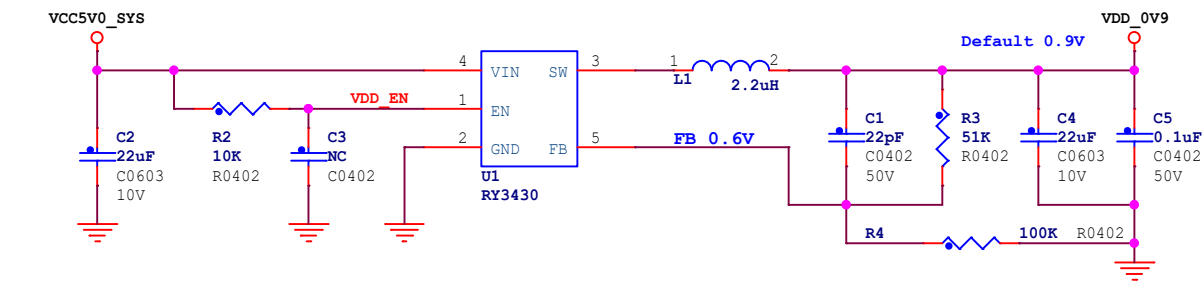


Power-on Sequence				
Power Name	PMIC Channel	Time Slot	Default voltage	Peak Current
VDD_0V9	DC-DC BUCK	Slot: 1	0.9V	2.0A
VCC_1V8	LDO	Slot: 2	1.8V	0.1A
VCC_DDR	DC-DC BUCK	Slot: 2	1.35V	0.5A
VCC_3V3	DC-DC BUCK	Slot: 3	3.3V	1.0A
RESET	Finally , nPOR RESET 10ms after VCC 3V3 is ready			



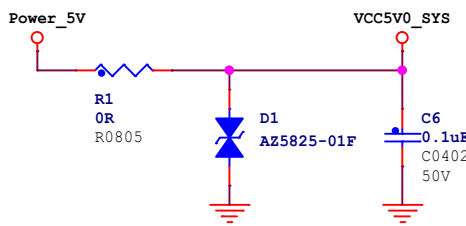
VCC5V0_SYS-->VDD_0V9

Step 1



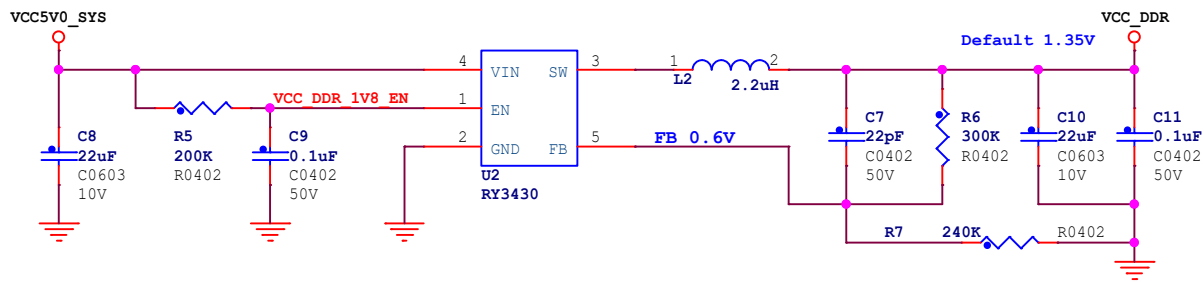
POWER_IN

5V2A MAX



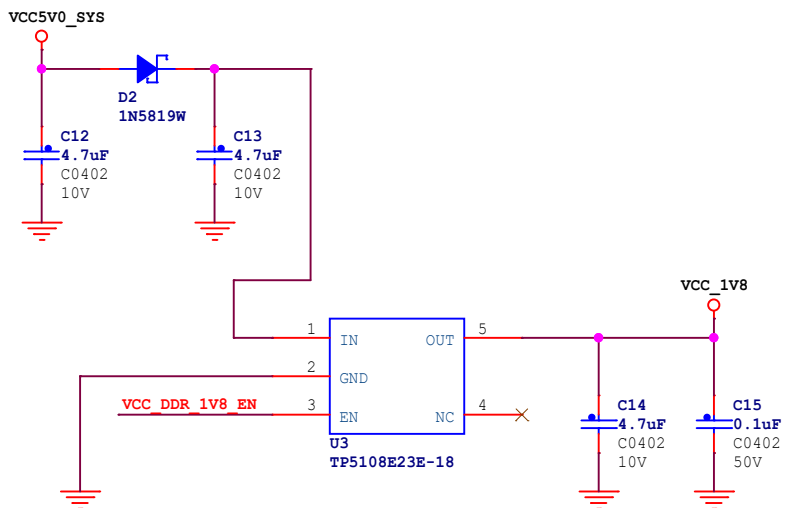
VCC5V0_SYS-->VCC_DDR

Step 2



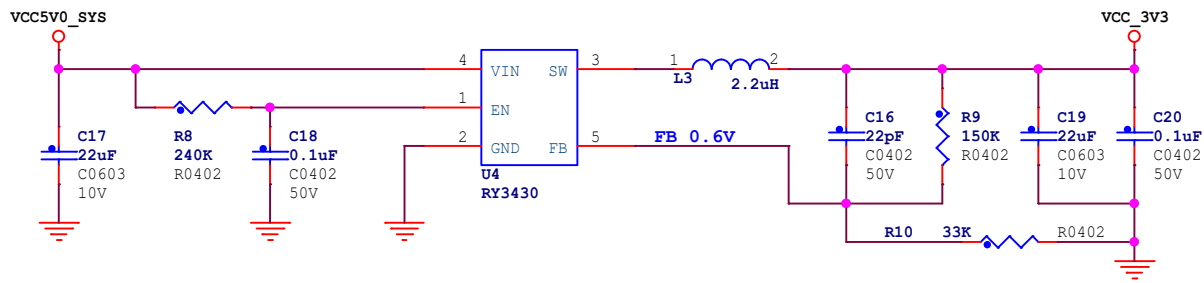
VCC5V0_SYS-->VCC_1V8

Step 2



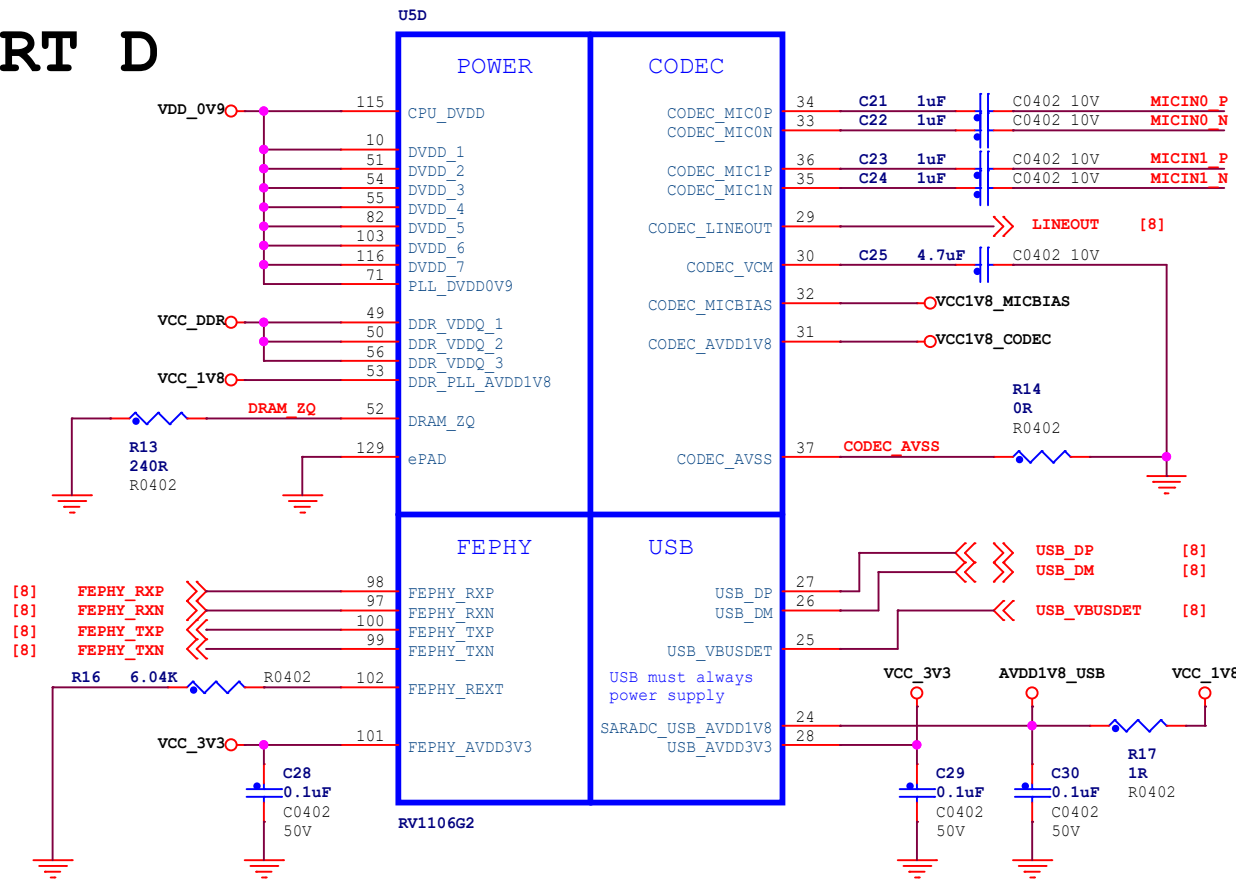
VCC5V0_SYS-->VCC_3V3

Step 3

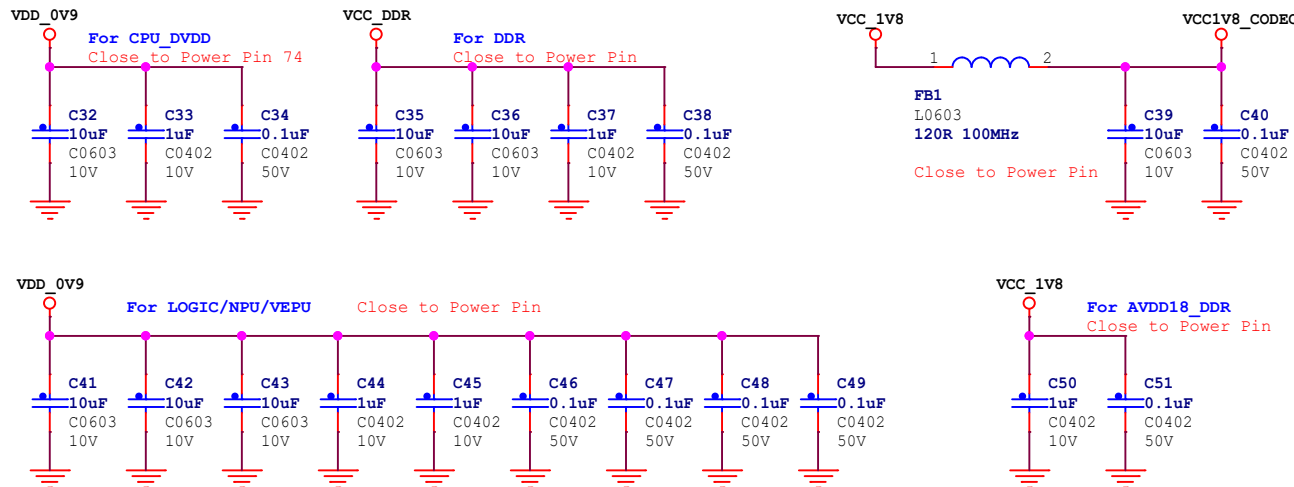
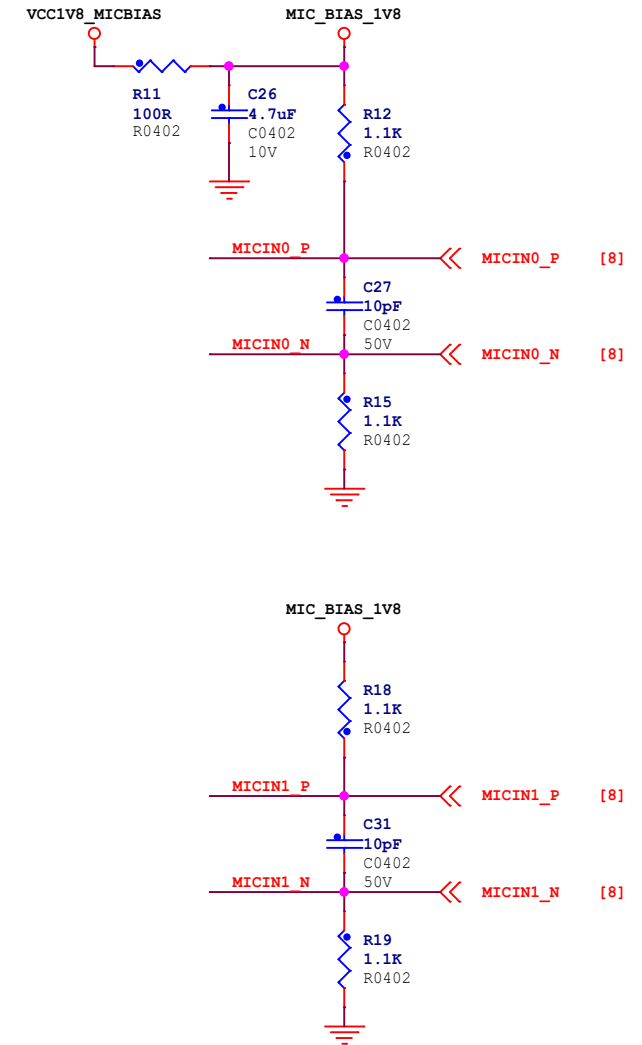


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PART D



MIC IN



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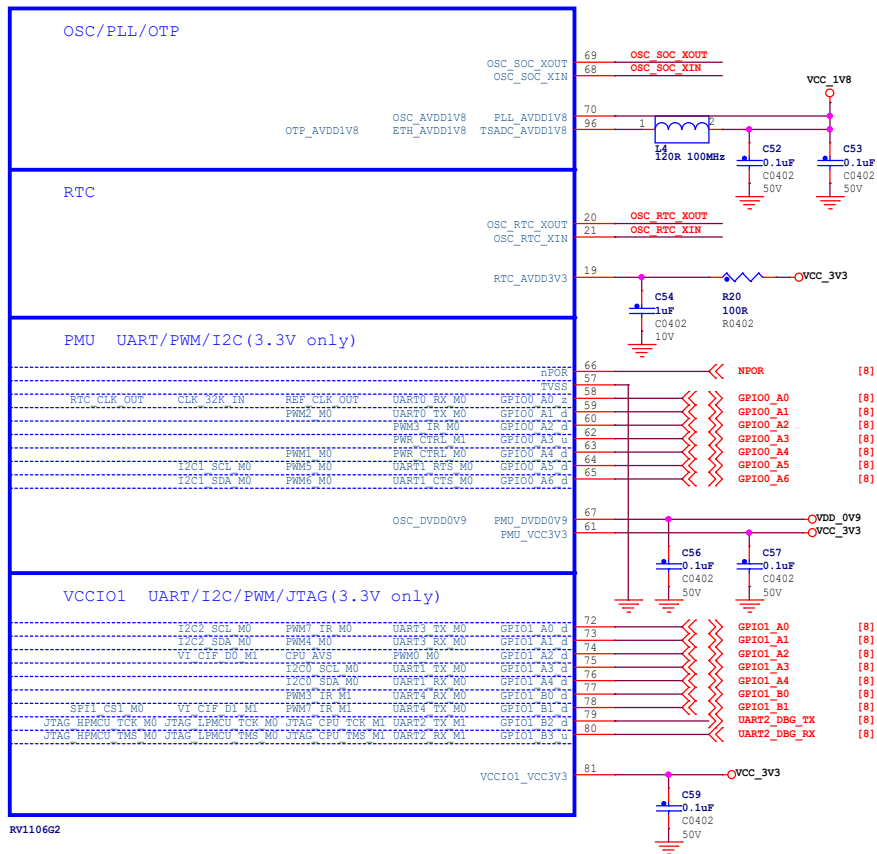
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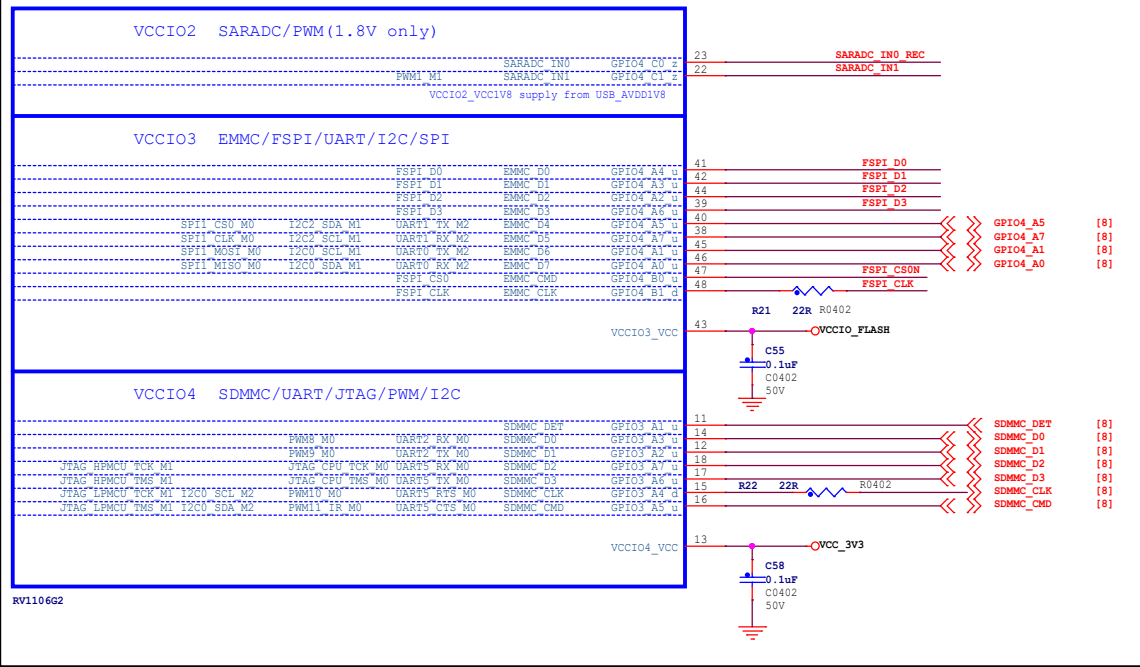
PART A

USA



PART B

USB



PART B

U5C

VCCIO5 SDIO/I2S/VO_LCDC/UART/PWM/I2C

	I2C4_SCL M0	UART1_RTS M1	VO_LCDC_D9	I2S0_LRCK	SDIO_D0 M0	GPIO2_A1 d
	I2C4_SDA M0	UART1_CTS M1	VO_LCDC_D8	I2S0_SCLK	SDIO_D1 M0	GPIO2_A0 d
		UART1_RX M1	VO_LCDC_D13	I2S0_SDI0	SDIO_D2 M0	GPIO2_A5 d
		UART1_TX M1	VO_LCDC_D12	I2S0_SD00	SDIO_D3 M0	GPIO2_A4 d
			VO_LCDC_D10	I2S0_MCLK	SDIO_CLK M0	GPIO2_A2 d
			VO_LCDC_D11	I2S0_SD03 SDI1	SDIO_CMD M0	GPIO2_A3 d
FLASH TRIGOUT	I2C3_SCL M0	PWM2 M1	VO_LCDC_D14	I2S0_SD02 SDI2	UART0_RTS M1	GPIO2_A6 d
PRELIGHT TRIGOUT	I2C3_SDA M0	PWM4 M1	VO_LCDC_D15	I2S0_SD01 SDI3	UART0_CTS M1	GPIO2_A7 d
		PWM5 M1	VO_LCDC_D16	I2C1_SCL M1	UART0_RX M1	GPIO2_B0 d
		PWM6 M1	VO_LCDC_D17	I2C1_SDA M1	UART0_TX M1	GPIO2_B1 d

VCCIO5_VCC

VCCIO6 VO_LCDC/VI_CIF/PWM/UART/I2C/SDIO/SPI/AUD_DSM/VI_BT656

VI_BT656_D0 M1		SDIO_D1 M1		SPI0_CS0 M0		PWM2 M2		VI_CIF_D2 M1		VO_LCDC_D7		GPIO1_C0 d		
VI_BT656_D1 M1				SDIO_D0 M1		SPI0_CLK M0		PWM4 M2		VI_CIF_D3 M1		VO_LCDC_D6		GPIO1_C1 d
VI_BT656_D2 M1		I2C4_SCL M1				SPI0_CLK M1		PWM5 M2		VI_CIF_D4 M1		VO_LCDC_D5		GPIO1_C2 d
VI_BT656_D3 M1		I2C4_SDA M1				SPI0_CMD M1		PWM6 M2		VI_CIF_D5 M1		VO_LCDC_D4		GPIO1_C3 d
VI_BT656_D4 M1				SDIO_D3 M1		UART4_RX M1		PWM8 M1		VI_CIF_D6 M1		VO_LCDC_D3		GPIO1_C4 d
VI_BT656_D5 M1				SDIO_D2 M1		UART4_TX M1		PWM9 M1		VI_CIF_D7 M1		VO_LCDC_D2		GPIO1_C5 d
VI_BT656_D6 M1						UART4_RTS M1		PWM10 M1		VI_CIF_D8 M1		VO_LCDC_D1		GPIO1_C6 d
VI_BT656_D7 M1						UART4_CTS M1		PWM11_IR M1		VI_CIF_D9 M1		VO_LCDC_D0		GPIO1_C7 d
VI_BT656_CLK M1						UART3_TX M1				VI_CIF_CLKI M1		VO_LCDC_DEN		GPIO1_D0 d
						UART3_RX M1				VI_CIF_HREF M1		VO_LCDC_HSYNC		GPIO1_D1 d
						AUD_DSM_P				VI_CIF_VSYNC M1		VO_LCDC_VSYNC		GPIO1_D2 d
SPI0_CS1 M0		I2C3_SDA M1				AUD_DSM_N				VI_CIF_CLKO M1		VO_LCDC_CLK		GPIO1_D3 d
		I2C3_SCL M1								PWM11_IR M2				
						UART5_RX M1								
						UART5_TX M1								

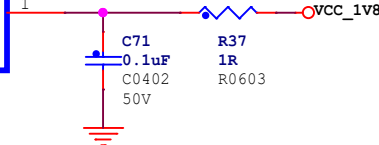
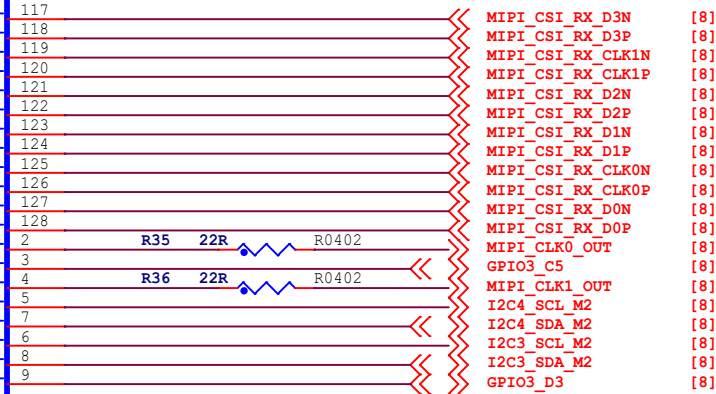
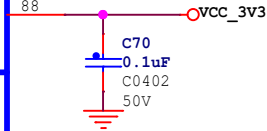
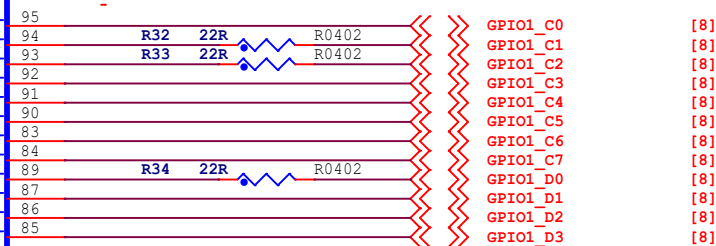
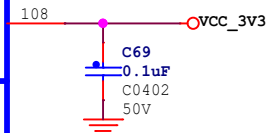
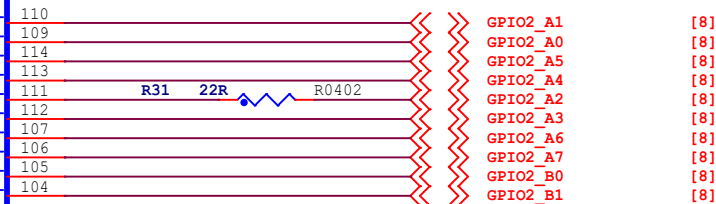
VCCIO6_VCC

VCCIO7 VI_CIF/MIPI CSI/LVDS_RX/UART/I2C/VI_BT1120/VI_BT656
(1.8V only,some for input only)

VI_BT1120_D0	LVDS_RX_D3N	MIPI_CSI_RX_D3N	VI_CIF_D0 M0	GPIO3_B0 d	
VI_BT1120_D1	LVDS_RX_D3P	MIPI_CSI_RX_D3P	VI_CIF_D1 M0	GPIO3_B1 d	
VI_BT1120_D2	LVDS_RX_CLKIN	MIPI_CSI_RX_CLKIN	VI_CIF_D2 M0	GPIO3_B2 d	
VI_BT1120_D3	LVDS_RX_CLKIP	MIPI_CSI_RX_CLKIP	VI_CIF_D3 M0	GPIO3_B3 d	
VI_BT1120_D4	LVDS_RX_D2N	MIPI_CSI_RX_D2N	VI_CIF_D4 M0	GPIO3_B4 d	
VI_BT1120_D5	LVDS_RX_D2P	MIPI_CSI_RX_D2P	VI_CIF_D5 M0	GPIO3_B5 d	
VI_BT1120_D6	LVDS_RX_D1N	MIPI_CSI_RX_D1N	VI_CIF_D6 M0	GPIO3_B6 d	
VI_BT1120_D7	LVDS_RX_D1P	MIPI_CSI_RX_D1P	VI_CIF_D7 M0	GPIO3_B7 d	
VI_BT656_D0 M0	VI_BT1120_D8	LVDS_RX_CLKON	MIPI_CSI_RX_CLKON	VI_CIF_D8 M0	GPIO3_C0 d
VI_BT656_D1 M0	VI_BT1120_D9	LVDS_RX_CLKOP	MIPI_CSI_RX_CLKOP	VI_CIF_D9 M0	GPIO3_C1 d
VI_BT656_CLK M0	VI_BT1120_CLK	LVDS_RX_D0N	MIPI_CSI_RX_D0N	VI_CIF_CLKI M0	GPIO3_C2 d
		LVDS_RX_D0P	MIPI_CSI_RX_D0P	VI_CIF_HREF M0	GPIO3_C3 d
		MIPI_CLKR0_OUT		VI_CIF_CLKR0 M0	GPIO3_C4 d
				VI_CIF_VSYNC M0	GPIO3_C5 d
VI_BT656_D2 M0	VI_BT1120_D10	PWM7_IR M2	MIPI_CLKI_OUT	VI_CIF_D10	GPIO3_C6 d
VI_BT656_D3 M0	VI_BT1120_D11	UART5_TX M2	I2C4_SCL M2	VI_CIF_D11	GPIO3_C7 d
VI_BT656_D4 M0	VI_BT1120_D12	UART5_RX M2	I2C4_SDA M2	VI_CIF_D12	GPIO3_D0 d
VI_BT656_D5 M0	VI_BT1120_D13	UART5_RTS M2	I2C3_SCL M2	VI_CIF_D13	GPIO3_D1 d
VI_BT656_D6 M0	VI_BT1120_D14	UART5_CTS M2	I2C3_SDA M2	VI_CIF_D14	GPIO3_D2 d
VI_BT656_D7 M0	VI_BT1120_D15	PWM1 M2	VI_CIF_D15	GPIO3_D3 d	

MIPI_AVDD1V8 VCCIO7_VCC1V8

RV1106G2

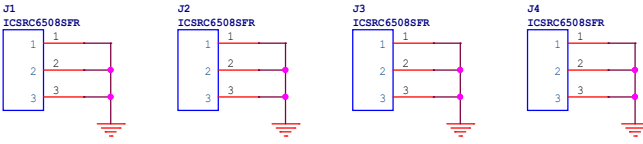
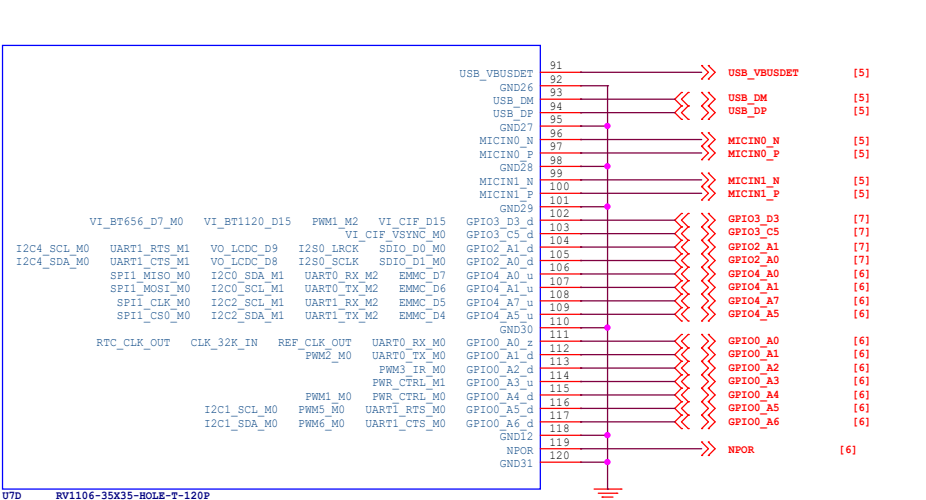
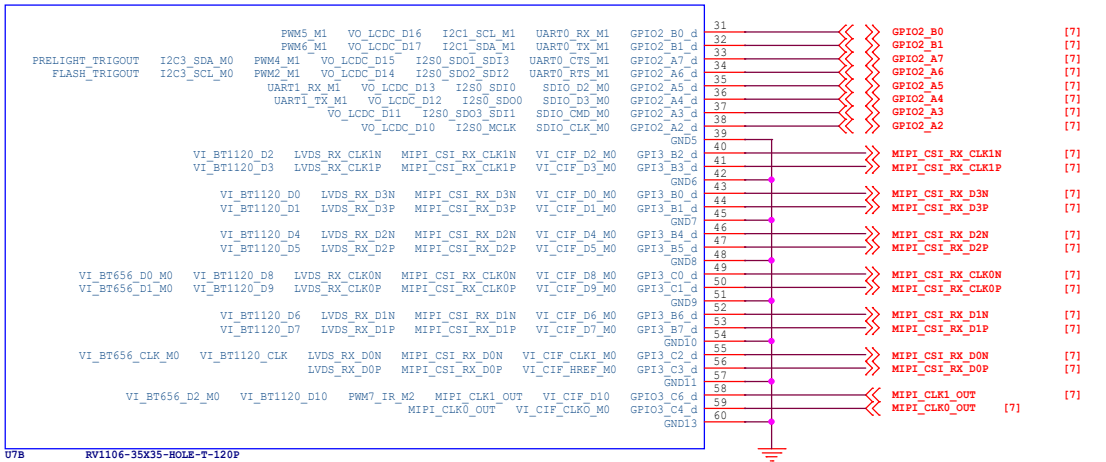
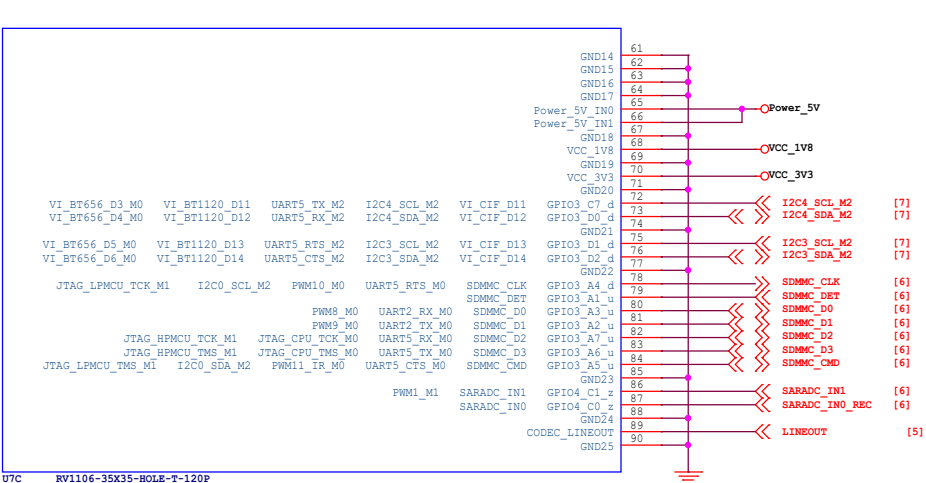
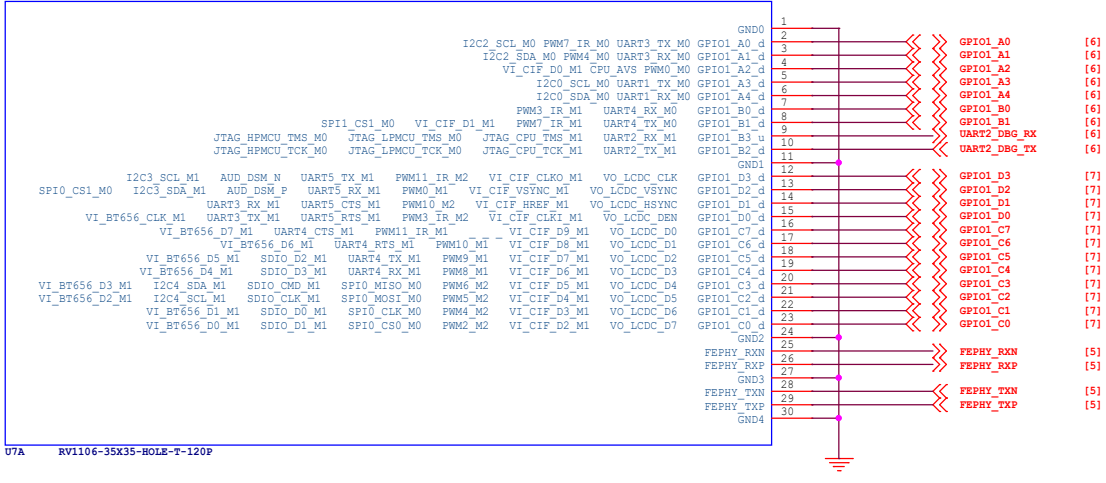
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SOM Connector



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