

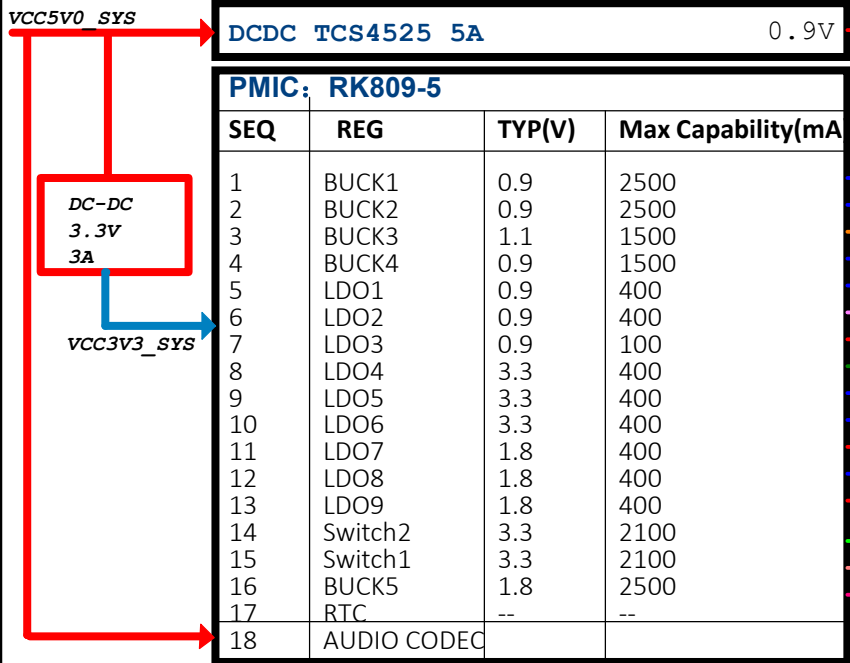
野火 EBF RK3566 核心板

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历史版本

版本号	日期	设计	描述
v1.0	2022-06-09	ghs	初始版本, 开发阶段

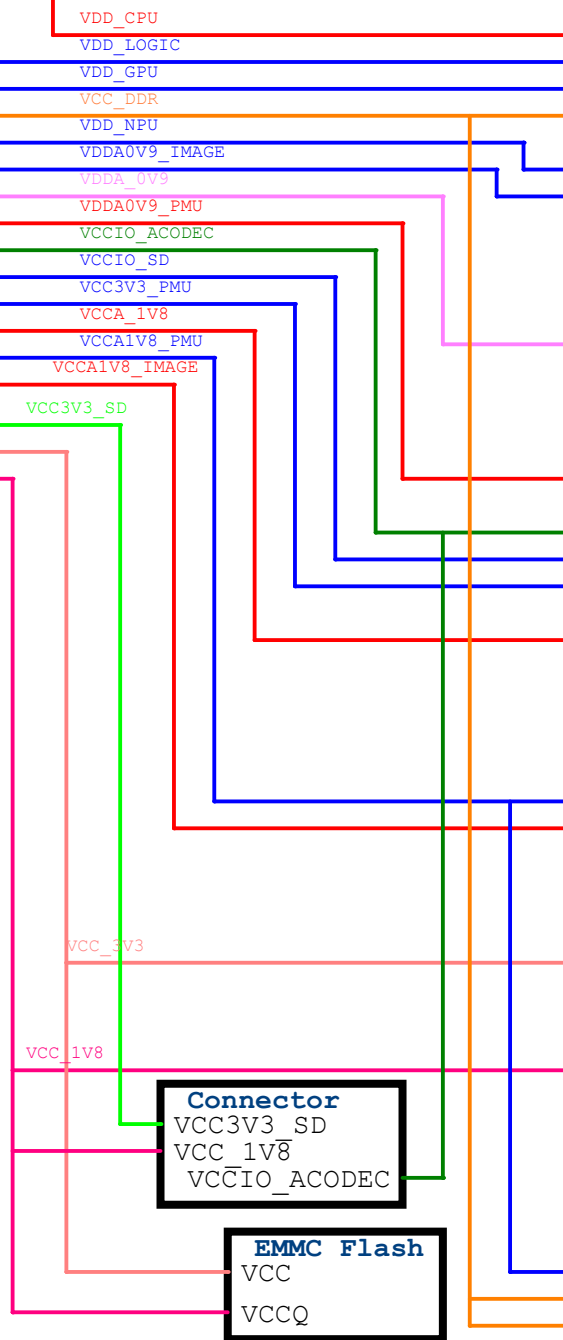
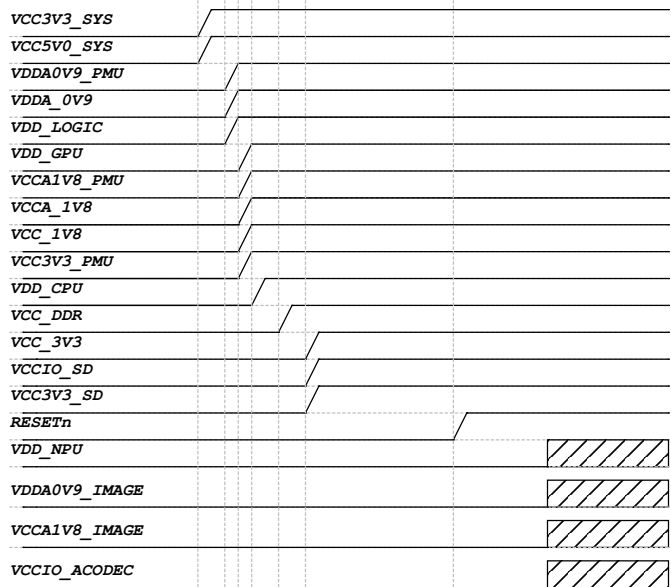


PWR TREE

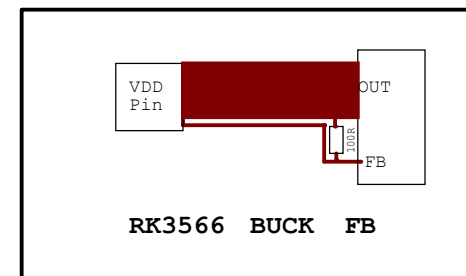
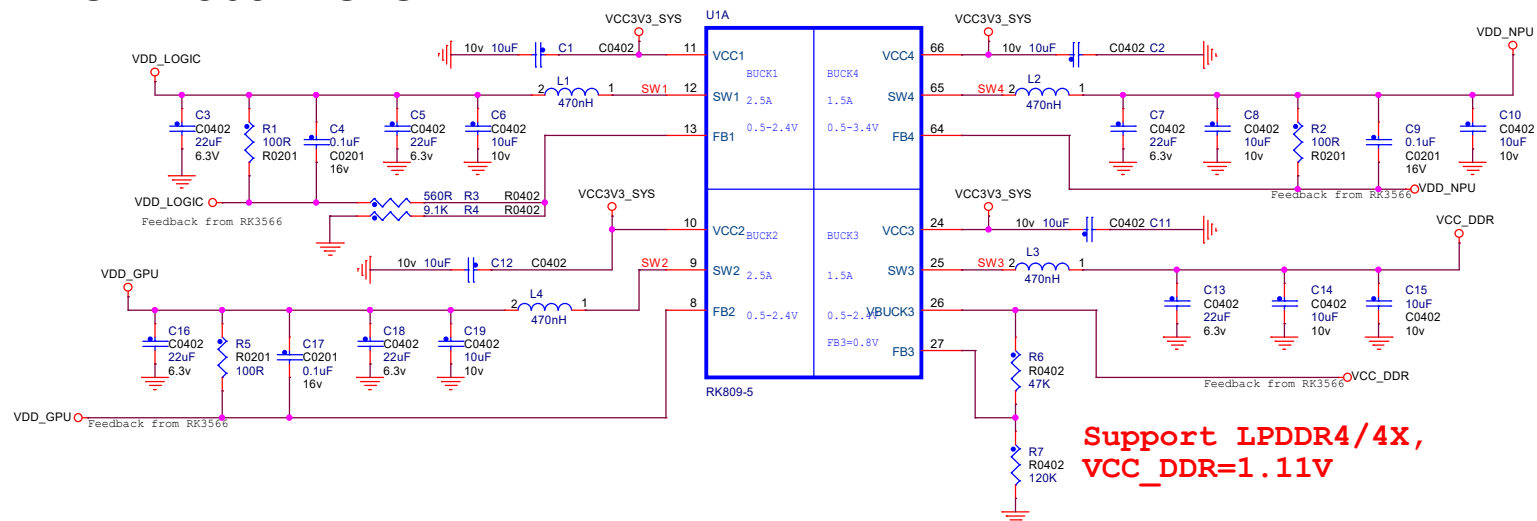
CPU:RK3566

SEQ	REG	TYP(V)
1	VDD_CPU	0.9
2	VDD_LOGIC	0.9
3	VDD_GPU	0.9
4	DDRPHY_VDDQ	1.1
5	DDRPHY_VDDQL	1.1
	VDD_NPU	0.9
6	MIPI_CSI_RX_AVDD_0V9	0.9
	MIPI_DSI_TX0/LVDS_TX0_AVDD_0V9	0.9
	MIPI_DSI_TX1_AVDD_0V9	0.9
	EDP_TX_AVDD_0V9	0.9
	HDMI_TX_AVDD_0V9	0.9
7	SYSPLL_AVDD_0V9	0.9
	USB2_AVDD_0V9	0.9
	USB3_AVDD_0V9	0.9
	MULTI_PHY_AVDD_0V9	0.9
		0.9
8	PMU_VDD_LOGIC_0V9	0.9
	PMUPLL_AVDD_0V9	0.9
9	VCCIO1	3.3
10	VCCIO3	3.3
11	PMUIO1	3.3
	PMUIO2	3.3
12	SYSPLL_AVDD_1V8	1.8
	USB2_AVDD_1V8	1.8
	USB3_AVDD_1V8	1.8
	MULTI_PHY_AVDD_1V8	1.8
	SARADC_AVDD_1V8	1.8
		1.8
13	PMUPLL_AVDD_1V8	1.8
14	MIPI_CSI_RX_AVDD_1V8	1.8
	MIPI_DSI_TX0/LVDS_TX0_AVDD_1V8	1.8
	MIPI_DSI_TX1_AVDD_1V8	1.8
	EDP_TX_AVDD_1V8	1.8
	HDMI_TX_AVDD_1V8	1.8
		1.8
15	USB2_AVDD_3V3	3.3
	USB3_AVDD_3V3	3.3
	VCCIO5	3.3
	VCCIO7	3.3
16	OTP_VCC18	1.8
	VCCIO2	1.8
	VCCIO4	1.8
	VCCIO6	1.8

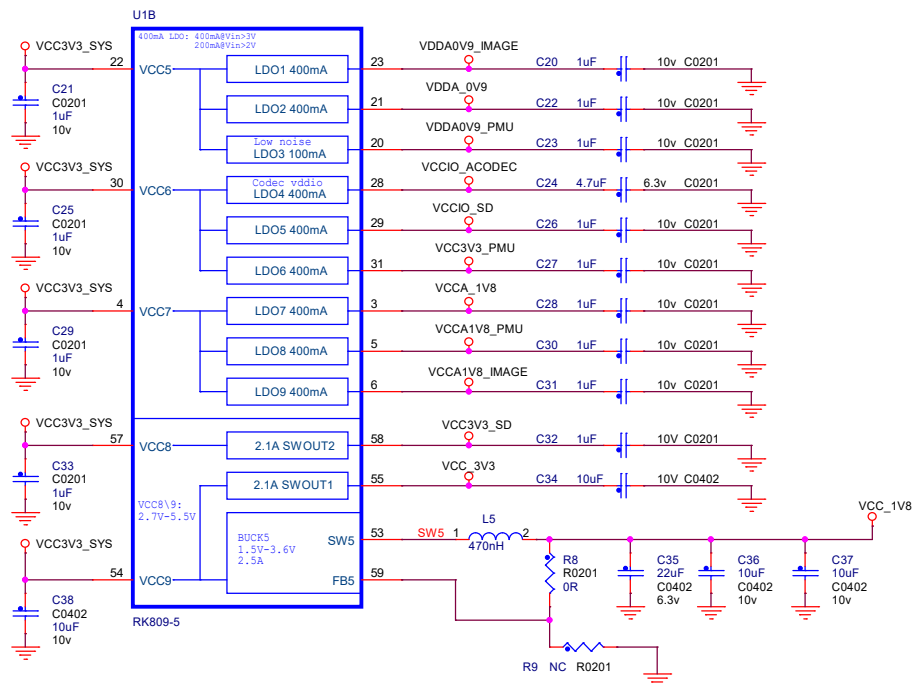
Power Sequence



PMIC RK809 DCDC

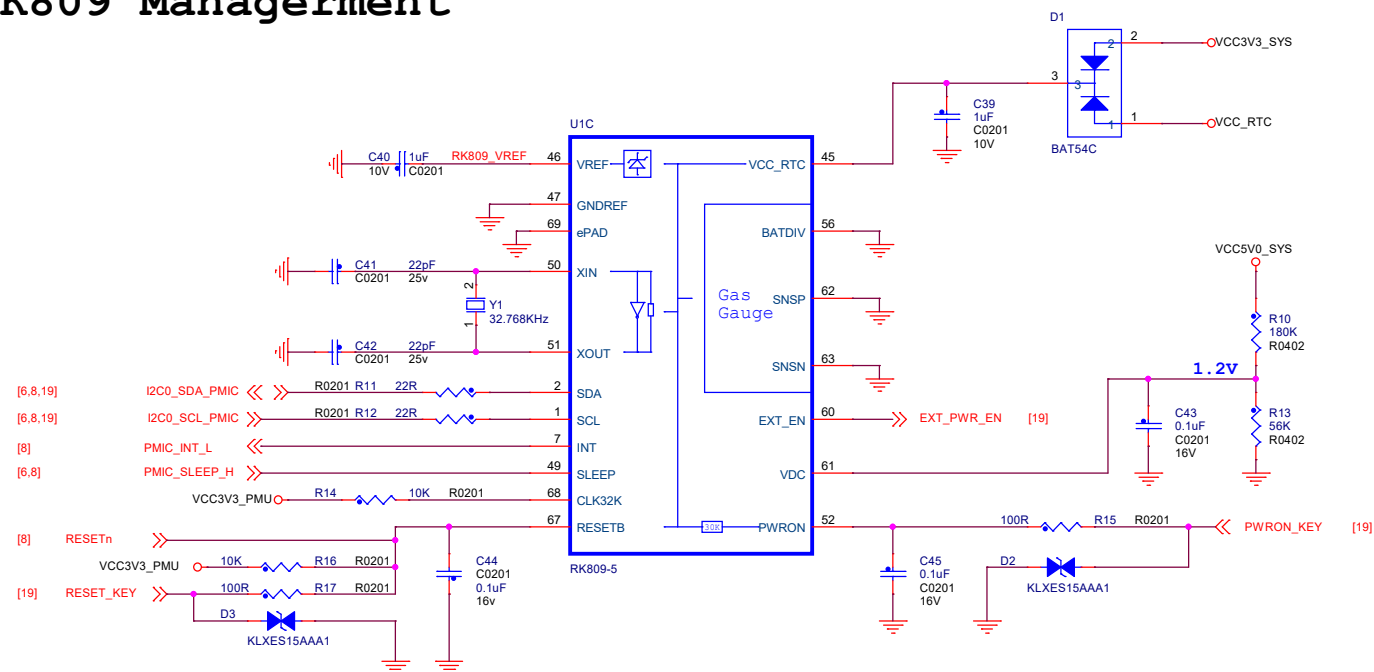


PMIC RK809 LDO

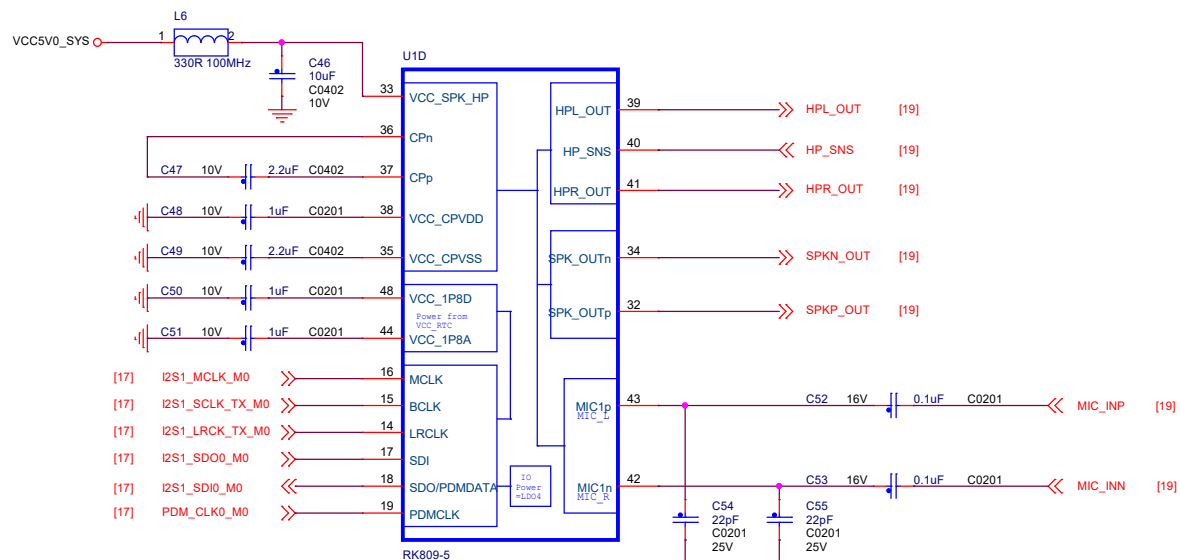


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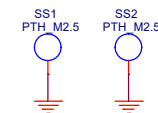
PMIC RK809 Managerment



PMIC RK809 CODEC



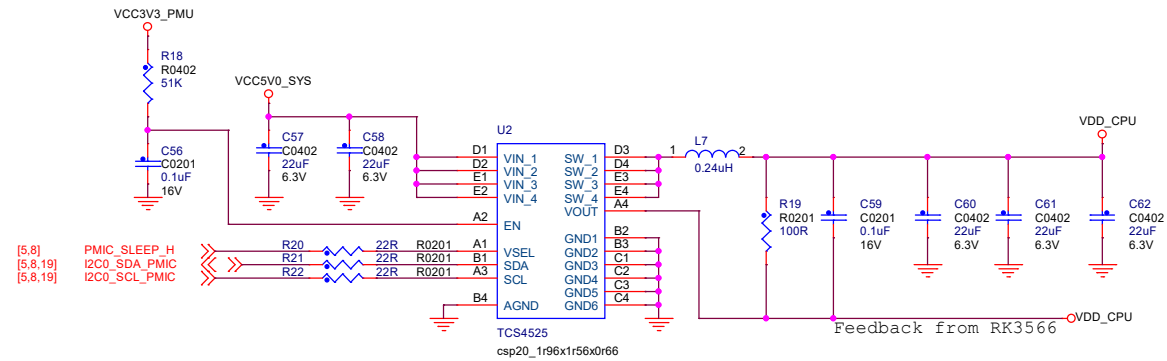
Fix Hole



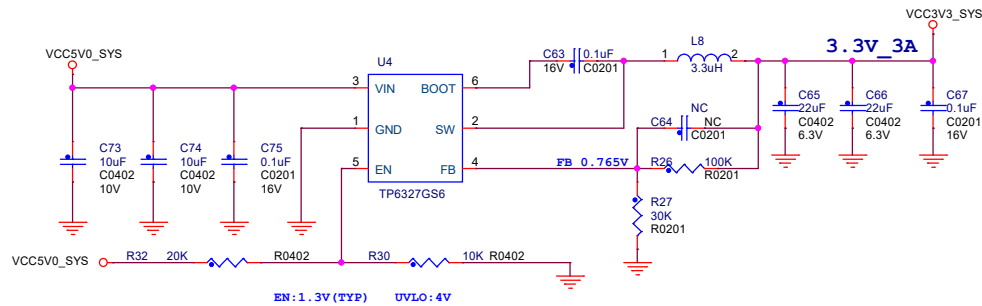
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RK3566 CPU Power

VDD_CPU_EXT

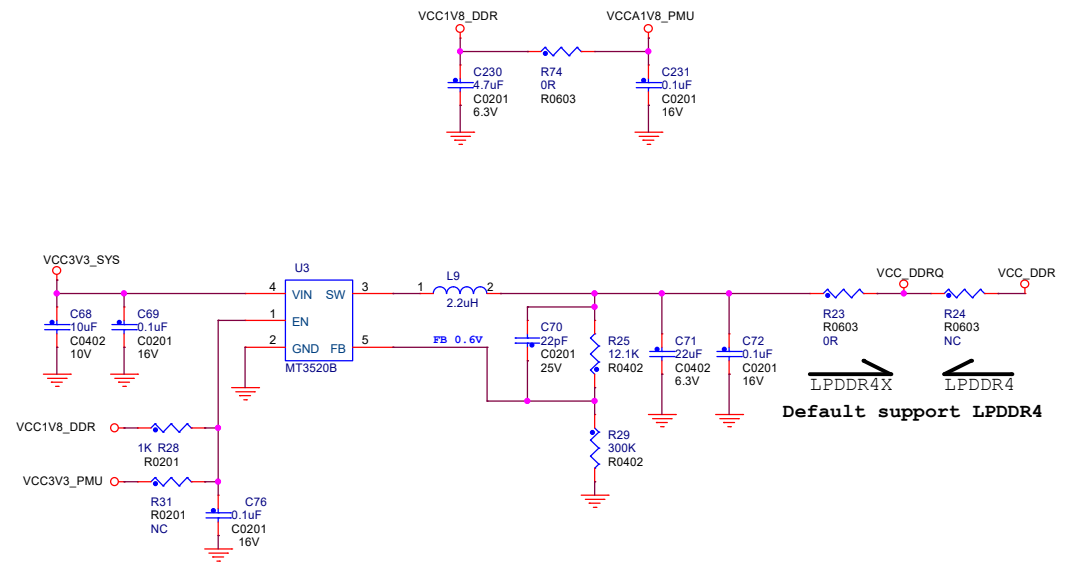


3V3_SYS



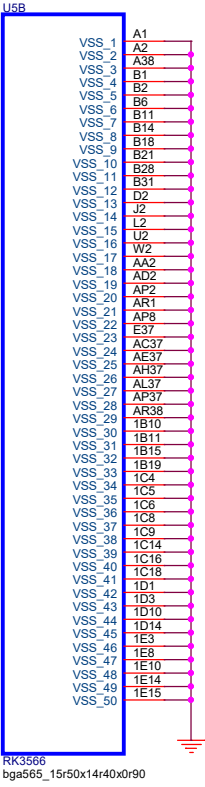
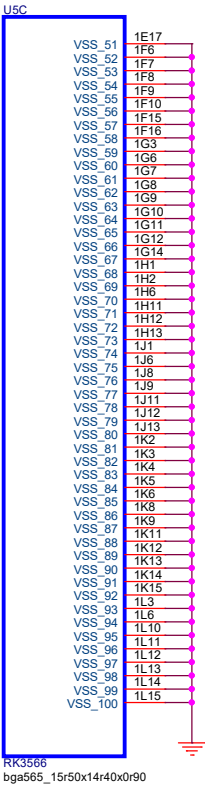
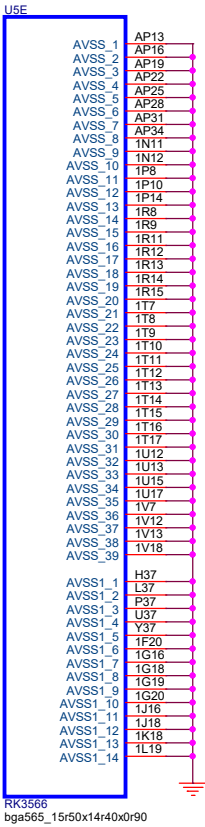
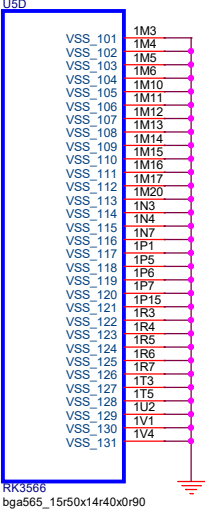
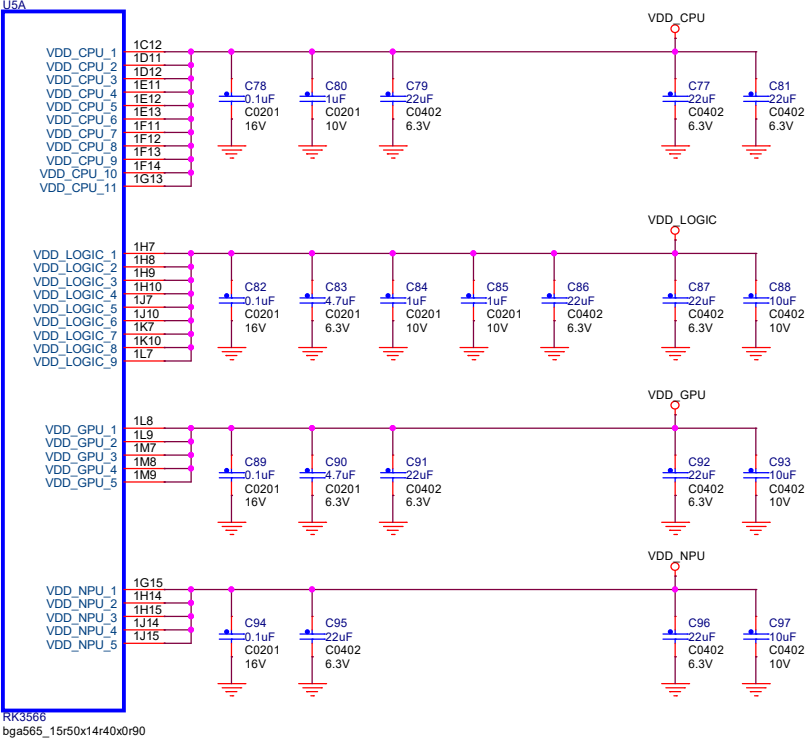
LPDDR4X Power

output: 0.6V



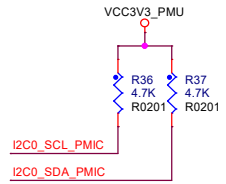
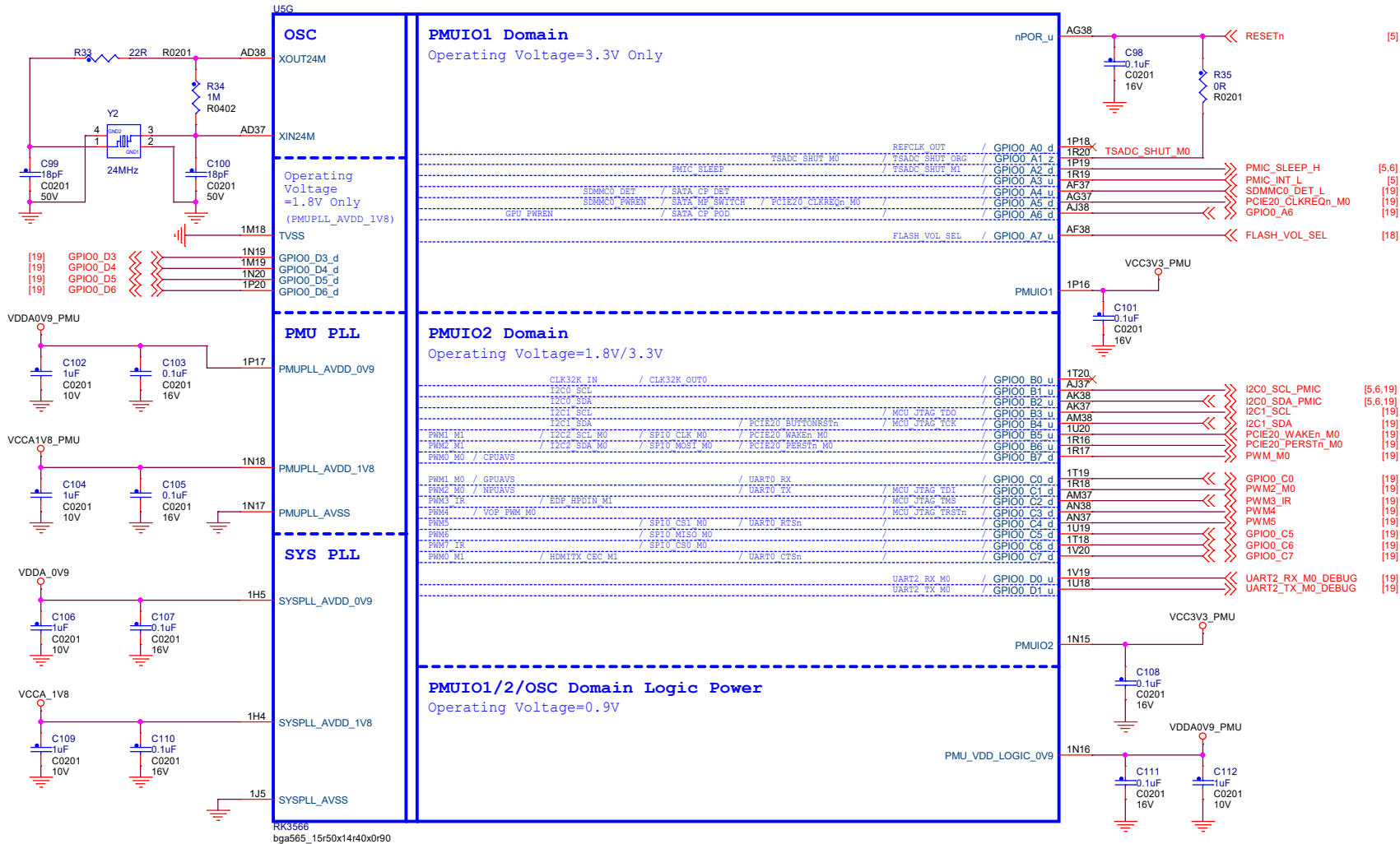
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RK3566 PWR



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RK3566 OSC/PLL/PMUIO



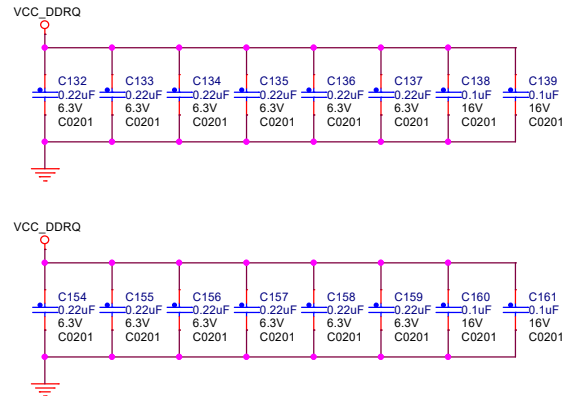
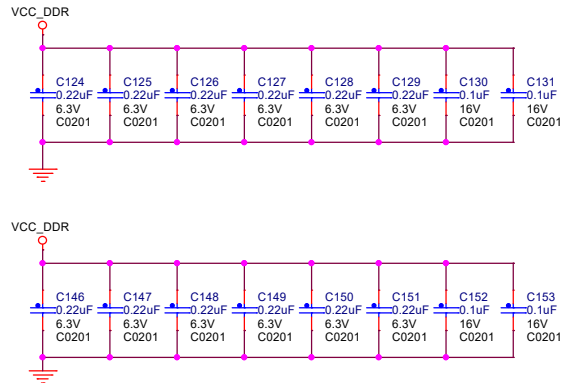
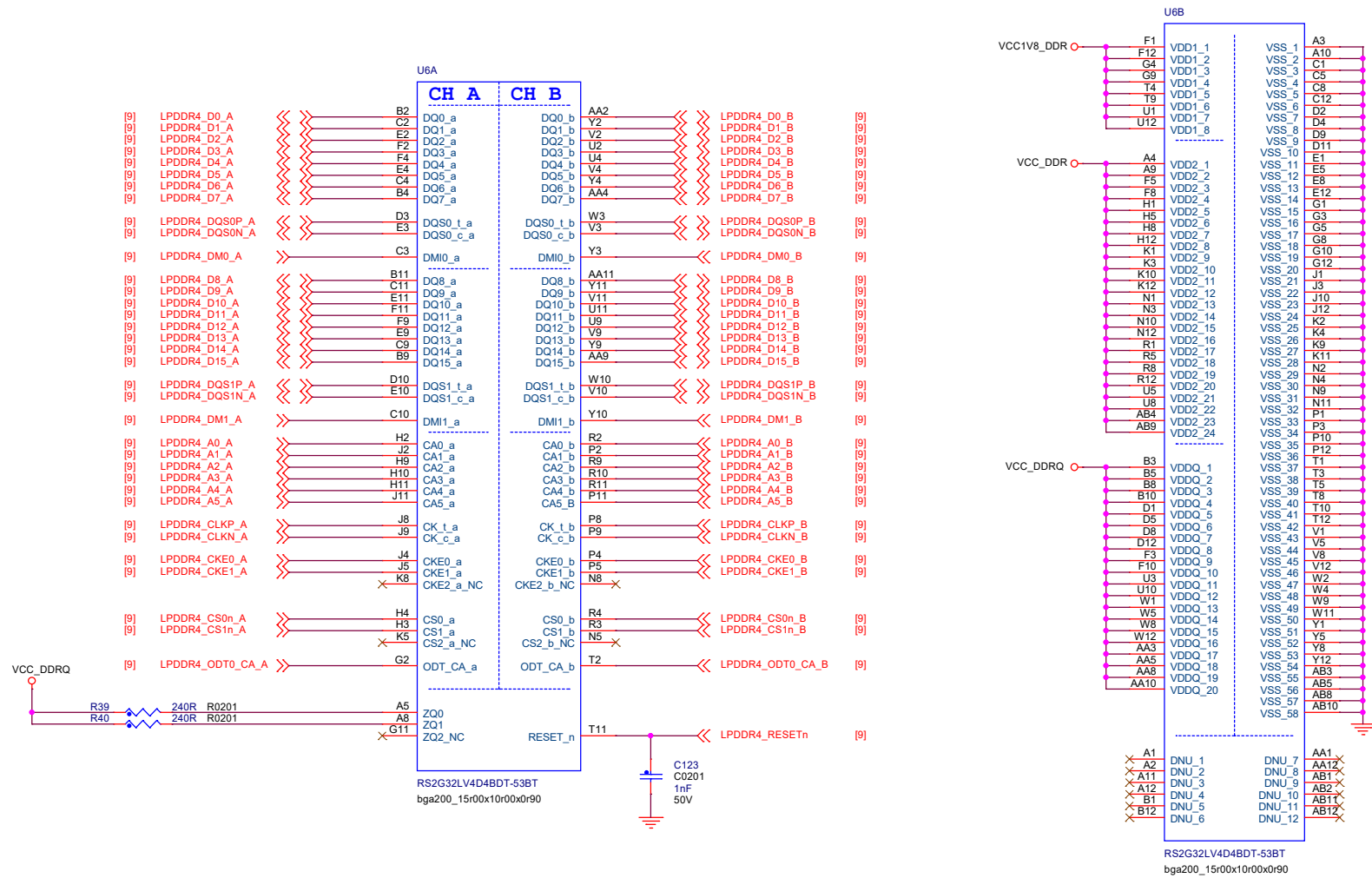
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DDR_VDDQL_1 1E6
DDR_VDDQL_2 1E7
DDR_VDDQL_3 1E9
DDR_VDDQL_4 1F5
DDR_VDDQL_5 1G5

DDR_AVSS 1D4

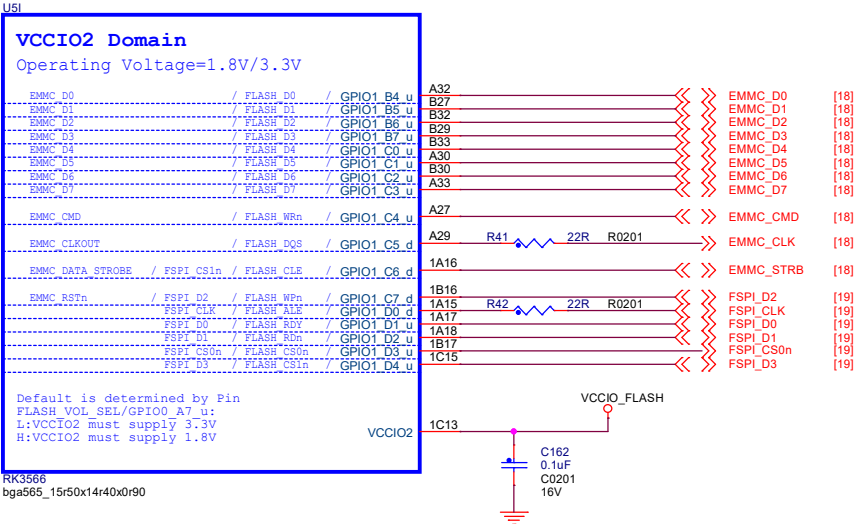
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LPDDR4x

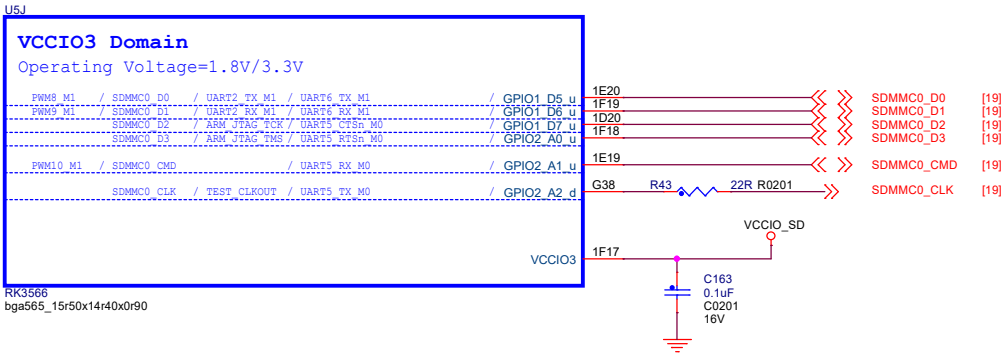


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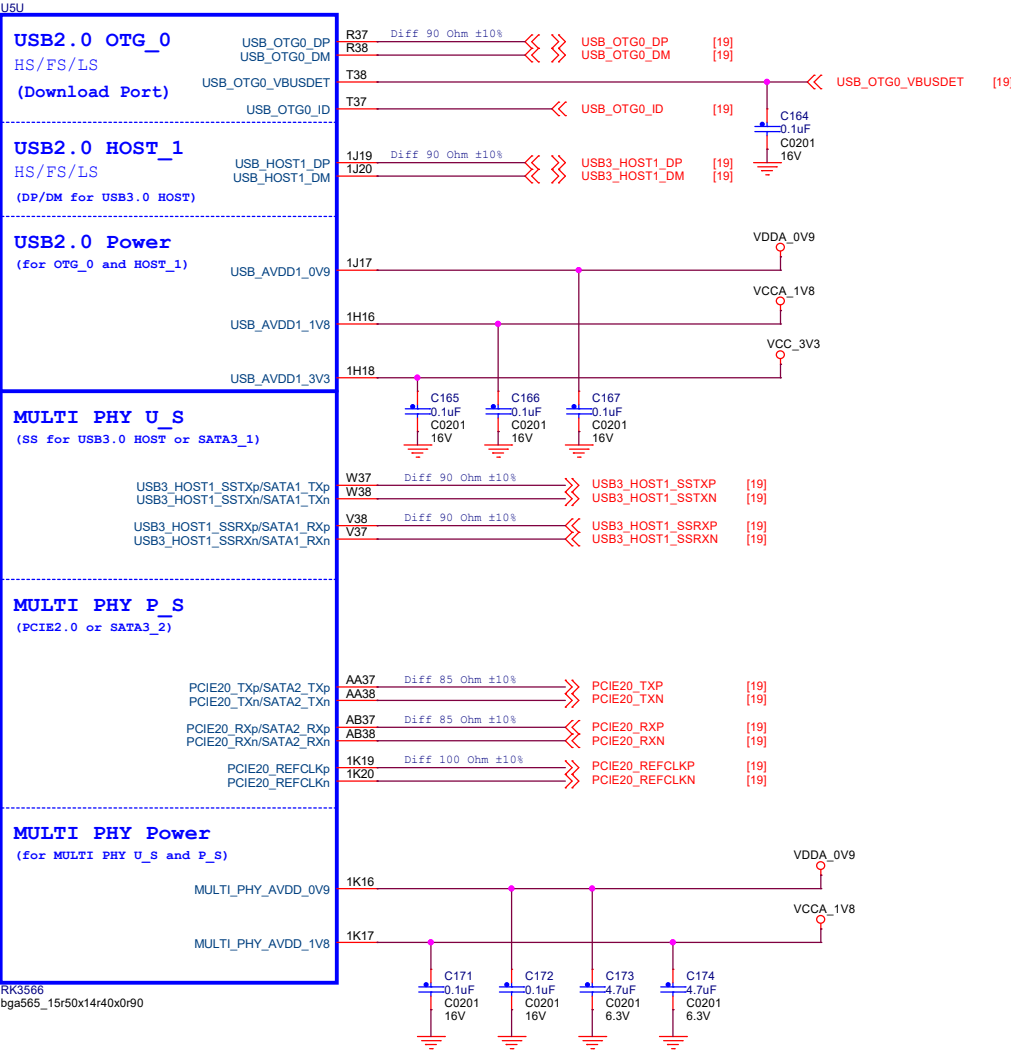
RK3566 VCCIO2 Domain



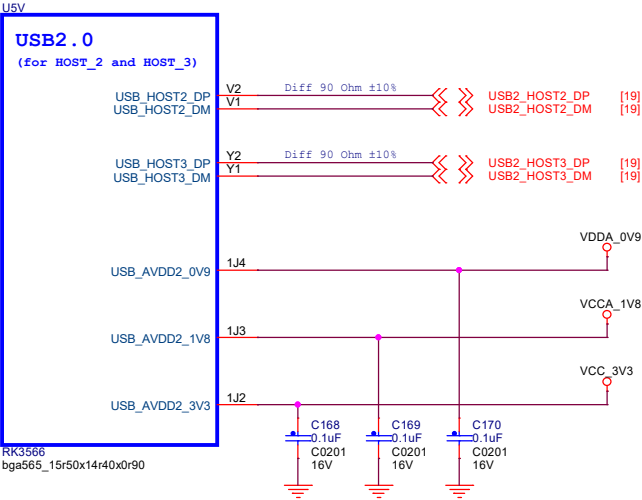
RK3566 VCCIO3 Domain



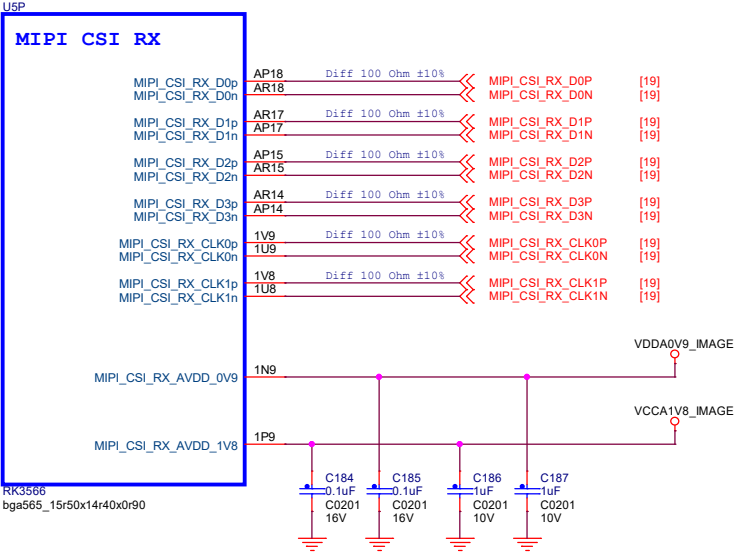
RK3566 USB3.0/SATA/QSGMII/PCIE2.0 x1



RK3566 USB2.0 HOST

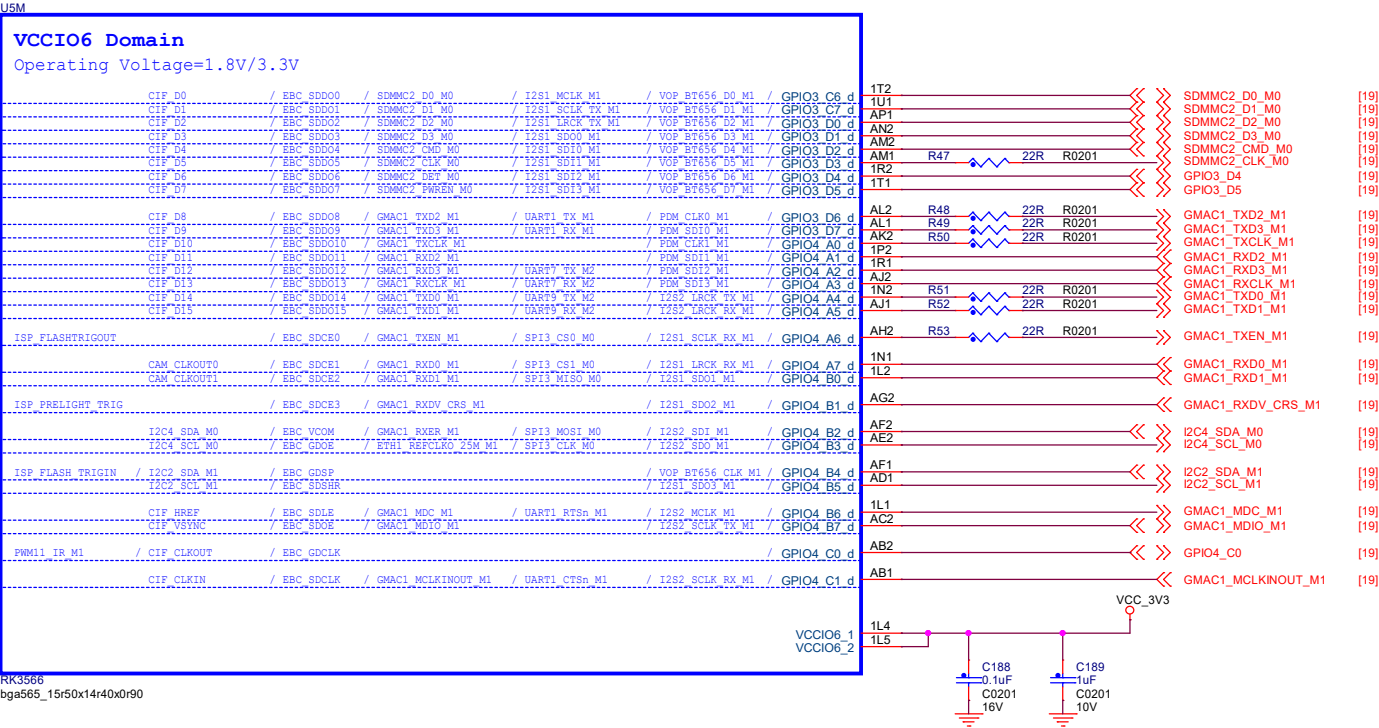


RK3566 MIPI_CSI_RX



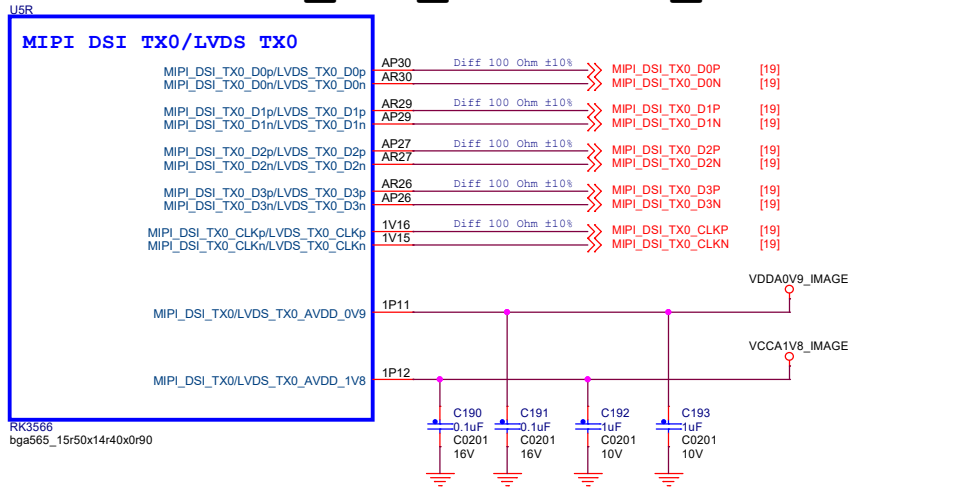
Option1	Sensor1 x4Lane	MIPI_CSI_RX_D0-3 MIPI_CSI_RX_CLK0
Option2	Sensor1 x2Lane	MIPI_CSI_RX_D0-1 MIPI_CSI_RX_CLK0
	+ Sensor2 x2Lane	MIPI_CSI_RX_D2-3 MIPI_CSI_RX_CLK1

RK3566 VCCIO6 Domain

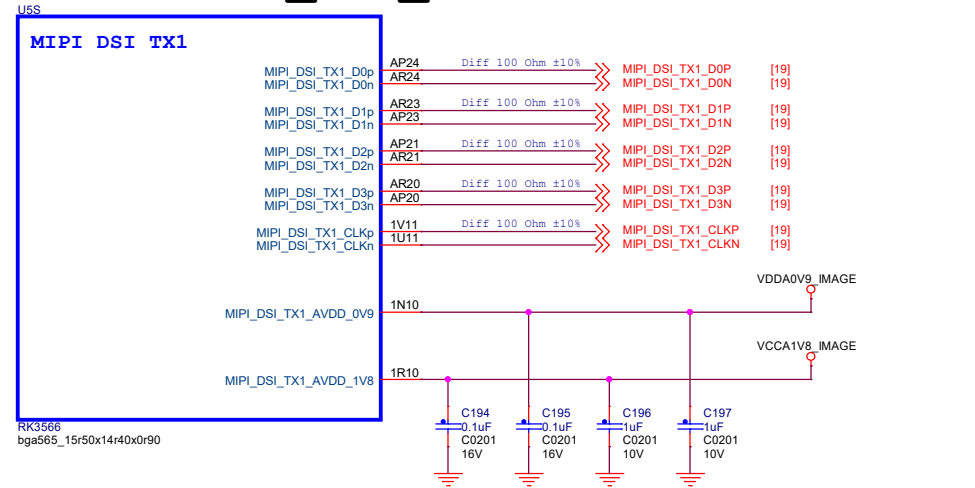


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Size A3	Document Number VI Interface	Rev V1.0
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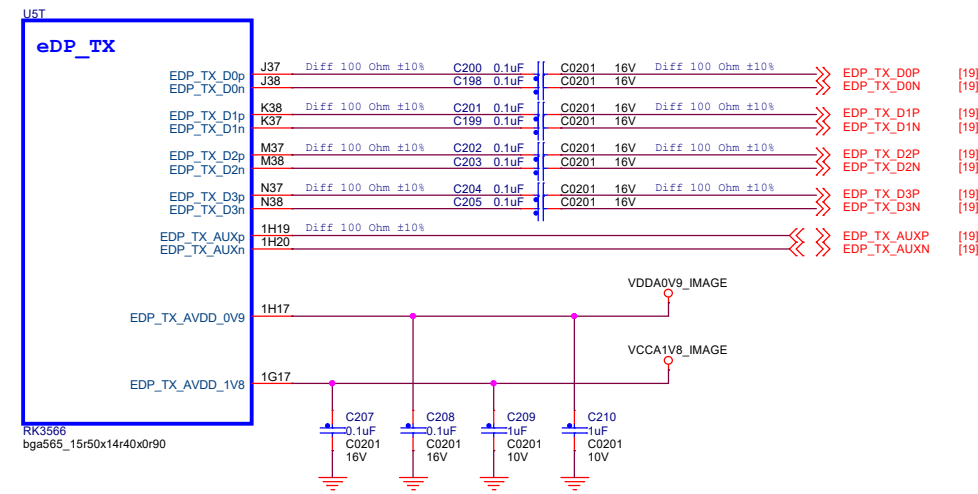
RK3566 MIPI_DSI_TX0/LVDS_TX0



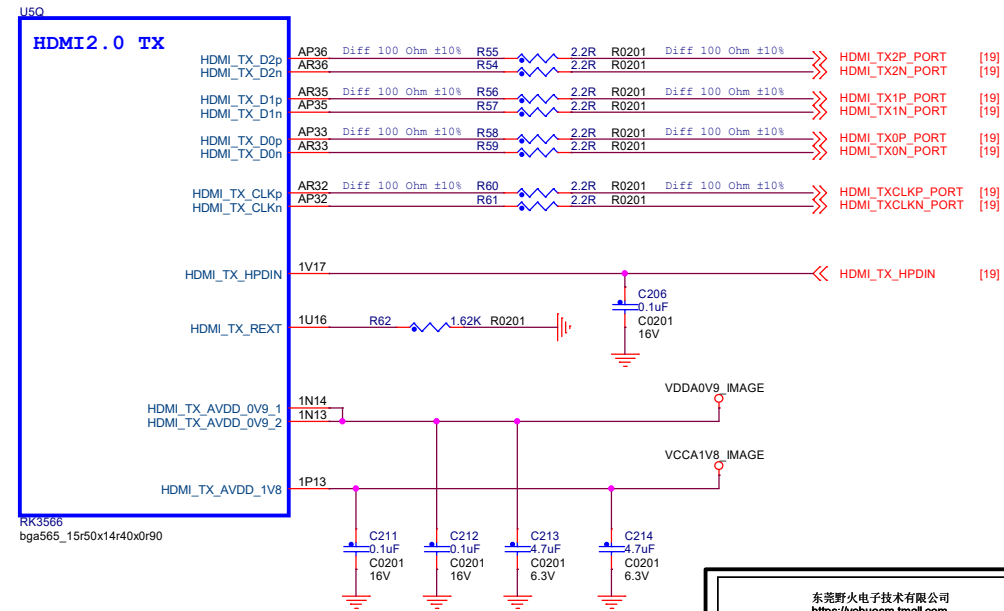
RK3566 MIPI_DSI_TX1



RK3566 eDP TX



RK3566 HDMI2.0 TX



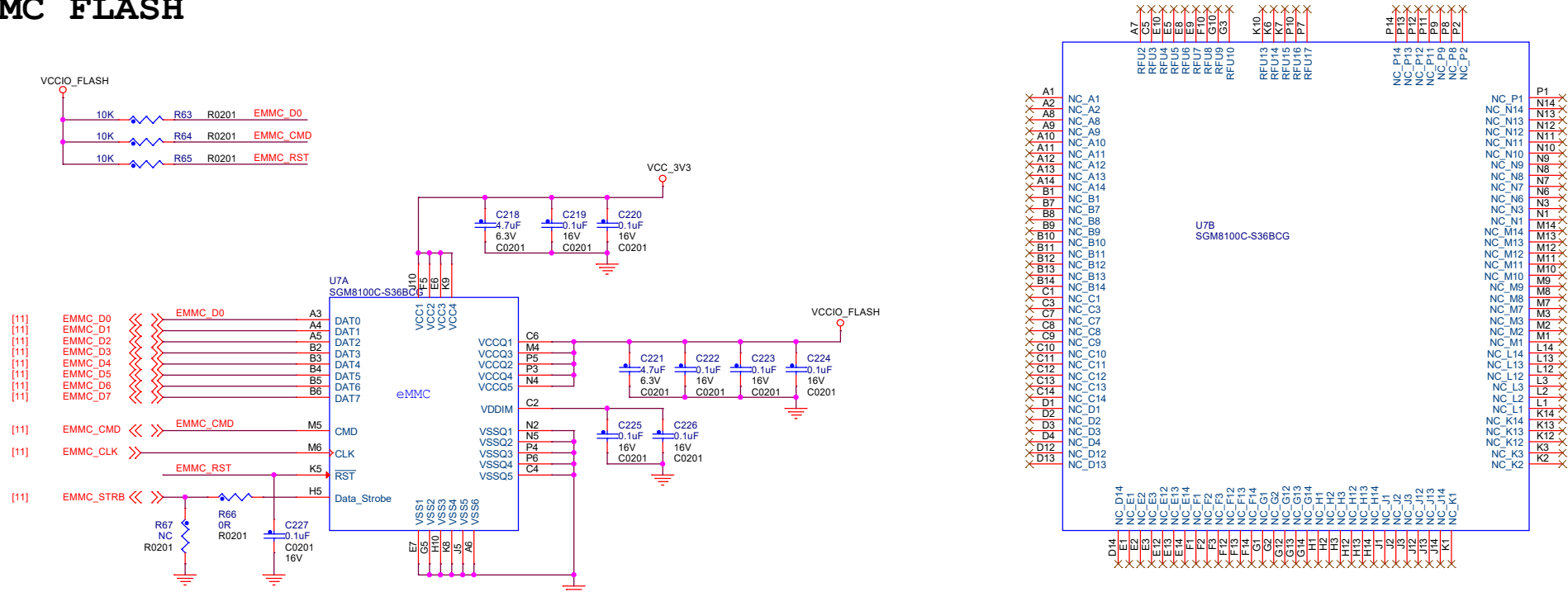
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A3	VO Interface_1				V1.0
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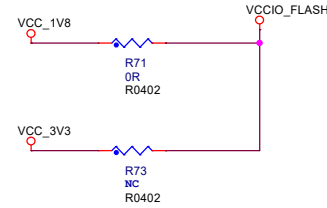
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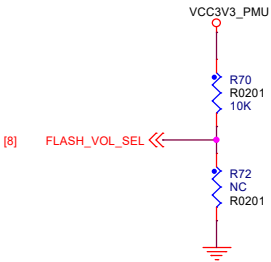
EMMC FLASH



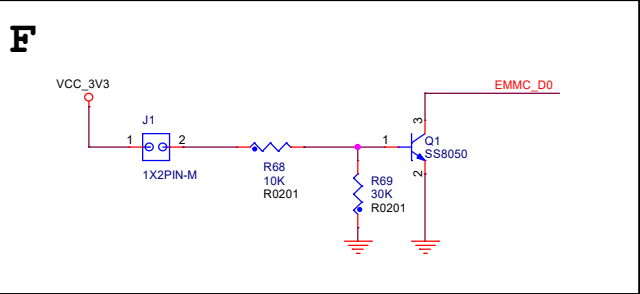
EMMC_IO_Toggle



FLASH_VOL_SEL



Note:
FLASH_VOL_SEL state decided
to VCCIO2 domain IO driven by default
Logic=L: 3.3V IO driven
Logic=H: 1.8V IO driven



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DDR4-SODIMM260 Connector

