

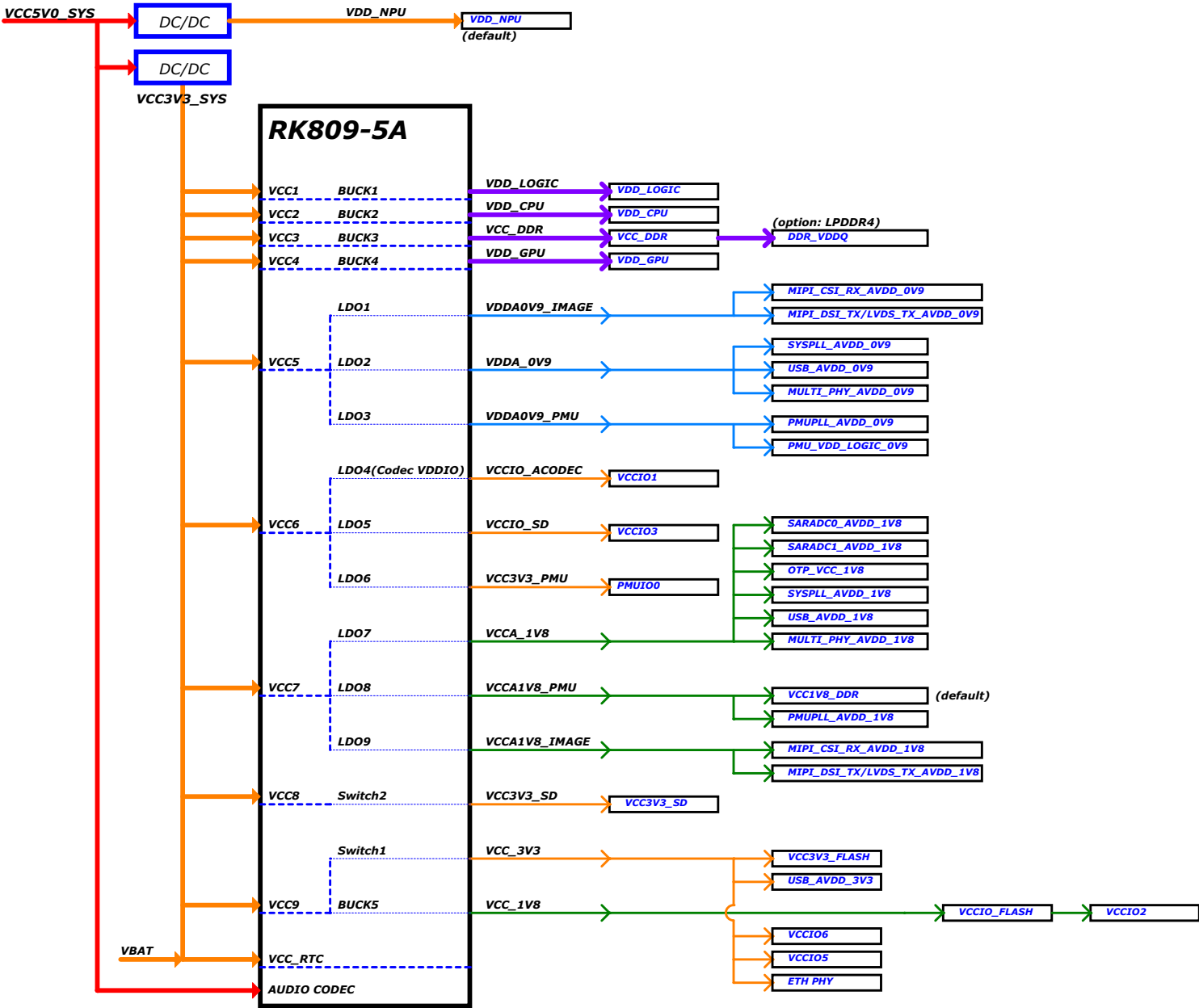
野火鲁班猫1HS. 邮票孔核心板

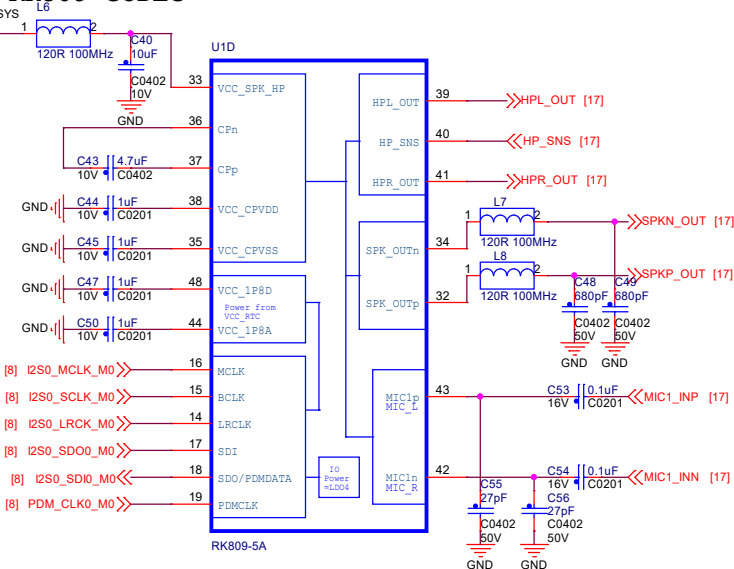
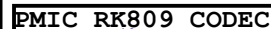
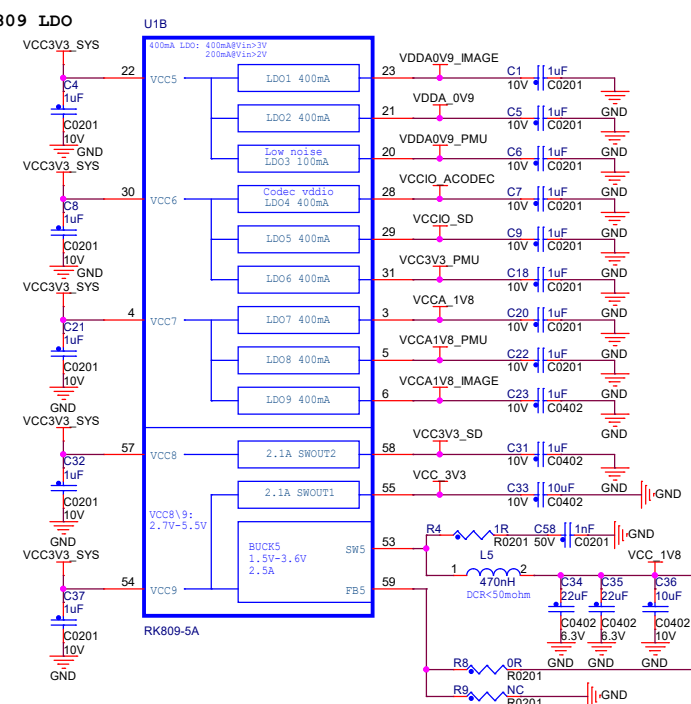
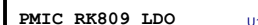
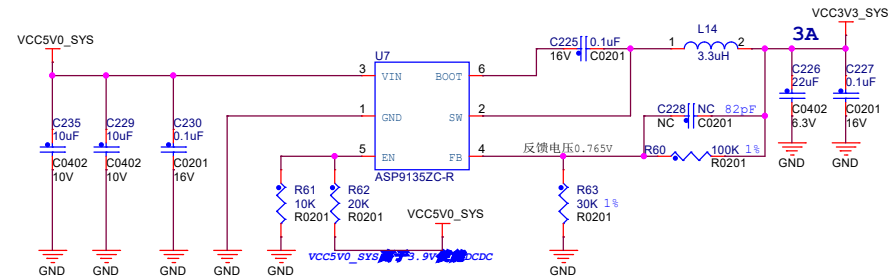
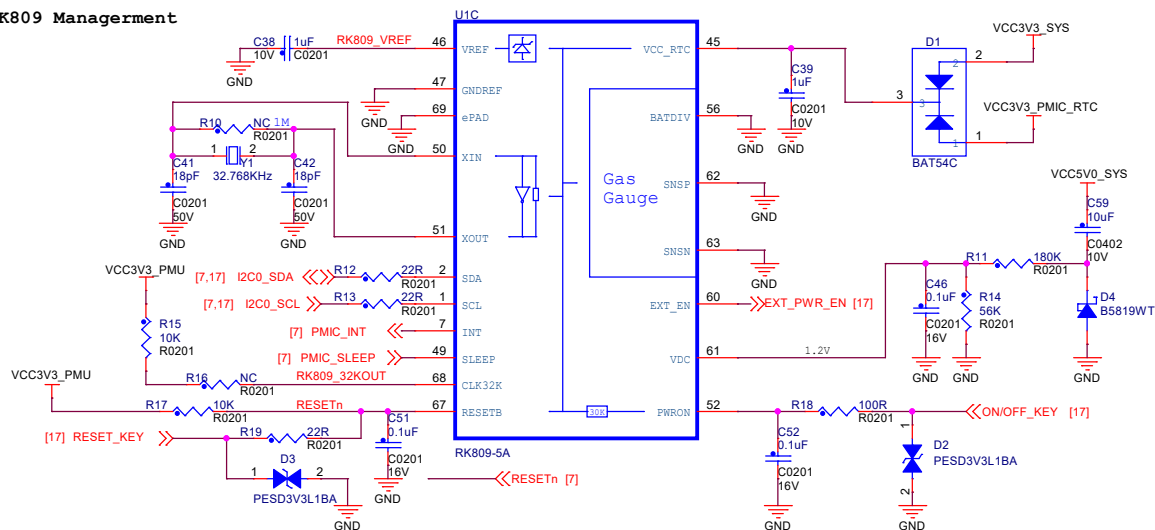
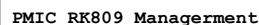
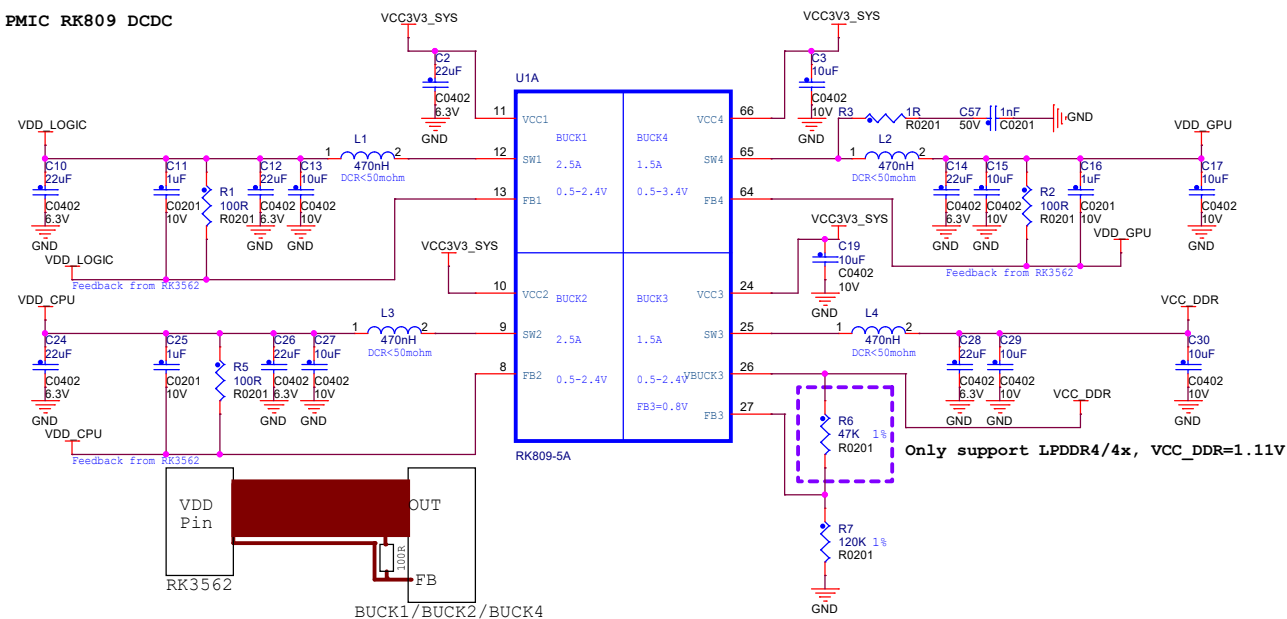
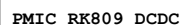
Page 1	目录
Page 2	历史版本
Page 3	PWR TREE
Page 4	PMIC
Page 5	VDD_NPU/CPU PWR
Page 6	DDR PHY
Page 7	OSC/PLL/PMUIO
Page 8	VCCIO1
Page 9	FLASH/SD CONTROLER
Page 10	VCCIO4/SARADC
Page 11	VCCIO5/MIPI CSI
Page 12	VCCIO6/MIPI DSI/LVDS
Page 13	USB/PCIE2.0
Page 14	LPDDR4/LPDDR4x
Page 15	EMMC FLASH
Page 16	邮票孔

历史版本

版本号	日期	设计	描述
V1.0	2023-07-27	llm	初始版本

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Title 野火鲁班猫1HS.邮票孔核心板		
Size A4	Document Number 历史版本	Rev V1.0
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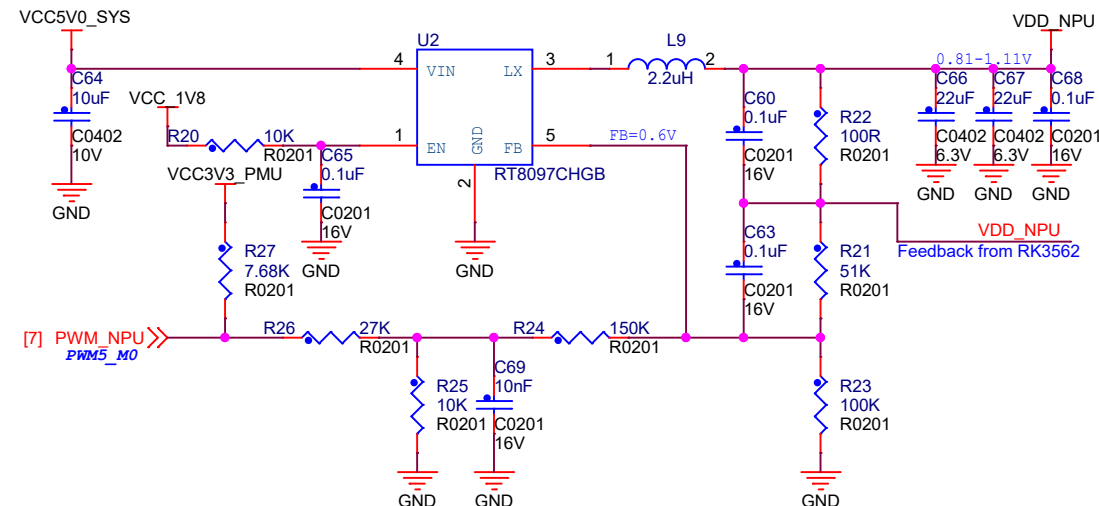


U3A

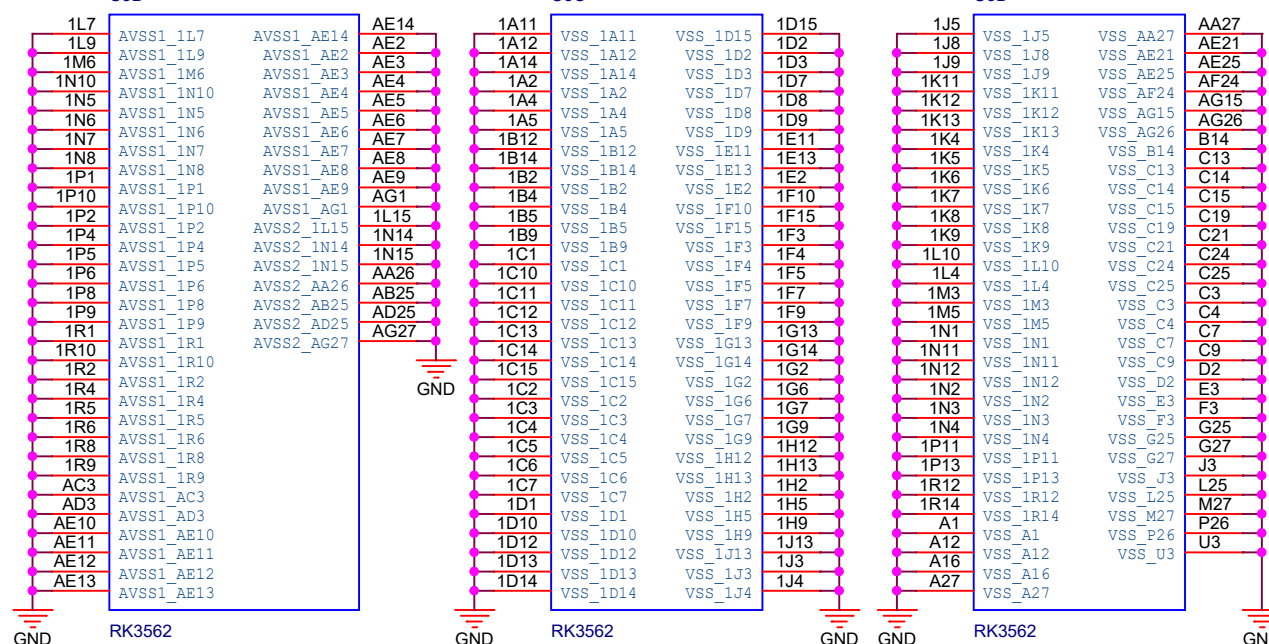


Caps should be placed close to the U3 package

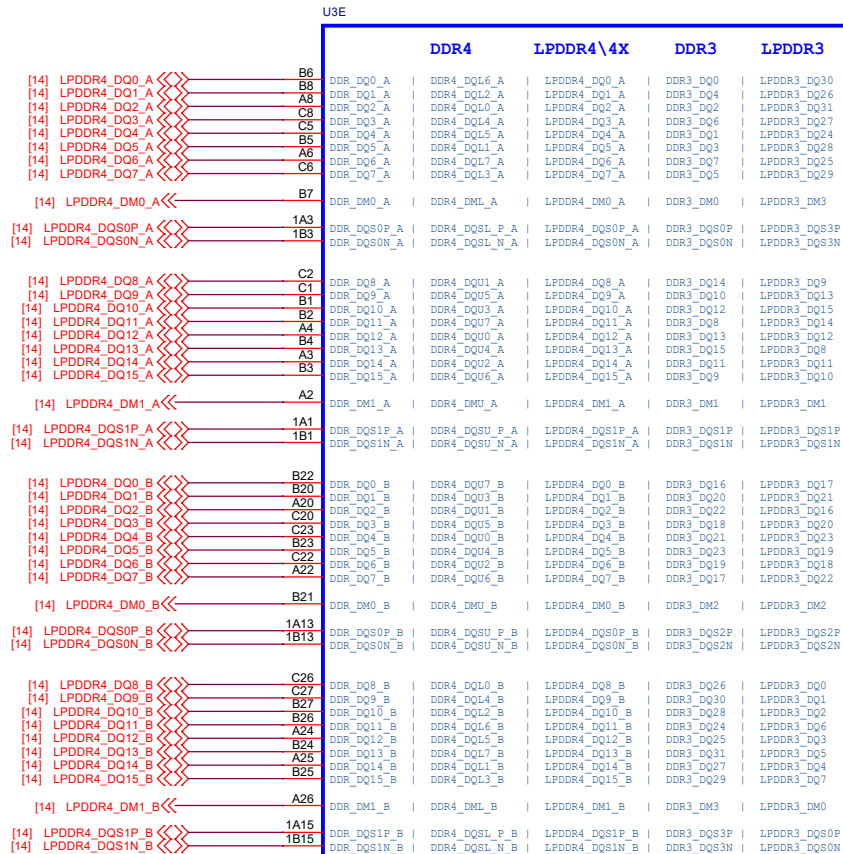
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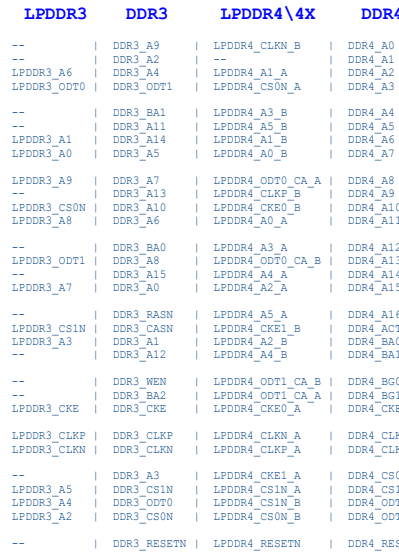
U3



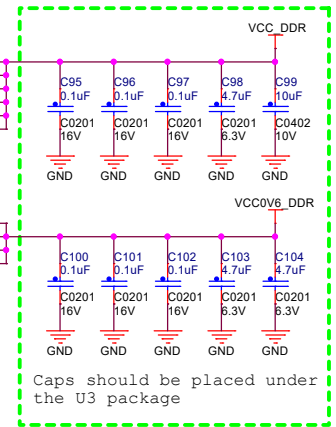
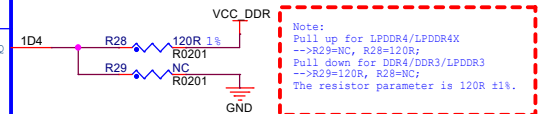
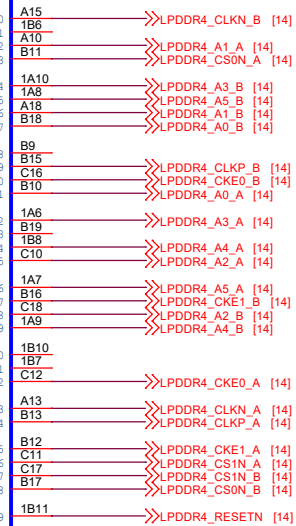
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Note:
Except DDR3/DDR4, other DQ sequences
can not be swap



Note: Sequences can not be swap. Note the CLK polarity.



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Title
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Size
A3

Document Number
DDR PHY

Rev
V1.0

Date: Friday, November 29, 2024

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Caps of between dashed green lines and U3
should be placed under the U3 package.
Other caps should be placed close to the U3 package

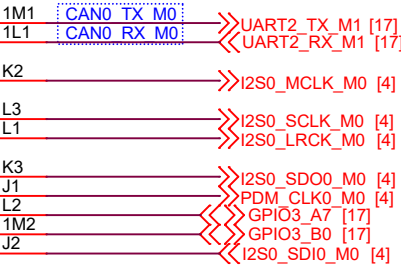
RK3562 VCCIO1

U3G

VCCIO1 Domain

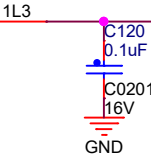
Operating Voltage=1.8V/3.3V

--	UART5_CTSN_M1	PDM_SDI3_M0	UART2_TX_M1	I2C3_SCL_M0	GPIO3_A0_d
--	UART5_RTSN_M1	SPDIF_TX_M0	UART2_RX_M1	I2C3_SDA_M0	GPIO3_A1_d
--	--	PDM_CLK1_M0	UART2_CTSN_M1	I2S0_MCLK_M0	GPIO3_A2_d
--	--	--	UART2_RTSN_M1	I2S0_SCLK_M0	GPIO3_A3_d
--	PWM8_M0	--	--	I2S0_LRCK_M0	GPIO3_A4_d
--	PWM9_M0	--	--	I2S0_SDO0_M0	GPIO3_A5_d
PCIE20_CLKREQN_M1	UART5_TX_M1	PDM_CLK0_M0	I2S0_SDI3_M0	I2S0_SDO1_M0	GPIO3_A6_d
PCIE20_WAKEN_M1	UART5_RX_M1	PDM_SDI2_M0	I2S0_SDI2_M0	I2S0_SDO2_M0	GPIO3_A7_d
PCIE20_PERSTN_M1	--	PDM_SDI1_M0	I2S0_SDI1_M0	I2S0_SDO3_M0	GPIO3_B0_d
--	--	PDM_SDI0_M0	--	I2S0_SDI0_M0	GPIO3_B1_d



VCCIO1

VCCIO_ACODEC



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Size A4	Document Number VCCIO1	Rev V1.0
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RK3562 VCCIO2

U3H

VCCIO2 Domain

Operating Voltage=1.8V/3.3V

FSPI_D0	EMMC_D0	GPI01_A0_u	J25		EMMC_D0 [15]
FSPI_D1	EMMC_D1	GPI01_A1_u	J26		EMMC_D1 [15]
FSPI_D2	EMMC_D2	GPI01_A2_u	H25		EMMC_D2 [15]
FSPI_D3	EMMC_D3	GPI01_A3_u	K27		EMMC_D3 [15]
--	EMMC_D4	GPI01_A4_u	K26		EMMC_D4 [15]
--	EMMC_D5	GPI01_A5_u	K25		EMMC_D5 [15]
--	EMMC_D6	GPI01_A6_u	H26		EMMC_D6 [15]
--	EMMC_D7	GPI01_A7_u	H27		EMMC_D7 [15]
FSPI_CSN0	EMMC_CMD	GPI01_B0_u	M26		EMMC_CMD [15]
FSPI_CLK	EMMC_CLK	GPI01_B1_d	M25		EMMC_CLK [15]
FSPI_CSN1	EMMC_STRB	GPI01_B2_d	L26		EMMC_DATA_STROBE [15]

VCCIO2

VCCIO_FLASH

1F14

C121
0.1uF

C0201
16V

GND

RK3562 VCCIO3

U3I

VCCIO3 Domain

Operating Voltage=1.8V/3.3V

DSM_AUD_LP	--	SPI1_MOSI_M1	UART7_RX_M1	UART0_RX_M1	SDMMC0_D0	GPI01_B3_u
DSM_AUD_LN	--	SPI1_MISO_M1	UART7_TX_M1	UART0_TX_M1	SDMMC0_D1	GPI01_B4_u
DSM_AUD_RP	PWM10_M0	SPI1_CSN1_M1	UART5_CTSN_M0	JTAG_CPU_MCU_TCK_M1	SDMMC0_D2	GPI01_B5_u
DSM_AUD_RN	PWM11_M0	SPI1_CSN0_M1	UART5_RTSN_M0	JTAG_CPU_MCU_TMS_M1	SDMMC0_D3	GPI01_B6_u
--	SPDIF_TX_M2	--	UART5_RX_M0	--	SDMMC0_CMD	GPI01_B7_u
--	--	SPI1_CLK_M1	UART5_TX_M0	TEST_CLK_OUT	SDMMC0_CLK	GPI01_C0_d

VCCIO3

VCCIO_SD

1G15

C122
0.1uF

C0201
16V

GND

R25		SDMMC0_D0 [17]
R26		SDMMC0_D1 [17]
N25		SDMMC0_D2 [17]
N26		SDMMC0_D3 [17]
P25	CAN1_RX_M0	SDMMC0_CMD [17]
P27	CAN1_TX_M0	SDMMC0_CLK [17]

R34
22R
R0201

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Size A4	Document Number FLASH/SD CONTROLLER	Rev V1.0
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RK3562 VCCIO4

U3J

VCCIO4 Domain

Operating Voltage=1.8V/3.3V

--	PWM8_M1	--	I2S0_SDIO_M1	RGMI1_TXD0_M1	SDMMC1_D0	GPIO1_C1_d	U26	>>>SDMMC1_D0 [17]
--	PWM9_M1	--	I2S0_SDI1_M1	RGMI1_TXD3_M1	SDMMC1_D1	GPIO1_C2_d	V25	>>>SDMMC1_D1 [17]
--	PWM10_M1	--	I2S0_SDO0_M1	RGMI1_TXCLK_M1	SDMMC1_D2	GPIO1_C3_d	V25	>>>SDMMC1_D2 [17]
--	PWM11_M1	--	I2S0_LRCK_M1	RGMI1_RXD2_M1	SDMMC1_D3	GPIO1_C4_d	V26	>>>SDMMC1_D3 [17]
--	PWM0_M1	--	I2S0_SCLK_M1	RGMI1_RXD3_M1	SDMMC1_CMD	GPIO1_C5_d	T27	>>>SDMMC1_CMD [17]
--	PWM1_M1	--	I2S0_MCLK_M1	RGMI1_RXCLK_M1	SDMMC1_CLK	GPIO1_C6_d	U25	>>>SDMMC1_CLK [17]
RMII_MDC	PWM2_M1	I2C5_SCL_M1	--	RGMI1_MDC_M1	SDMMC1_PWREN	GPIO1_C7_d	1J14	>>>I2C5_SCL_M1 [17]
RMII_MDIO	PWM3_M1	I2C5_SDA_M1	--	RGMI1_MDIO_M1	SDMMC1_DET_N	GPIO1_D0_d	V26	>>>I2C5_SDA_M1 [17]
RMII_TXD0	PWM4_M1	--	I2S0_SDO1_M1	RGMI1_TXD0_M1	UART1_RX_M0	GPIO1_D1_d	Y25	>>>UART1_RX_M0 [17]
RMII_TXD1	PWM5_M1	--	I2S0_SDO2_M1	RGMI1_TXD1_M1	UART1_TX_M0	GPIO1_D2_d	V26	>>>UART1_TX_M0 [17]
RMII_TXEN	PWM6_M1	--	I2S0_SDI2_M1	RGMI1_TXEN_M1	UART1_RTSN_M0	GPIO1_D3_d	AA25	>>>UART1_RTSN_M0 [17]
RMII_RXD0	PWM7_M1	--	I2S0_SDI3_M1	RGMI1_RXD0_M1	UART1_CTSN_M0	GPIO1_D4_d	Y27	>>>UART1_CTSN_M0 [17]
RMII_CLK	--	SPI2_CS_N1_M1	UART4_RX_M1	RGMI1_CLK_M1	I2S2_SCLK_M0	GPIO1_D5_d	W26	>>>I2S2_SCLK_M0 [17]
RMII_RXDV_CRS	--	SPI2_CS_N0_M1	UART4_TX_M1	RGMI1_RXDV_M1	I2S2_LRCK_M0	GPIO1_D6_d	V27	>>>I2S2_LRCK_M0 [17]
RMII_RXD1	PWM14_M1	SPI2_MISO_M1	UART4_RTSN_M1	RGMI1_RXD1_M1	I2S2_SDO_M0	GPIO1_D7_d	1H15	>>>I2S2_SDO_M0 [17]
RMII_RXER	PWM15_M1	SPI2_MISO_M1	UART4_CTSN_M1	RGMI1_RXER_M1	I2S2_SDI_M0	GPIO1_A0_d	1J15	>>>I2S2_SDI_M0 [17]
--	CLK1_32K_OUT	SPI2_CLK_M1	I2S0_SDO3_M1	ETH_CLK_25M_OUT_M1	I2S2_MCLK_M0	GPIO2_A1_d	W25	>>>I2S2_MCLK_M0 [17]

RK3562

RK3562 SARADC

U3M

SARADC0

BOOT/ Recovery/	SARADC0_BOOT	D26	SARADC0_BOOT	C127	1nF 50V C0201	GND	SARADC0_BOOT <<<SARADC0_BOOT [17]
	SARADC0_IN1	F26	SARADC0_IN1	C128	1nF 50V C0201	GND	SARADC0_IN1 <<<RECOVERY_KEY [17]
	SARADC0_IN2	E25	SARADC0_IN2	C129	1nF 50V C0201	GND	
	SARADC0_IN3	G26	SARADC0_IN3	C130	1nF 50V C0201	GND	
	SARADC0_IN4	D25	SARADC0_IN4	C131	1nF 50V C0201	GND	
	SARADC0_IN5	E26	SARADC0_IN5	C132	1nF 50V C0201	GND	
	SARADC0_IN6	E27	SARADC0_IN6	C133	1nF 50V C0201	GND	
	SARADC0_IN7	F25	SARADC0_IN7	C134	1nF 50V C0201	GND	

SARADC0_AVDD_1V8

OTP

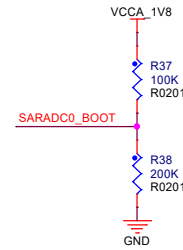
SARADC1

	SARADC1_IN0	1F1	SARADC1_IN0	C135	1nF 50V C0201	GND	
	SARADC1_IN1	1J1	SARADC1_IN1	C136	1nF 50V C0201	GND	
	SARADC1_IN2	1F2	SARADC1_IN2	C137	1nF 50V C0201	GND	
	SARADC1_IN3	1J2	SARADC1_IN3	C138	1nF 50V C0201	GND	
	SARADC1_IN4	1H1	SARADC1_IN4	C139	1nF 50V C0201	GND	
	SARADC1_IN5	1L2	SARADC1_IN5	C140	1nF 50V C0201	GND	
	SARADC1_IN6	1K1	SARADC1_IN6	C141	1nF 50V C0201	GND	
	SARADC1_IN7	1E1	SARADC1_IN7	C142	1nF 50V C0201	GND	

SARADC1_AVDD_1V8

RK3562

启动流程设置



BOARD ID设置



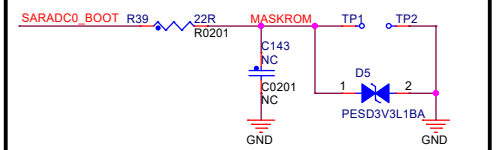
商业级:
BOARD ID: 0x00xx, CH2: 电压0.3V, ADC值170
工业级:
BOARD ID: 0x01xx, CH2: 电压0.507V, ADC值288

BOOT MODE CONFIG

Config Table for SARADC0_BOOT

Item	Rup	Rdown	ADC Value	Boot Mode
Config1	DNP	100K	0	USB (Maskrom mode)
Config2	100K	12K	114	Do Not Use
Config3	100K	27K	228	FSPI--USB
Config4	100K	51K	342	Do Not Use
Config5	100K	82K	456	Do Not Use
Config6	100K	120K	570	eMMC--USB
Config7	100K	200K	683	eMMC--SD Card--USB
Config8	100K	330K	796	SD Card--USB
Config9	100K	820K	910	Do Not Use
Config10	100K	NC	1023	FSPI--eMMC--SD Card--USB

MASKROM按键



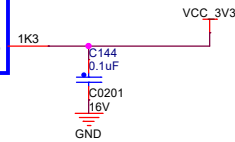
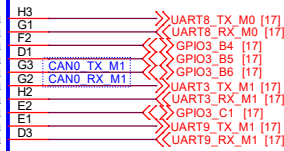
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Size A3	Document Number VCCIO4/SARADC	Rev V1.0
Date: Tuesday, December 03, 2024 Sheet 10 of 17		

U3K

VCCIO5 Domain
Operating Voltage=1.8V/3.3V

--	--	--	UART8_TX_M0	CAM_CLK0_OUT_M0	I2S1_MCLK_M1	GPIO3_B2_d
--	--	--	UART8_RX_M0	CAM_CLK1_OUT_M0	I2S1_SCLK_M1	GPIO3_B3_d
PWM12_M1	--	--	SPI0_MOSI_M1	UART8_CTSN_M0	I2S1_LRCK_M1	GPIO3_B4_d
PWM13_M1	--	--	SPI0_CLK_M1	UART8_RTSN_M0	CAM_CLK3_OUT	I2S1_SDO0_M1
--	I2C4_SCL_M0	--	SPI0_CSN1_M1	UART3_CTSN_M1	I2S1_SD13_M1	I2S1_SDO1_M1
--	I2C4_SDA_M0	--	SPI0_CSN0_M1	UART3_TX_M1	I2S1_SD12_M1	I2S1_SDO2_M1
--	--	--	SPI0_MISO_M1	UART3_RX_M1	I2S1_SD11_M1	I2S1_SDO3_M1
--	--	--	--	UART3_RTSN_M1	ISP_FLASH_TRIGIN	I2S1_SD10_M1
--	I2C5_SCL_M0	--	--	UART9_TX_M1	ISP_PHELIGHT_TRIGOUT	--
--	I2C5_SDA_M0	--	--	UART9_RX_M1	ISP_FLASH_TRIGOUT	--



RK3562

U3O

MIPI CSI RX0

MIPI_CSI_RX0_D0P	AF9	MIPI_CSI_RX0_D0P [17]
MIPI_CSI_RX0_D0N	AG9	MIPI_CSI_RX0_D0N [17]
MIPI_CSI_RX0_D1P	AF8	MIPI_CSI_RX0_D1P [17]
MIPI_CSI_RX0_D1N	AG8	MIPI_CSI_RX0_D1N [17]
MIPI_CSI_RX0_D2P	AF6	MIPI_CSI_RX0_D2P [17]
MIPI_CSI_RX0_D2N	AG6	MIPI_CSI_RX0_D2N [17]
MIPI_CSI_RX0_D3P	AF5	MIPI_CSI_RX0_D3P [17]
MIPI_CSI_RX0_D3N	AG5	MIPI_CSI_RX0_D3N [17]
MIPI_CSI_RX0_CLK0P	AF7	MIPI_CSI_RX0_CLK0P [17]
MIPI_CSI_RX0_CLK0N	AG7	MIPI_CSI_RX0_CLK0N [17]
MIPI_CSI_RX0_CLK1P	1P7	MIPI_CSI_RX0_CLK1P [17]
MIPI_CSI_RX0_CLK1N	1R7	MIPI_CSI_RX0_CLK1N [17]

MIPI_CSI_RX0_AVDD_0V9

MIPI_CSI_RX0_AVDD_1V8

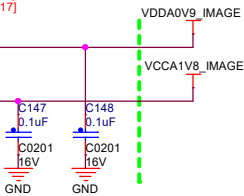
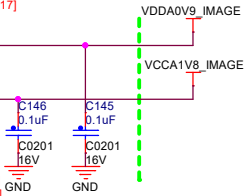
MIPI CSI RX1

MIPI_CSI_RX1_D0P	AF4	MIPI_CSI_RX1_D0P [17]
MIPI_CSI_RX1_D0N	AG4	MIPI_CSI_RX1_D0N [17]
MIPI_CSI_RX1_D1P	AF3	MIPI_CSI_RX1_D1P [17]
MIPI_CSI_RX1_D1N	AG3	MIPI_CSI_RX1_D1N [17]
MIPI_CSI_RX1_D2P	AE1	MIPI_CSI_RX1_D2P [17]
MIPI_CSI_RX1_D2N	AF1	MIPI_CSI_RX1_D2N [17]
MIPI_CSI_RX1_D3P	AD1	MIPI_CSI_RX1_D3P [17]
MIPI_CSI_RX1_D3N	AD2	MIPI_CSI_RX1_D3N [17]
MIPI_CSI_RX1_CLK0P	AF2	MIPI_CSI_RX1_CLK0P [17]
MIPI_CSI_RX1_CLK0N	AG2	MIPI_CSI_RX1_CLK0N [17]
MIPI_CSI_RX1_CLK1P	1P3	MIPI_CSI_RX1_CLK1P [17]
MIPI_CSI_RX1_CLK1N	1R3	MIPI_CSI_RX1_CLK1N [17]

MIPI_CSI_RX1_AVDD_0V9

MIPI_CSI_RX1_AVDD_1V8

RK3562



Usage of each MIPI CSI RXn & CLKn

Option1	Sensor1 x4Lane	MIPI_CSI_RXn_D0-3 MIPI_CSI_RXn_CLK0
Option2	Sensor1 x2Lane + Sensor2 x2Lane	MIPI_CSI_RXn_D0-1 MIPI_CSI_RXn_CLK0 MIPI_CSI_RXn_D2-3 MIPI_CSI_RXn_CLK1

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RK3562 MIPI DSI/LVDS

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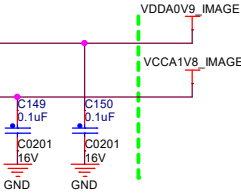
MIPI DSI TX/LVDS TX

LVDS_TX_D0P		MIPI_DSI_TX_D0P	AG14	>>>	MIPI_DSI_TX_D0P [17]
LVDS_TX_D0N		MIPI_DSI_TX_D0N	AF14	>>>	MIPI_DSI_TX_D0N [17]
LVDS_TX_D1P		MIPI_DSI_TX_D1P	AF13	>>>	MIPI_DSI_TX_D1P [17]
LVDS_TX_D1N		MIPI_DSI_TX_D1N	AG13	>>>	MIPI_DSI_TX_D1N [17]
LVDS_TX_D2P		MIPI_DSI_TX_D2P	AF11	>>>	MIPI_DSI_TX_D2P [17]
LVDS_TX_D2N		MIPI_DSI_TX_D2N	AG11	>>>	MIPI_DSI_TX_D2N [17]
LVDS_TX_D3P		MIPI_DSI_TX_D3P	AG10	>>>	MIPI_DSI_TX_D3P [17]
LVDS_TX_D3N		MIPI_DSI_TX_D3N	AF10	>>>	MIPI_DSI_TX_D3N [17]
LVDS_TX_CLKP		MIPI_DSI_TX_CLKP	AG12	>>>	MIPI_DSI_TX_CLKP [17]
LVDS_TX_CLKN		MIPI_DSI_TX_CLKN	AF12	>>>	MIPI_DSI_TX_CLKN [17]

MIPI_DSI_TX/LVDS_TX_AVDD_0V9

MIPI_DSI_TX/LVDS_TX_AVDD_1V8

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RK3562 VCCIO6

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VCCIO6 Domain

Operating Voltage=1.8V/3.3V

--	--	--	UART7_TX_M0	I2S1_MCLK_M0	VO_LCDC_D3	GPIO3_C4_d	N1	>>>	UART7_TX_M0 [17]
--	--	--	UART4_CTSN_M0	I2S1_SCLK_M0	VO_LCDC_D4	GPIO3_C5_d	N2	>>>	GPIO3_C5 [17]
--	--	--	UART4_RTSN_M0	I2S1_LRCK_M0	VO_LCDC_D5	GPIO3_C6_d	P3	>>>	GPIO3_C6 [17]
--	--	--	UART7_RX_M0	I2S1_SDO0_M0	VO_LCDC_D6	GPIO3_C7_d	R2	>>>	UART7_RX_M0 [17]
--	--	--	UART4_TX_M0	I2S1_SDI0_M0	VO_LCDC_D7	GPIO3_D0_d	R3	>>>	UART4_TX_M0 [17]
UART3_RTSN_M0	--	--	UART4_RX_M0	I2S1_SDI1_M0	VO_LCDC_D10	GPIO3_D1_d	R1	>>>	UART4_RX_M0 [17]
--	I2C2_SCL_M1	SPI2_MISO_M0	UART7_CTSN_M0	I2S1_SDI2_M0	VO_LCDC_D11	GPIO3_D2_d	R2	>>>	I2C2_SCL_M1 [17]
--	I2C2_SDA_M1	SPI2_MOSI_M0	UART7_RTSN_M0	I2S1_SDI3_M0	VO_LCDC_D12	GPIO3_D3_d	T3	>>>	I2C2_SDA_M1 [17]
I2S2_SDI_M1	--	--	UART8_TX_M1	RGMI1_TXD2_M0	VO_LCDC_D13	GPIO3_D4_d	AB3	>>>	RGMI1_TXD2_M0 [16]
I2S2_SDO_M1	--	--	UART8_RX_M1	RGMI1_TXD3_M0	VO_LCDC_D14	GPIO3_D5_d	AB2	>>>	RGMI1_TXD3_M0 [16]
I2S2_MCLK_M1	--	--	SPI1_CLK_M0	RGMI1_TXCLK_M0	VO_LCDC_D15	GPIO3_D6_d	Y2	>>>	RGMI1_TXCLK_M0 [16]
--	--	--	SPI1_CSN0_M0	UART8_CTSN_M1	VO_LCDC_D19	GPIO3_D7_d	W2	>>>	RGMI1_RXD2_M0 [16]
--	--	--	SPI1_CSN1_M0	UART8_RTSN_M1	VO_LCDC_D20	GPIO4_A0_d	R3	>>>	RGMI1_RXD3_M0 [16]
I2S2_LRCK_M1	PWM12_M0	--	SPI1_MOSI_M0	RGMI1_RXCLK_M0	VO_LCDC_D21	GPIO4_A1_d	U2	>>>	RGMI1_RXCLK_M0 [16]
UART6_CTSN_M1	--	--	SPI1_MISO_M0	RGMI1_TXD0_M0	VO_LCDC_D22	GPIO4_A2_d	AA1	>>>	RGMI1_TXD0_M0 [16]
UART6_RTSN_M1	--	--	--	RGMI1_TXD1_M0	VO_LCDC_D23	GPIO4_A3_d	AA2	>>>	RGMI1_TXD1_M0 [16]
--	PWM13_M0	--	--	RGMI1_TXEN_M0	VO_LCDC_D0	GPIO4_A4_d	AA3	>>>	RGMI1_TXEN_M0 [16]
--	I2C3_SCL_M1	PDM_SD12_M1	UART1_TX_M1	RGMI1_RXD0_M0	VO_LCDC_D1	GPIO4_A5_d	W3	>>>	RGMI1_RXD0_M0 [16]
--	I2C3_SDA_M1	PDM_SD13_M1	UART1_RX_M1	RGMI1_RXD1_M0	VO_LCDC_D8	GPIO4_A6_d	W1	>>>	RGMI1_RXD1_M0 [16]
UART6_TX_M1	--	PDM_SD10_M1	UART1_RTSN_M1	RGMI1_RXD2_M0	VO_LCDC_D9	GPIO4_A7_d	V2	>>>	RGMI1_RXD2_M0 [16]
UART6_RX_M1	--	PDM_SD11_M1	UART1_CTSN_M1	RGMI1_RXD3_M0	VO_LCDC_D16	GPIO4_B0_d	V3	>>>	ETH_PHY_INT [16]
I2S2_SCLK_M1	--	CAM_CLK0_OUT_M1	PDM_CLK1_M1	ETH_CLK_25M_OUT_M0	VO_LCDC_D17	GPIO4_B1_d	T2	>>>	ETH_PHY_RSTn [16]
--	--	--	--	UART9_TX_M0	VO_LCDC_D2	GPIO4_B2_d	AC1	>>>	RGMI1_MDC_M0 [16]
--	--	--	--	UART9_RX_M0	VO_LCDC_D18	GPIO4_B3_d	AC2	>>>	RGMI1_MDIO_M0 [16]
UART3_TX_M0	I2C1_SCL_M1	SPI2_CSN1_M0	UART9_CTSN_M0	I2S1_SDO1_M0	VO_LCDC_HSYNC	GPIO4_B4_d	M2	>>>	I2C1_SCL_M1 [17]
UART3_RX_M0	I2C1_SDA_M1	SPI2_CSN0_M0	UART9_RTSN_M0	I2S1_SDO2_M0	VO_LCDC_VSYNC	GPIO4_B5_d	M3	>>>	I2C1_SDA_M1 [17]
UART3_CTSN_M0	--	SPI2_CLK_M0	--	I2S1_SDO3_M0	VO_LCDC_DEN	GPIO4_B6_d	N3	>>>	GPIO4_B6 [17]
--	CAM_CLK1_OUT_M1	PDM_CLK0_M1	--	RGMI1_CLK_M0	VO_LCDC_CLK	GPIO4_B7_d	U1	>>>	RGMI1_CLK_M0 [16]

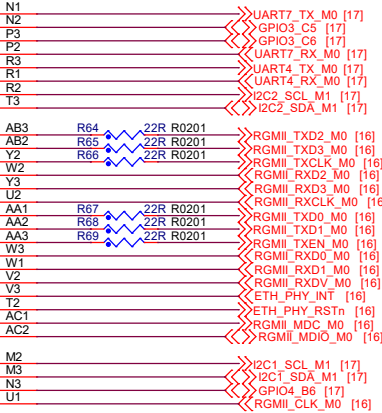
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VCCIO6_1
VCCIO6_2

1M4

1L5

VCC_3V3



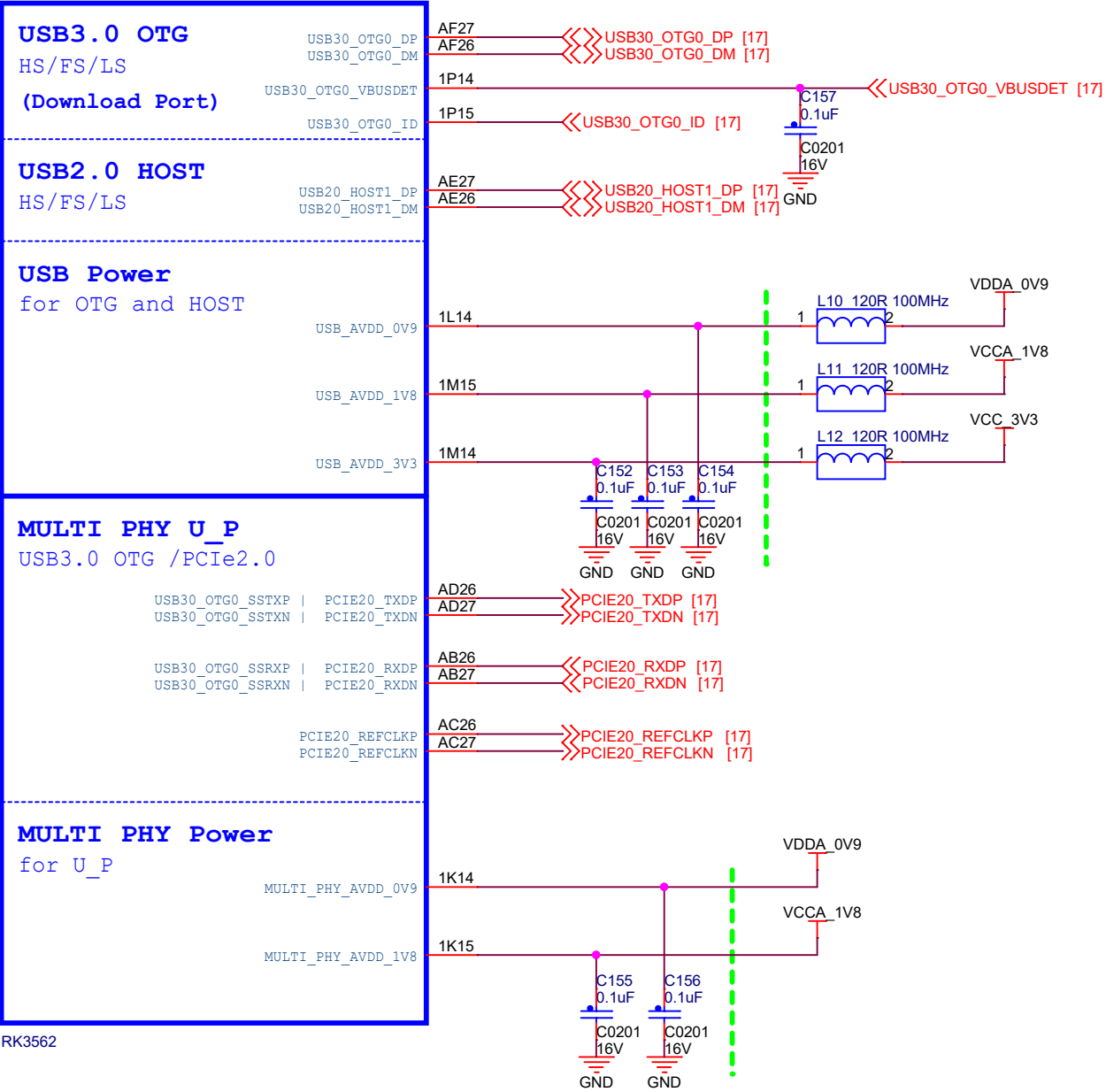
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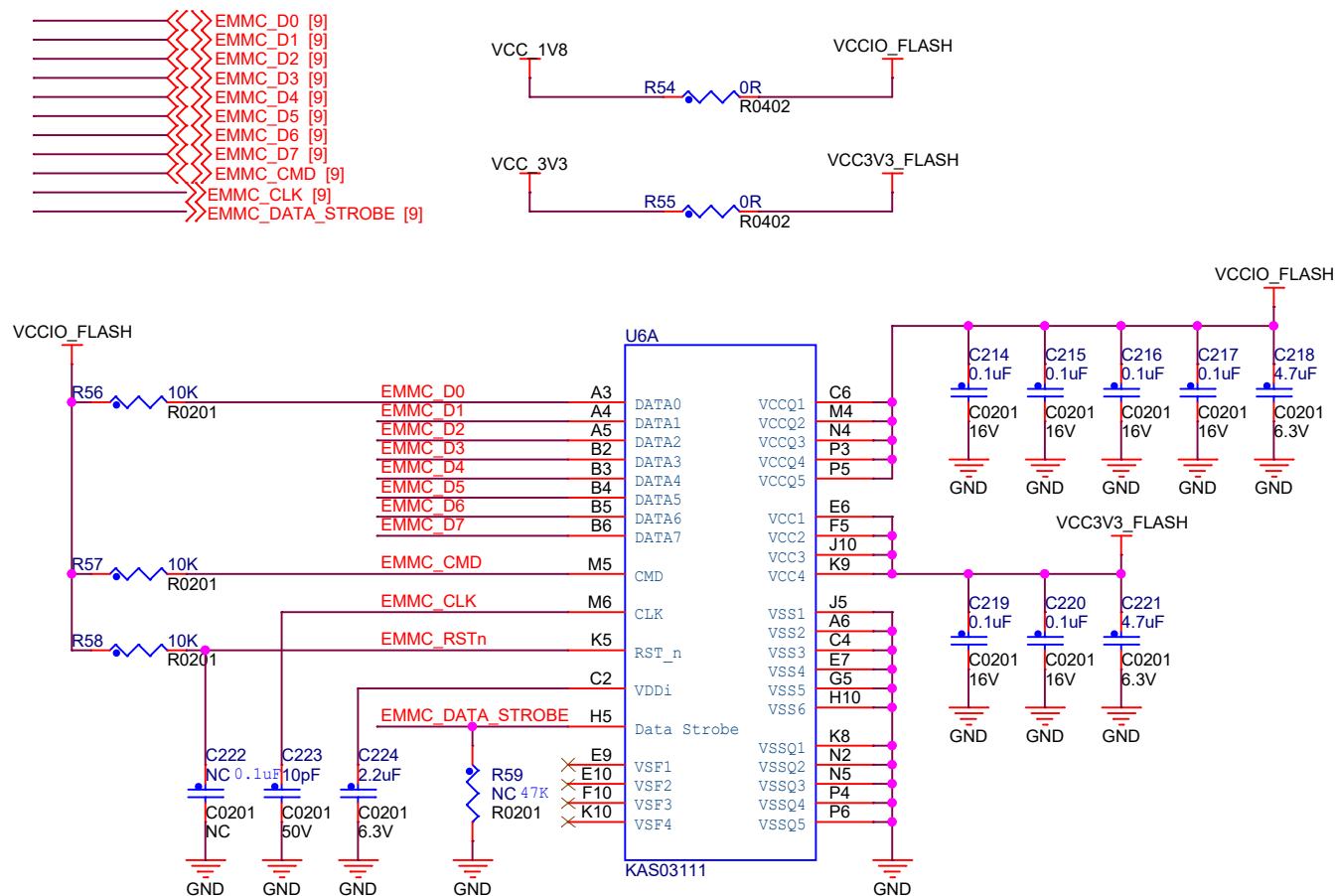
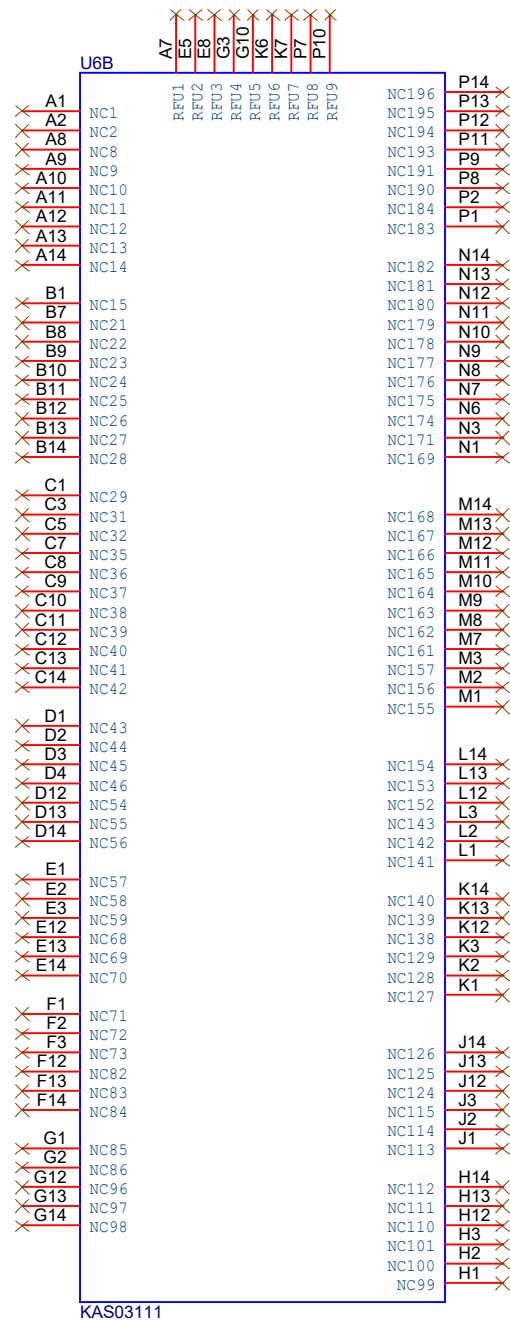


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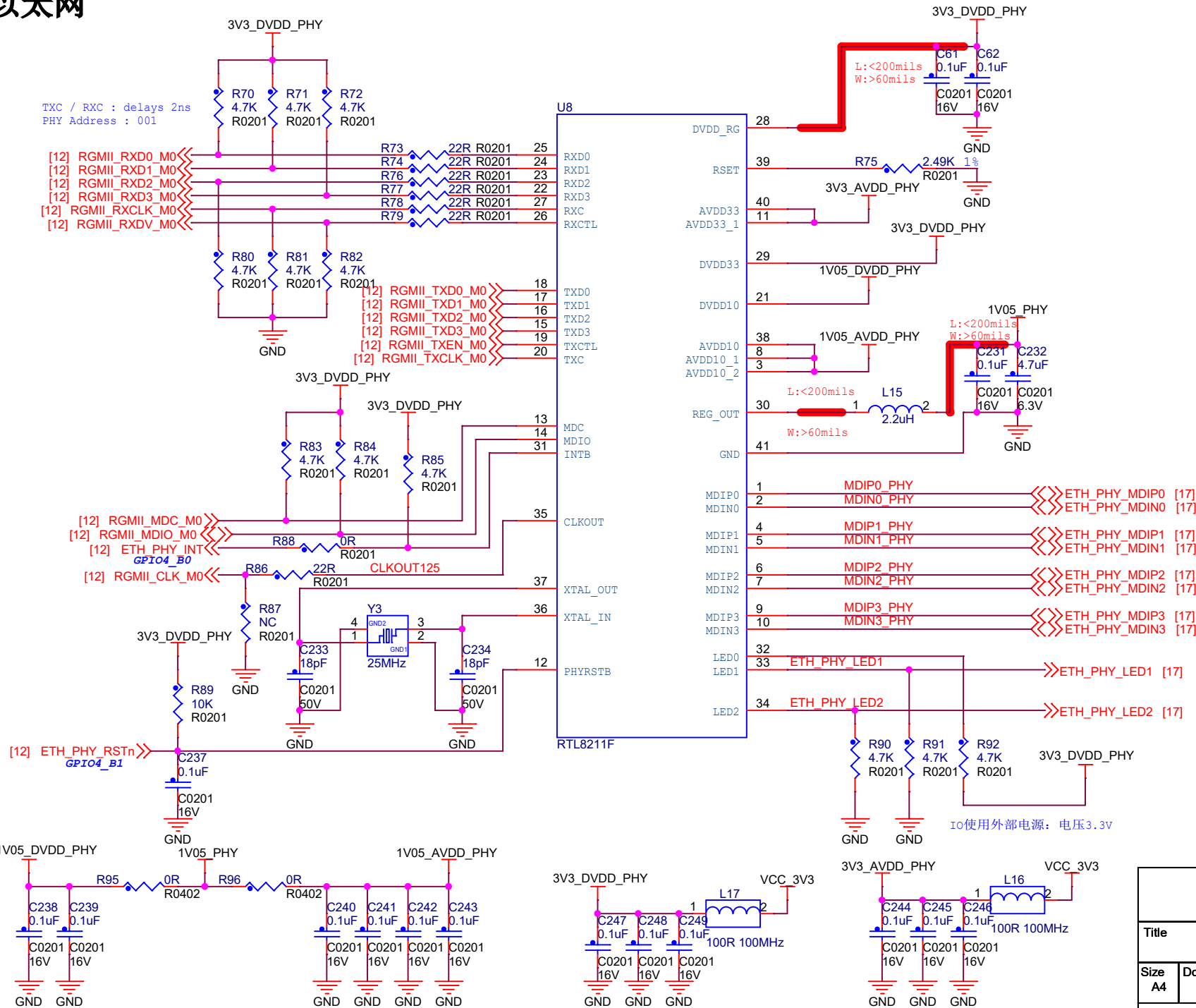
EMMC FLASH



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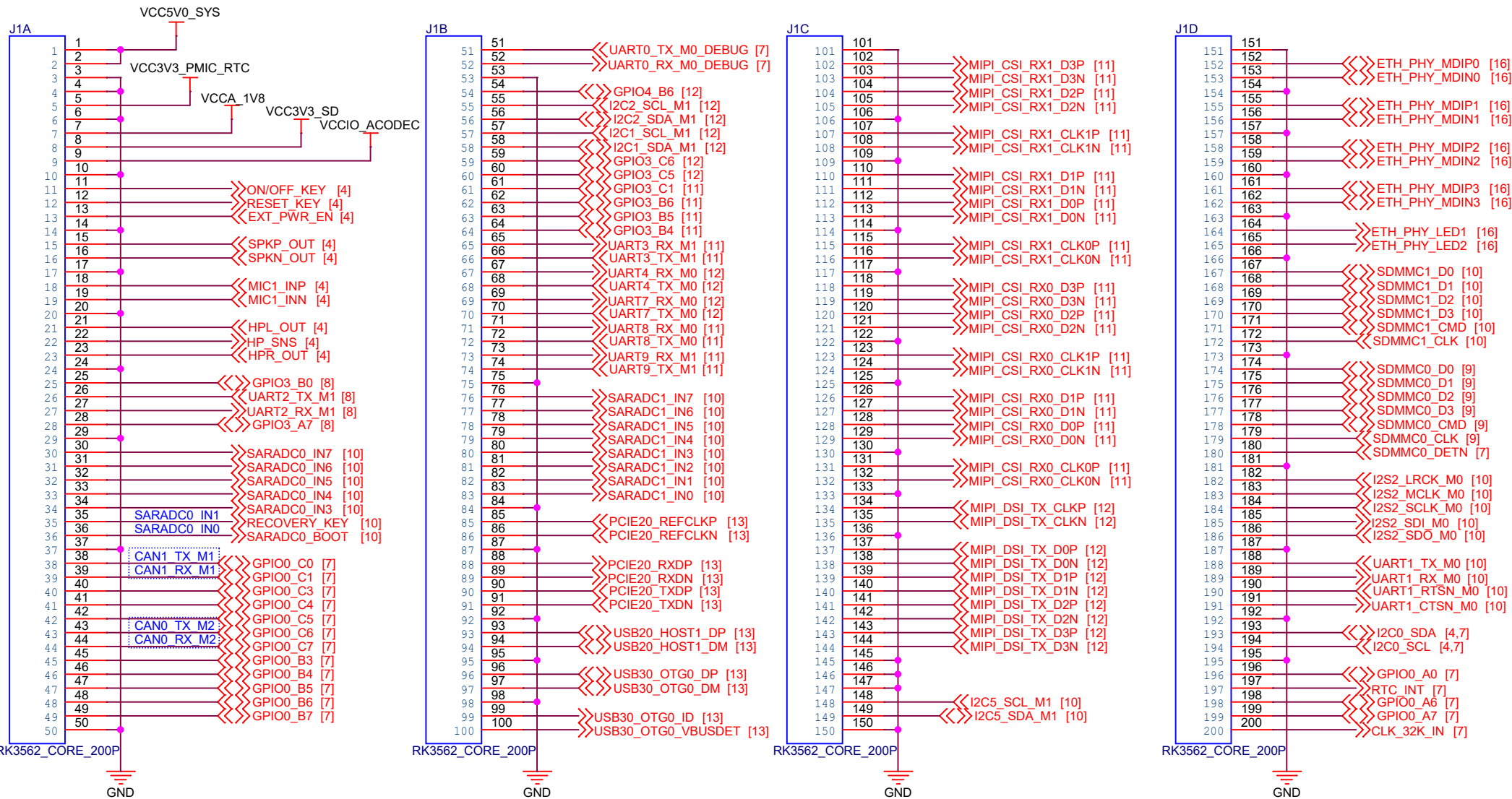
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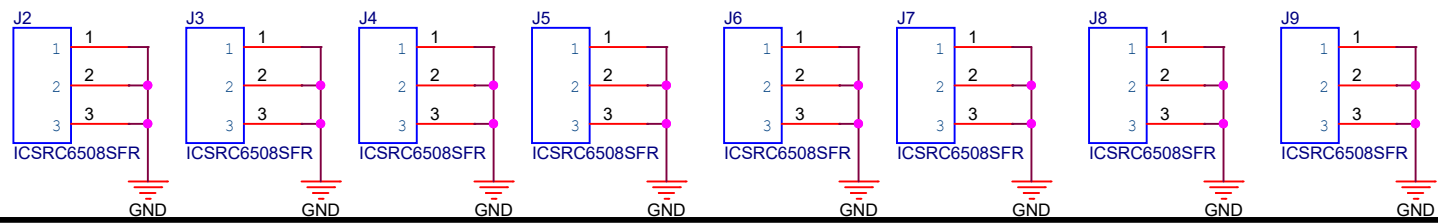
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200PIN 邮票孔



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