

野火 EBF RK3568 LBC

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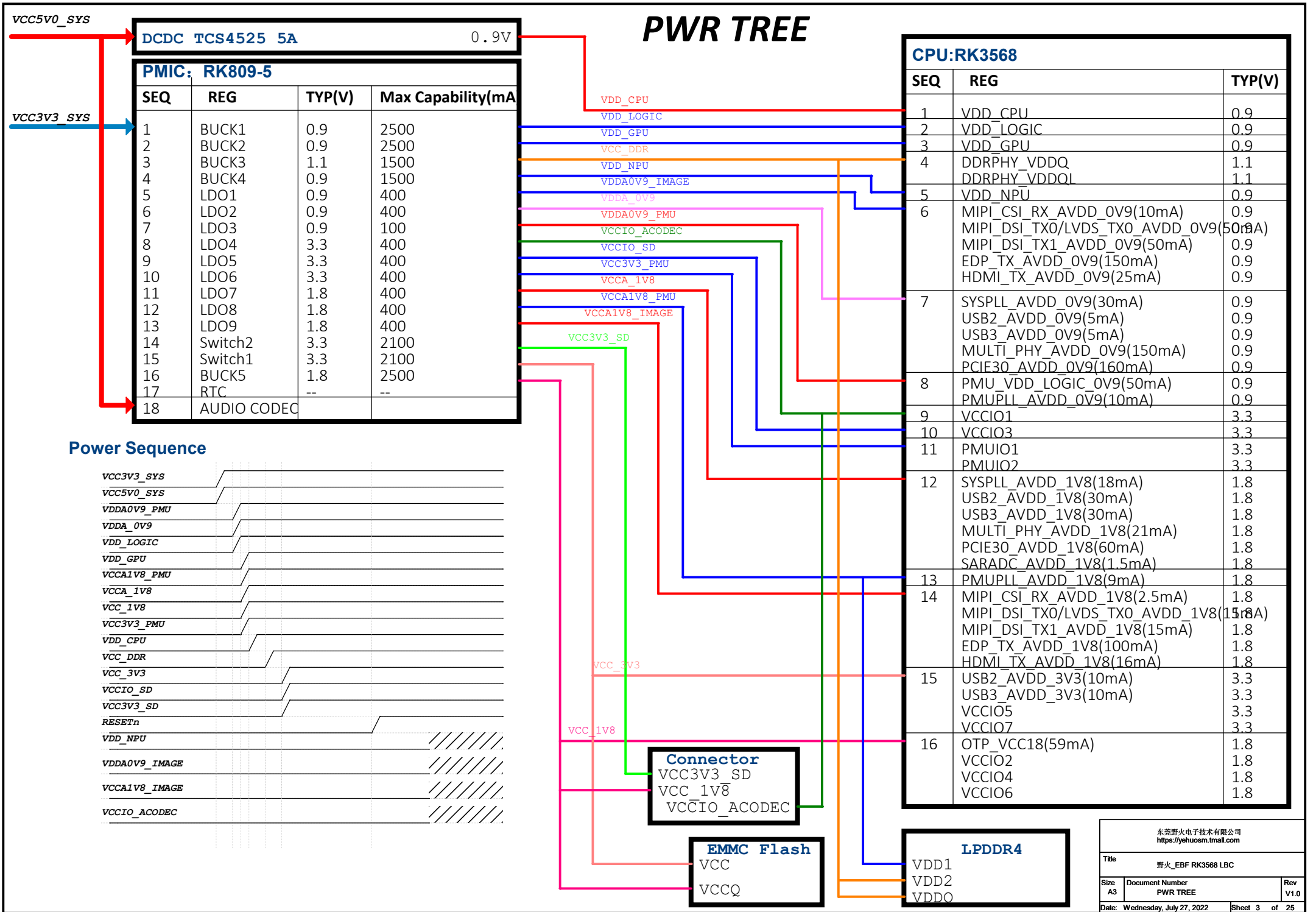
Title
野火_EBF RK3568 LBC

Size A4	Document Number 目录	Rev V1.0
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Date: Tuesday, November 29, 2022 Sheet 1 of 25

历史版本

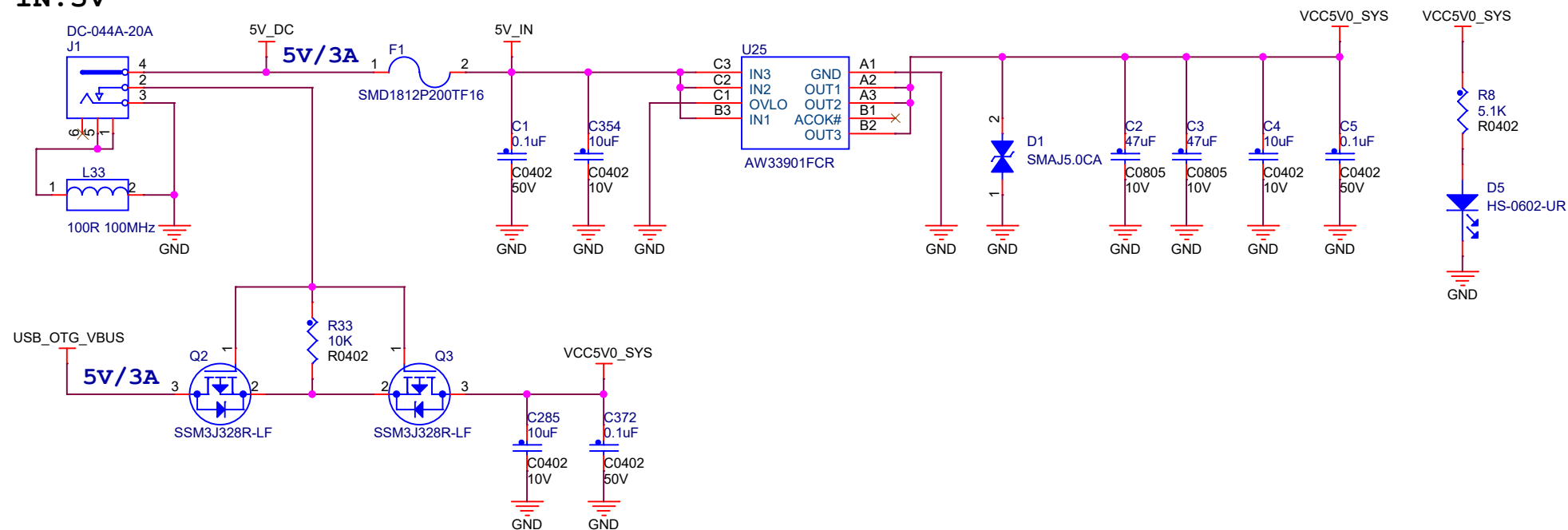
版本号	日期	设计	描述
V1.0	2022-01-25	llm	初始版本, 开发阶段
V1.1	2022-05-18	llm	修复初始版本的一些问题，增加SATA接口， typec添加OTG自动识别host/device功能
V1.2	2022-07-27	llm	1.MIPI CSI 添加2 Lanes 2.更新MINI PCIE、M.2 PCIE封装，添加独立的定位孔 3.把Z4、Z3、Z2、Z1位号改前缀为D的位号 4.把sim卡座位号U18改为J18 5.把mini pcie位号CON1改为J17
V1.2	2022-11-29	llm	添加LPDDR4x注释，电路不做任何更改



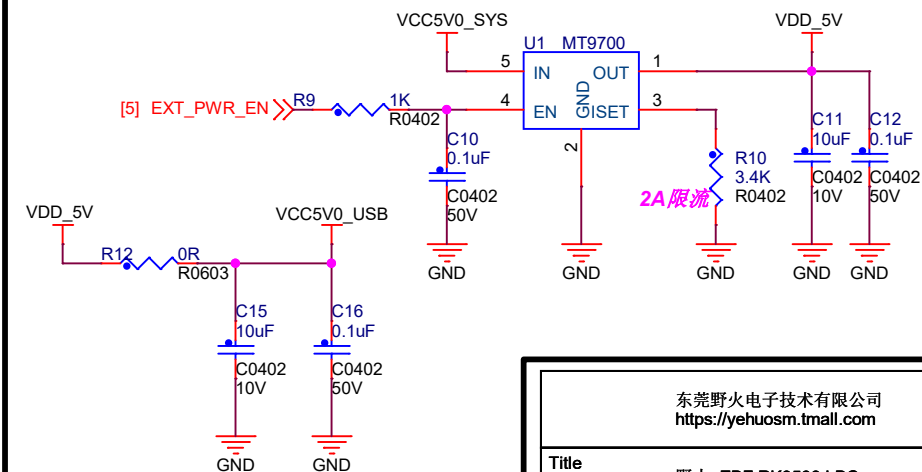
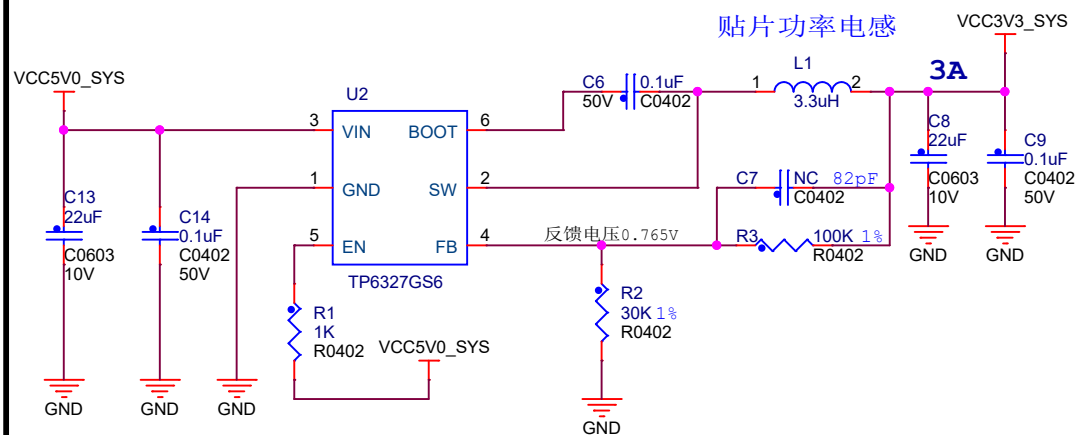
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Title 野火_EBF RK3568 LBC		
Size A3	Document Number PWR TREE	Rev V1.0
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DC IN: 5V



5V转3.3V



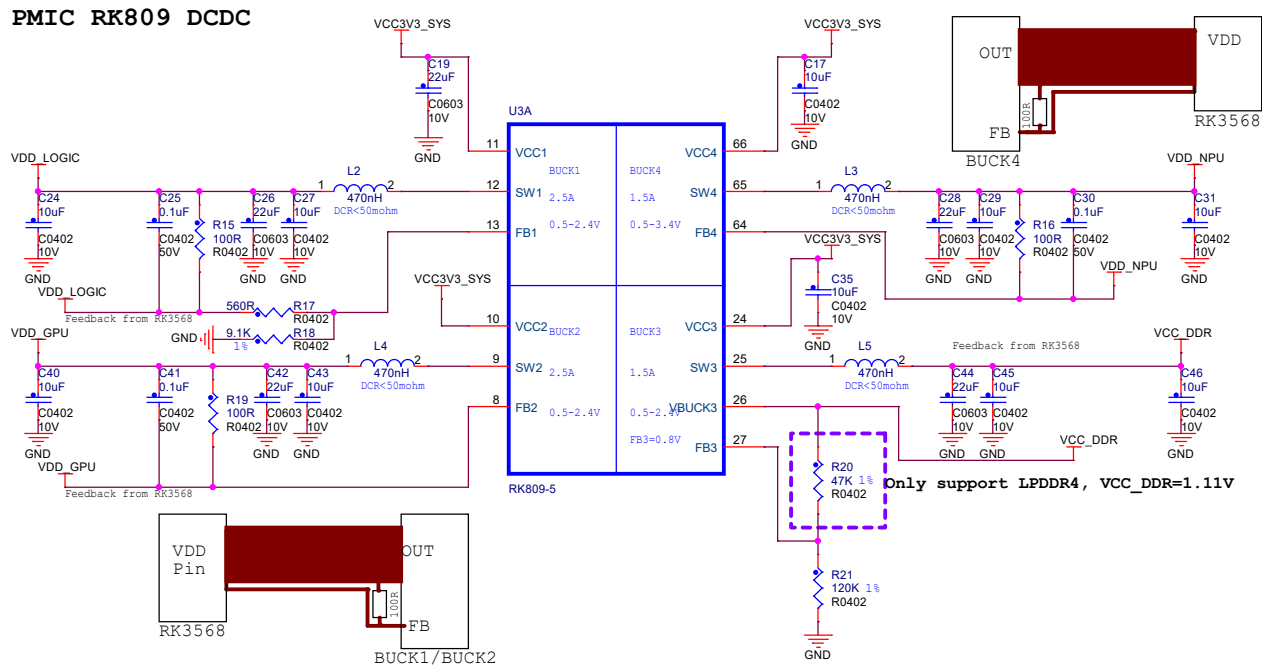
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Title	野火_EBF RK3568 LBC
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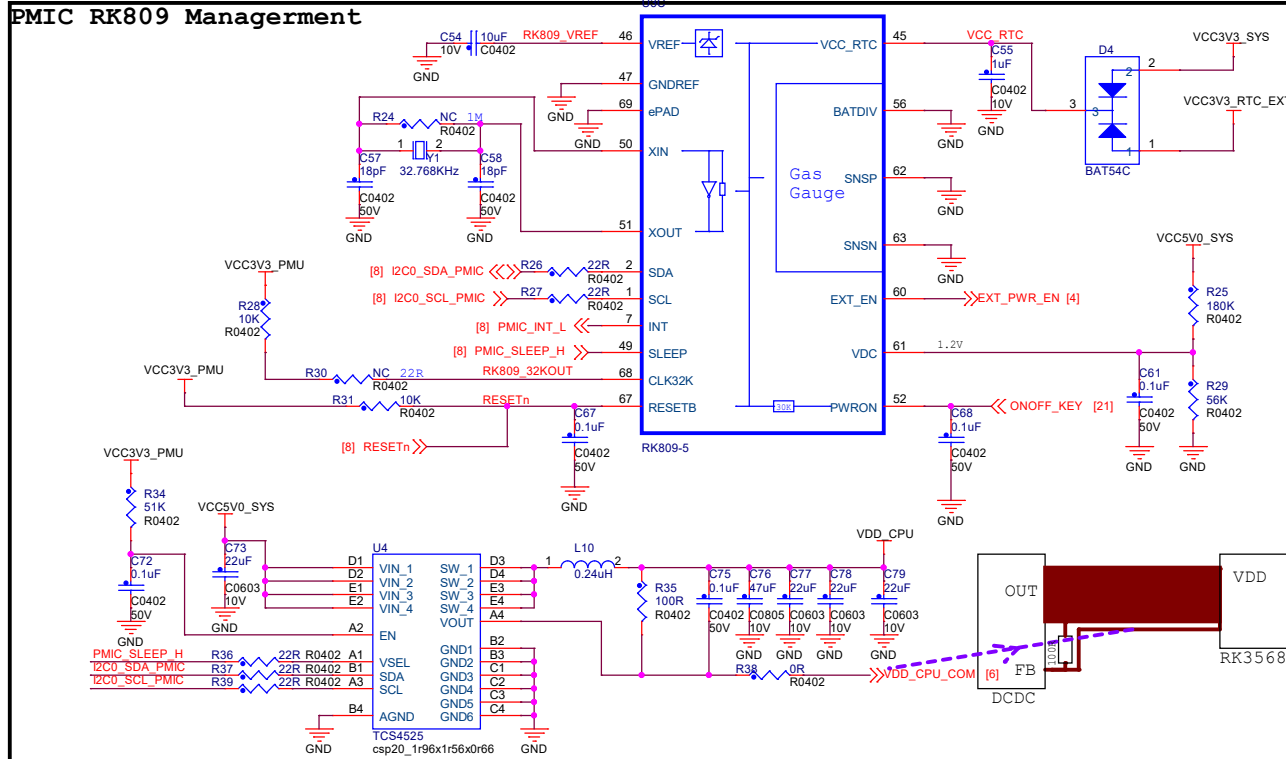
Size A4	Document Number 电源	Rev V1.0
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Date: Wednesday, July 27, 2022 Sheet 4 of 25

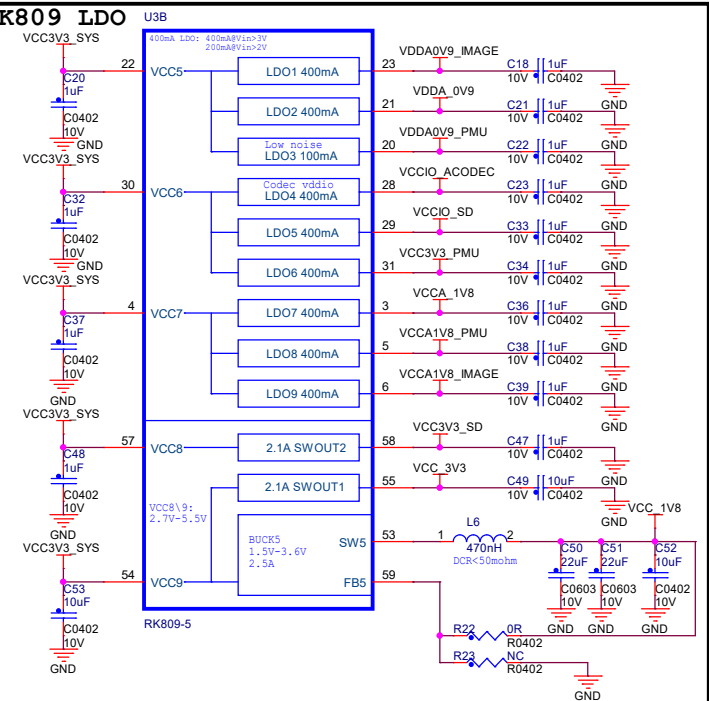
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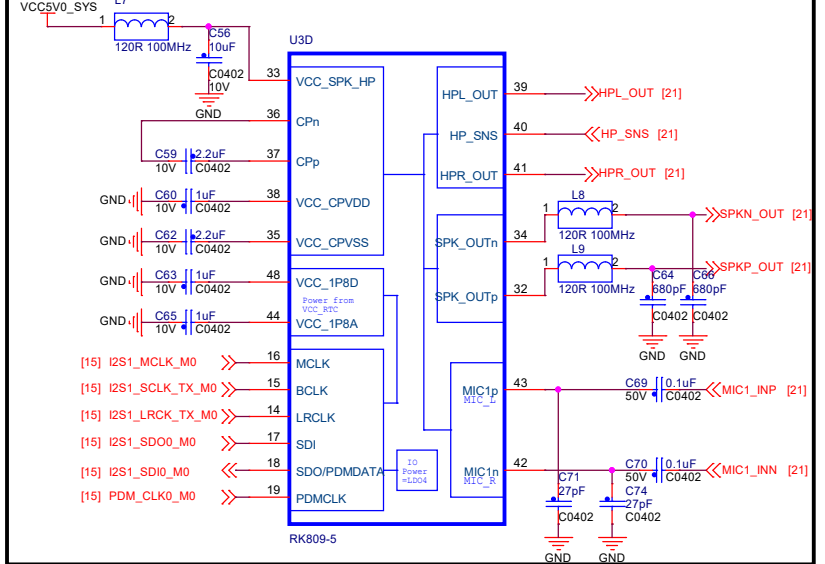
PMIC RK809 Managerment



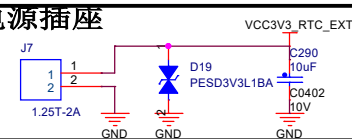
PMIC RK809 LDO



PMIC RK809 CODEC



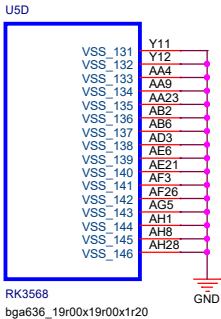
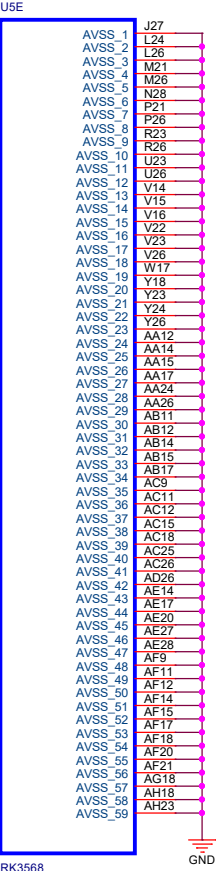
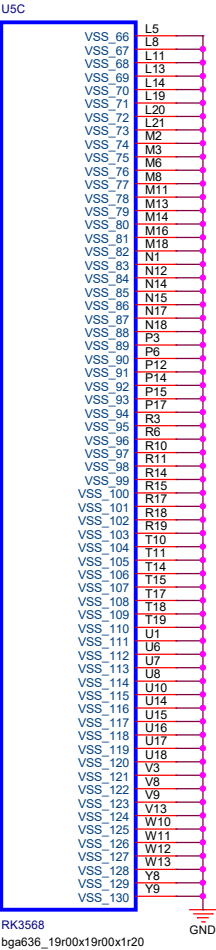
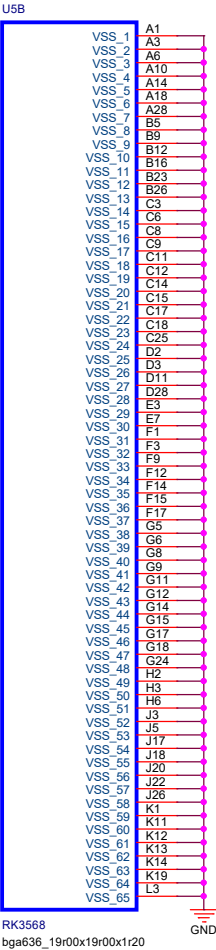
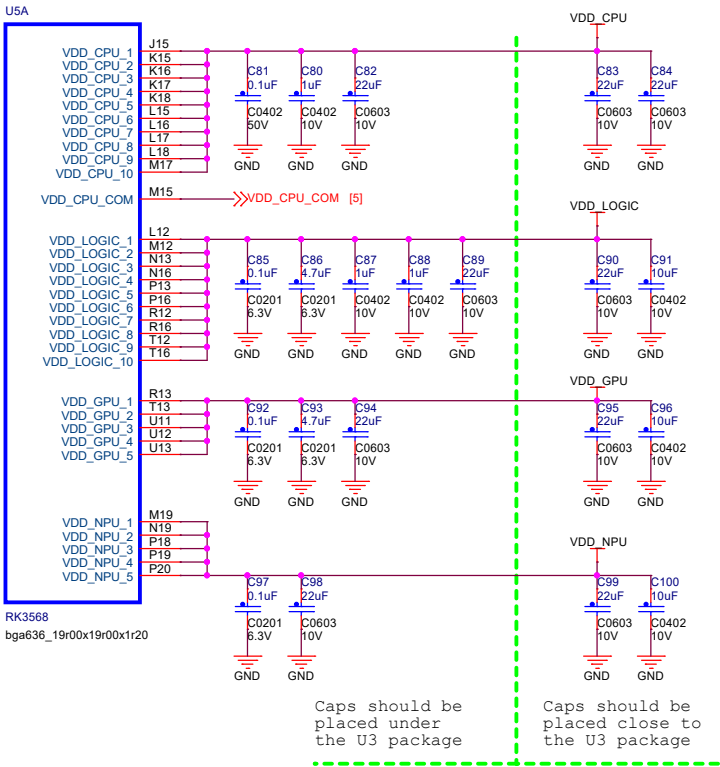
RTC电源插座



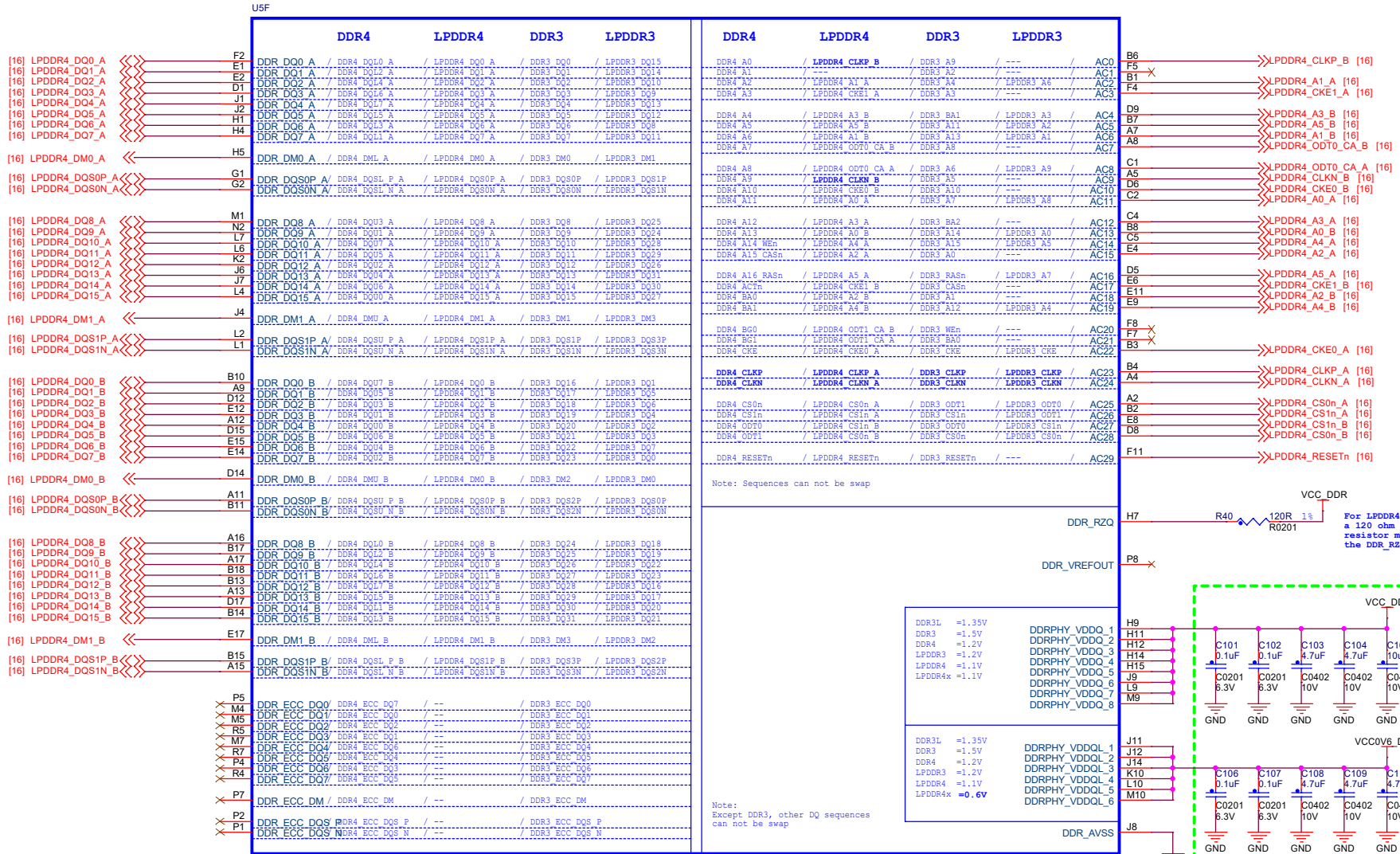
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Title		
野火_EBF RK3568 LBC		
Size	Document Number	Rev
A3	SYS PMIC	V1.0
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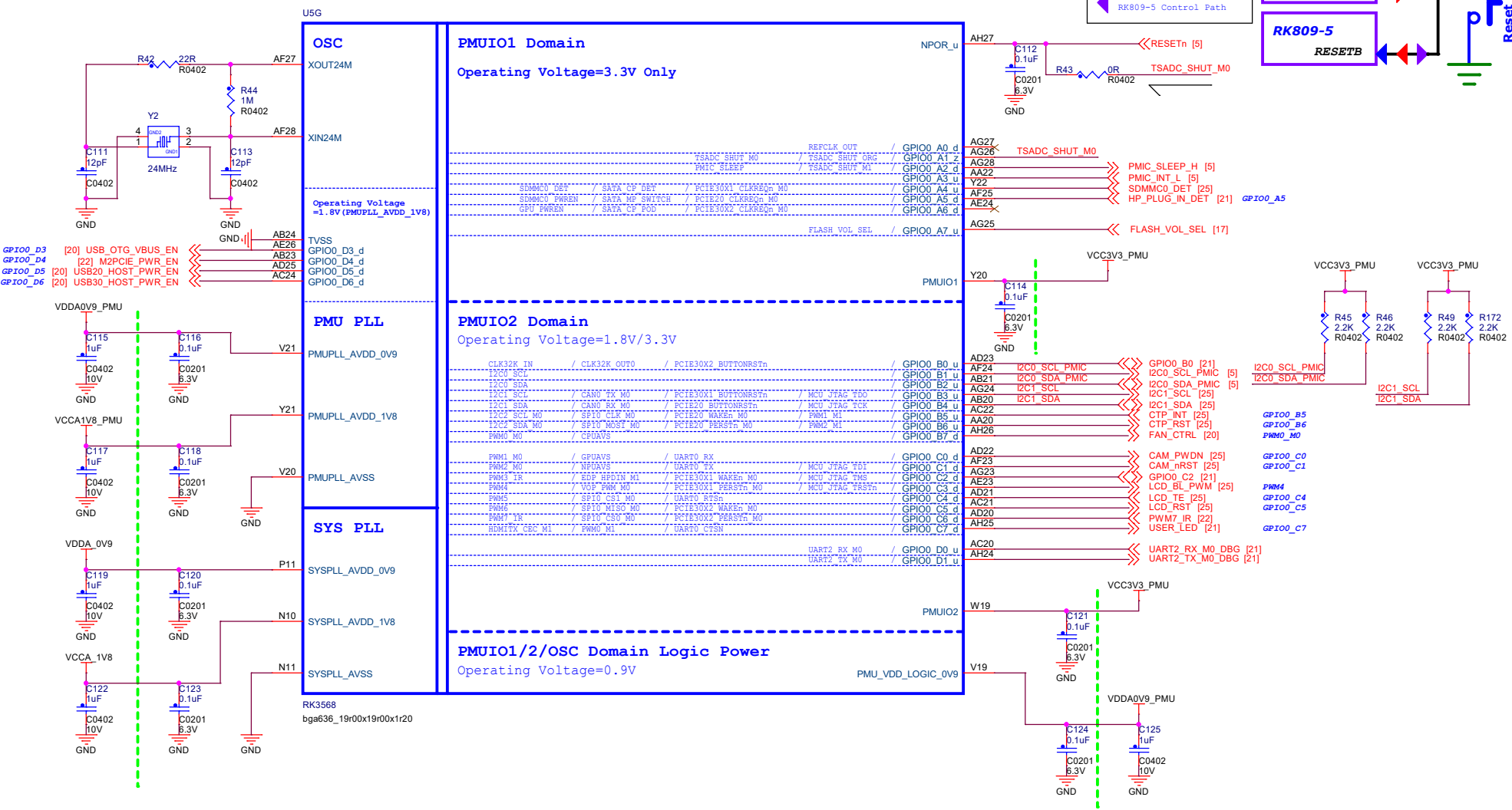
RK3568 PWR



RK3568 DDR PHY

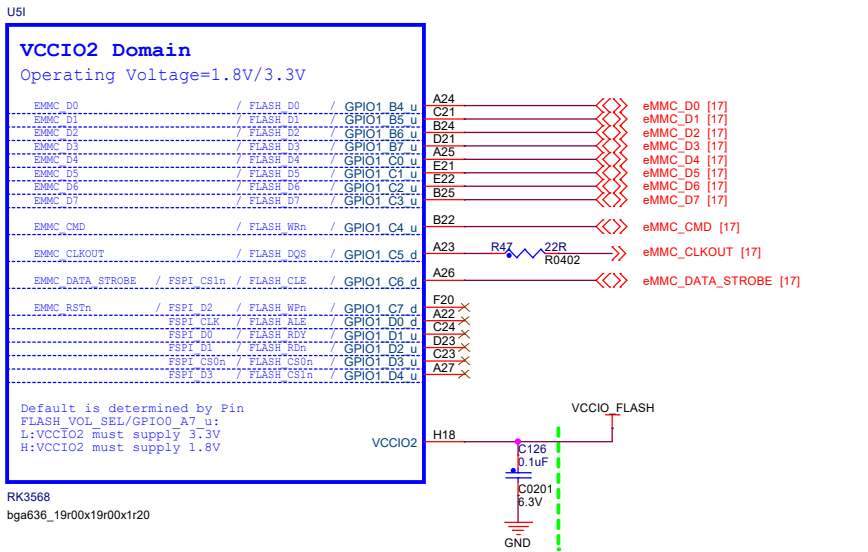


RK3568 OSC/PLL/PMUIO

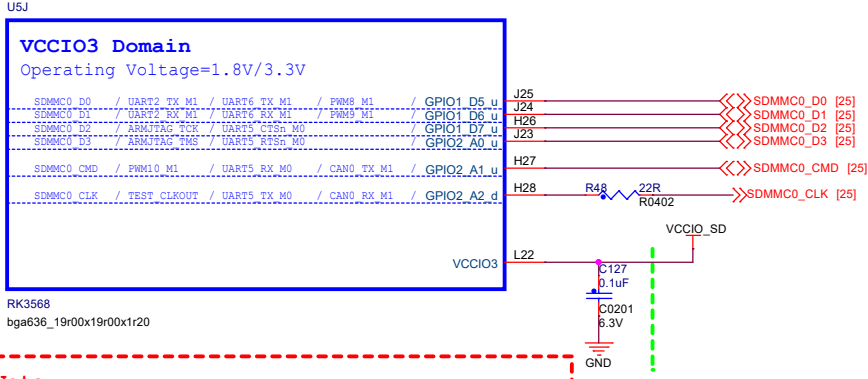


Note:
Caps of between dashed green lines
should be placed under the U3 package.
Other caps should be placed close to the U3 package

RK3568 VCCIO2 Domain

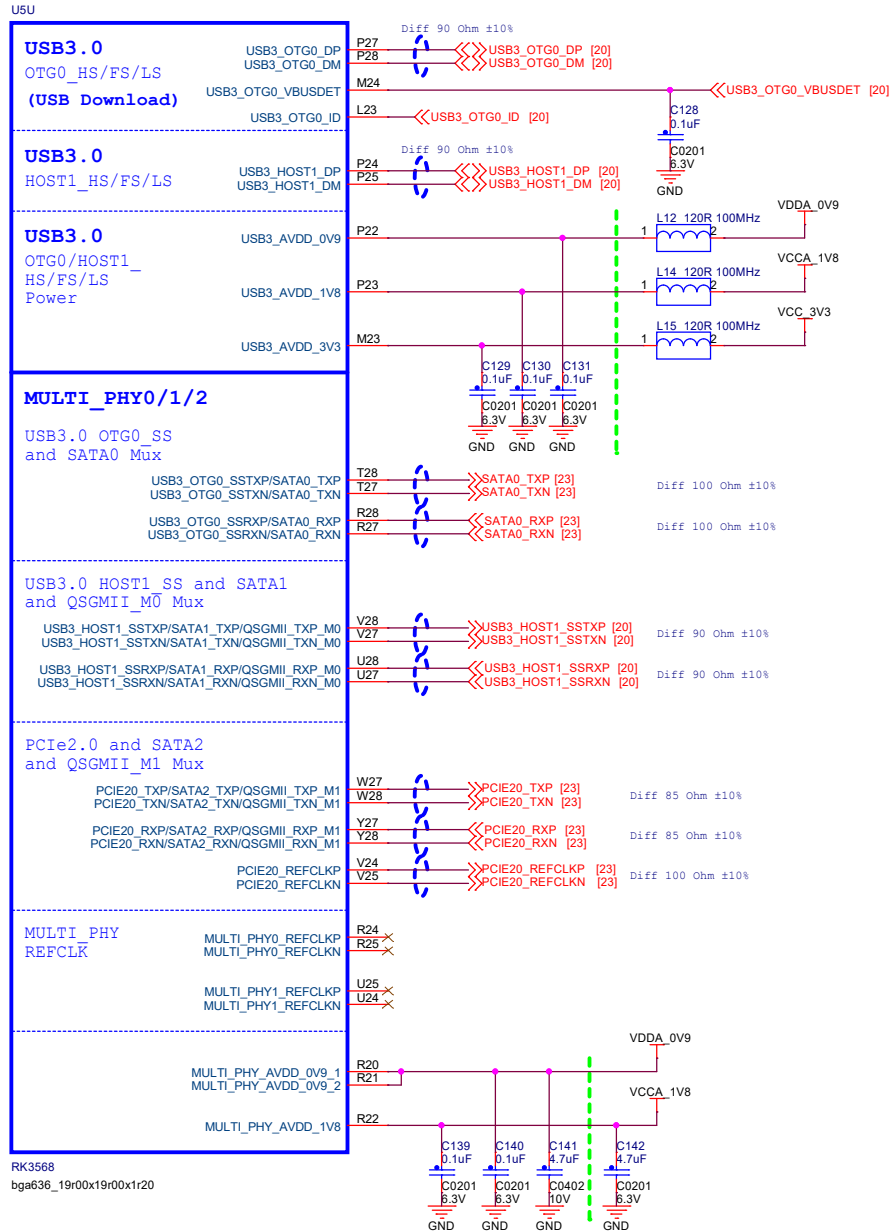


RK3568 VCCIO3 Domain



Note:
Caps of between dashed green lines and U3 should be placed under the U3 package.
Other caps should be placed close to the U3 package

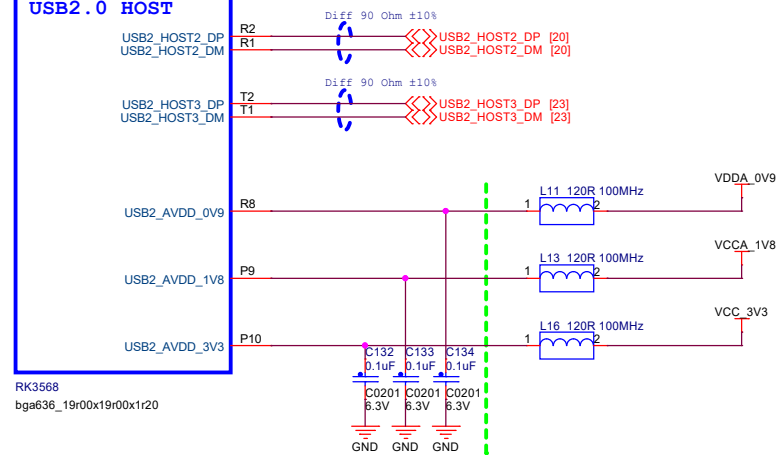
RK3568 USB3.0/SATA/QSGMII/PCIE2.0 x1



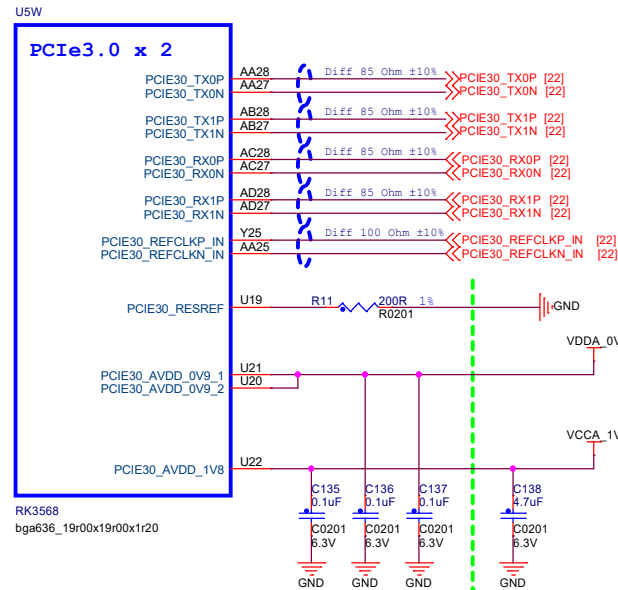
Note:

Caps of between dashed green lines and U3 should be placed under the U3 package.
Other caps should be placed close to the U3 package

RK3568 USB2.0 HOST



RK3568 PCIE3.0 x2



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Title 野火_EBF RK3568 LBC		
Size A3	Document Number USB/PCle/STAT PHY	Rev V1.0
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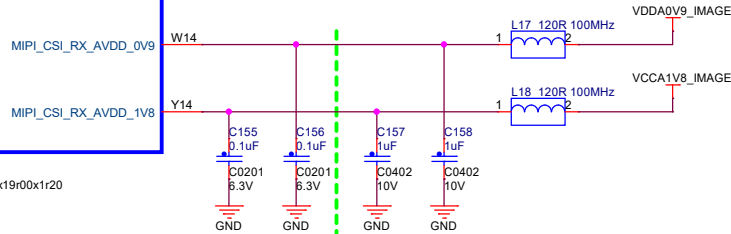
RK3568 MIPI_CSI_RX

U5P

MIPI CSI RX

MIPI_CSI_RX_D0P	AG12	Diff 100 Ohm ±10%	MIPI_CSI_RX_D0P [25]
MIPI_CSI_RX_D0N	AH12		MIPI_CSI_RX_D0N [25]
MIPI_CSI_RX_D1P	AG11	Diff 100 Ohm ±10%	MIPI_CSI_RX_D1P [25]
MIPI_CSI_RX_D1N	AH11		MIPI_CSI_RX_D1N [25]
MIPI_CSI_RX_D2P	AE11	Diff 100 Ohm ±10%	MIPI_CSI_RX_D2P [25]
MIPI_CSI_RX_D2N	AD11		MIPI_CSI_RX_D2N [25]
MIPI_CSI_RX_D3P	AD9	Diff 100 Ohm ±10%	MIPI_CSI_RX_D3P [25]
MIPI_CSI_RX_D3N	AE9		MIPI_CSI_RX_D3N [25]
MIPI_CSI_RX_CLK0P	AG10	Diff 100 Ohm ±10%	MIPI_CSI_RX_CLK0P [25]
MIPI_CSI_RX_CLK0N	AH10		MIPI_CSI_RX_CLK0N [25]
MIPI_CSI_RX_CLK1P	AG9		
MIPI_CSI_RX_CLK1N	AH9		

Option1	Sensor1 x4Lane	MIPI_CSI_RX_D0-3 MIPI_CSI_RX_CLK0
Option2	Sensor1 x2Lane + Sensor2 x2Lane	MIPI_CSI_RX_D0-1 MIPI_CSI_RX_CLK0 MIPI_CSI_RX_D2-3 MIPI_CSI_RX_CLK1



RK3568
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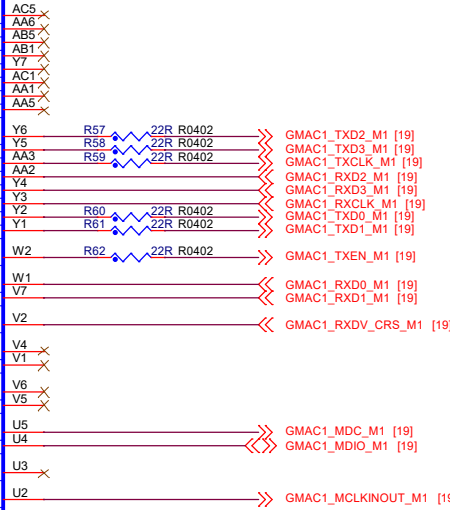
RK3568 VCCIO6 Domain

U5M

VCCIO6 Domain

Operating Voltage=1.8V/3.3V

CIF D0	/ EBC SDD00	/ SDMMC2 D0 M0	/ I2S1 MCLK M1	/ VOP BT656 D0 M1	/ GPIO3 C6 d
CIF D1	/ EBC SDD01	/ SDMMC2 D1 M0	/ I2S1 SCLK TX M1	/ VOP BT656 D1 M1	/ GPIO3 C7 d
CIF D2	/ EBC SDD02	/ SDMMC2 D2 M0	/ I2S1 LCLK TX M1	/ VOP BT656 D2 M1	/ GPIO3 D0 d
CIF D3	/ EBC SDD03	/ SDMMC2 D3 M0	/ I2S1 SDO0 M1	/ VOP BT656 D3 M1	/ GPIO3 D1 d
CIF D4	/ EBC SDD04	/ SDMMC2 D4 M0	/ I2S1 SDO1 M1	/ VOP BT656 D4 M1	/ GPIO3 D2 d
CIF D5	/ EBC SDD05	/ SDMMC2 D5 M0	/ I2S1 SDO2 M1	/ VOP BT656 D5 M1	/ GPIO3 D3 d
CIF D6	/ EBC SDD06	/ SDMMC2 D6 M0	/ I2S1 SDO3 M1	/ VOP BT656 D6 M1	/ GPIO3 D4 d
CIF D7	/ EBC SDD07	/ SDMMC2 D7 M0	/ I2S1 SDO4 M1	/ VOP BT656 D7 M1	/ GPIO3 D5 d
CIF D8	/ EBC SDD08	/ GMAC1 TXD2 M1	/ UART1 TX M1	/ PDM CLK0 M1	/ GPIO3 D6 d
CIF D9	/ EBC SDD09	/ GMAC1 TXD3 M1	/ UART1 RX M1	/ PDM SDO1 M1	/ GPIO3 D7 d
CIF D10	/ EBC SDD010	/ GMAC1 RXD0 M1	/ UART1 TX M2	/ PDM CLK1 M1	/ GPIO4 A0 d
CIF D11	/ EBC SDD011	/ GMAC1 RXD1 M1	/ UART1 RX M2	/ PDM SDO2 M1	/ GPIO4 A1 d
CIF D12	/ EBC SDD012	/ GMAC1 RXD2 M1	/ UART1 TX M2	/ PDM SDO3 M1	/ GPIO4 A2 d
CIF D13	/ EBC SDD013	/ GMAC1 RXD3 M1	/ UART1 RX M2	/ I2S2 SDO0 M1	/ GPIO4 A3 d
CIF D14	/ EBC SDD014	/ GMAC1 TXD0 M1	/ UART1 TX M2	/ I2S2 LCLK RX M1	/ GPIO4 A4 d
CIF D15	/ EBC SDD015	/ GMAC1 TXD1 M1	/ UART1 RX M2	/ I2S2 LCLK RX M1	/ GPIO4 A5 d
ISP FLASHTRIGOUT	/ EBC SDCE0	/ GMAC1 TXEN M1	/ SPI3 CS0 M0	/ I2S1 SCLK RX M1	/ GPIO4 A6 d
CAM CLKOUT0	/ EBC SDCE1	/ GMAC1 RXD0 M1	/ SPI3 CS1 M0	/ I2S1 LCLK RX M1	/ GPIO4 A7 d
CAM CLKOUT1	/ EBC SDCE2	/ GMAC1 RXD1 M1	/ SPI3 MISO M0	/ I2S1 SDO1 M1	/ GPIO4 B0 d
ISP PRELIGHT TRIG	/ EBC SDCE3	/ GMAC1 RXDV CRS M1	/ I2S1 SDO2 M1	/ I2S1 SDO2 M1	/ GPIO4 B1 d
I2C4 SDA M0	/ EBC VCOM	/ GMAC1 RXER M1	/ SPI3 MOSI M0	/ I2S2 SDO1 M1	/ GPIO4 B2 d
I2C4 SCL M0	/ EBC GD08	/ ETH1 REFCLK0 25M M1	/ SPI3 CLK M0	/ I2S2 SDO1 M1	/ GPIO4 B3 d
I2C2 SDA M1	/ EBC GDSP	/ CAN2 RX M0	/ ISP FLASH TRIGIN	/ VOP BT656 CLK M1	/ GPIO4 B4 d
I2C2 SCL M1	/ EBC SD5HA	/ CAN2 TX M0	/ I2S1 SDO3 M1	/ I2S1 SDO3 M1	/ GPIO4 B5 d
CIF HREF	/ EBC SDLE	/ GMAC1 MDC M1	/ UART1 RTS M1	/ I2S2 MCLK M1	/ GPIO4 B6 d
CIF VSYNC	/ EBC SD0E	/ GMAC1 MDIO M1	/ I2S2 SCLK TX M1	/ I2S2 SCLK TX M1	/ GPIO4 B7 d
CIF CLKOUT	/ EBC GDCLK	/ GMAC1 MDC M1	/ PWM1 IR M1	/ I2S2 SCLK TX M1	/ GPIO4 C0 d
CIF CLKIN	/ EBC SDCLK	/ GMAC1 MCLKINOUT M1	/ UART1 CTS M1	/ I2S2 SCLK RX M1	/ GPIO4 C1 d



RK3568
bga636_19r00x19r00x1r20

Note:

Caps of between dashed green lines and U3 should be placed under the U3 package. Other caps should be placed close to the U3 package

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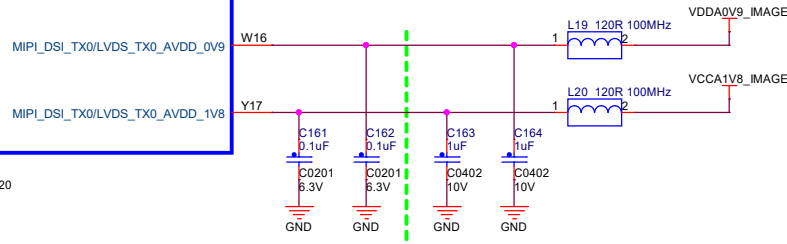
Title 野火_EBF RK3568 LBC		
Size A3	Document Number VI Interface	Rev V1.0
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RK3568 MIPI_DSI_TX0/LVDS_TX0

U5R

MIPI DSI TX0/LVDS TX0

MIPI_DSI_TX0_D0P/LVDS_TX0_D0P AH17 Diff 100 Ohm ±10% MIPI_DSI_TX0_D0P [25]
 MIPI_DSI_TX0_D0N/LVDS_TX0_D0N AG17 MIPI_DSI_TX0_D0N [25]
 MIPI_DSI_TX0_D1P/LVDS_TX0_D1P AH16 Diff 100 Ohm ±10% MIPI_DSI_TX0_D1P [25]
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 MIPI_DSI_TX0_D2P/LVDS_TX0_D2P AH14 Diff 100 Ohm ±10% MIPI_DSI_TX0_D2P [25]
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 MIPI_DSI_TX0_D3P/LVDS_TX0_D3P AH13 Diff 100 Ohm ±10% MIPI_DSI_TX0_D3P [25]
 MIPI_DSI_TX0_D3N/LVDS_TX0_D3N AG13 MIPI_DSI_TX0_D3N [25]
 MIPI_DSI_TX0_CLKP/LVDS_TX0_CLKP AH15 Diff 100 Ohm ±10% MIPI_DSI_TX0_CLKP [25]
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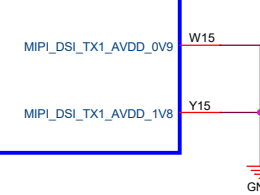
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RK3568 MIPI_DSI_TX1

U5S

MIPI DSI TX1

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 MIPI_DSI_TX1_D0N AE18
 MIPI_DSI_TX1_D1P AD17
 MIPI_DSI_TX1_D1N AC17
 MIPI_DSI_TX1_D2P AD14
 MIPI_DSI_TX1_D2N AC14
 MIPI_DSI_TX1_D3P AD12
 MIPI_DSI_TX1_D3N AE12
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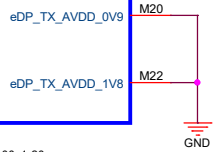
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RK3568 eDP TX

U5T

eDP_TX

eDP_TX_D0P J28
 eDP_TX_D0N K27
 eDP_TX_D1P K28
 eDP_TX_D1N L27
 eDP_TX_D2P L28
 eDP_TX_D2N M27
 eDP_TX_D3P M28
 eDP_TX_D3N N27
 eDP_TX_AUXP L25
 eDP_TX_AUXN M25



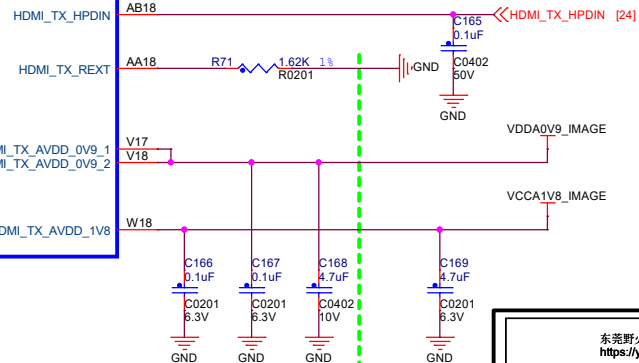
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RK3568 HDMI2.0 TX

U5Q

HDMI2.0 TX

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 HDMI_TX_D1P AG21 2.2R R0201 HDMI_TX1P_PORT [24]
 HDMI_TX_D1N AH21 2.2R R0201 HDMI_TX1N_PORT [24]
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 HDMI_TX_CLKN AG19 2.2R R0201 HDMI_TXCLKN_PORT [24]



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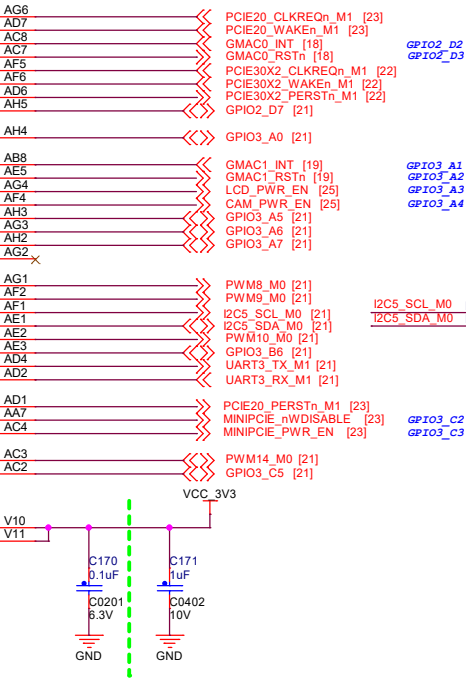
Title 野火_EBF RK3568 LBC		
Size A3	Document Number VO Interface_1	Rev V1.0
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RK3568 VCCIO5 Domain

USL

VCCIO5 Domain									
Operating Voltage=1.8V/3.3V									
LCDC D0	/ VOP BT656 D0 M0	/ SPI0 MISO M1	/ PCIE20 CLKREQn M1	/ I2S1 MCLK M2	/ GPIO2 D0 d				
LCDC D1	/ VOP BT656 D1 M0	/ SPI0 MOSI M1	/ PCIE20 WAKEn M1	/ I2S1 SCLK TX M2	/ GPIO2 D1 d				
LCDC D2	/ VOP BT656 D2 M0	/ SPI0 CS0 M1	/ PCIE30X1 CLKREQn M1	/ I2S1 LCLK TX M2	/ GPIO2 D2 d				
LCDC D3	/ VOP BT656 D3 M0	/ SPI0 CLK M1	/ PCIE30X1 WAKEn M1	/ I2S1 SDIO M2	/ GPIO2 D3 d				
LCDC D4	/ VOP BT656 D4 M0	/ SPI2 CS1 M1	/ PCIE30X2 CLKREQn M1	/ I2S1 SDIO M2	/ GPIO2 D4 d				
LCDC D5	/ VOP BT656 D5 M0	/ SPI2 CS0 M1	/ PCIE30X2 WAKEn M1	/ I2S1 SDIO M2	/ GPIO2 D5 d				
LCDC D6	/ VOP BT656 D6 M0	/ SPI2 MOSI M1	/ PCIE30X2 PERSTn M1	/ I2S1 SDIO M2	/ GPIO2 D6 d				
LCDC D7	/ VOP BT656 D7 M0	/ SPI2 MISO M1	/ UART9 TX M1	/ I2S1 SDIO M2	/ GPIO2 D7 d				
LCDC CLK	/ VOP BT656 CLK M0	/ SPI2 CLK M1	/ UART9 RX M1	/ I2S1 SDIO M2	/ GPIO3 A0 d				
LCDC D8	/ VOP BT1120 D0	/ SPI1 CS0 M1	/ PCIE30X1 PERSTn M1	/ SDMMC2 D0 M1	/ GPIO3 A1 d				
LCDC D9	/ VOP BT1120 D1	/ GMAC1 TXD2 M0	/ I2S3 MCLK M0	/ SDMMC2 D1 M1	/ GPIO3 A2 d				
LCDC D10	/ VOP BT1120 D2	/ GMAC1 TXD3 M0	/ I2S3 SCLK M0	/ SDMMC2 D2 M1	/ GPIO3 A3 d				
LCDC D11	/ VOP BT1120 D3	/ GMAC1 RXD2 M0	/ I2S3 LCLK M0	/ SDMMC2 D3 M1	/ GPIO3 A4 d				
LCDC D12	/ VOP BT1120 D4	/ GMAC1 RXD3 M0	/ I2S3 CS0 M1	/ SDMMC2 D4 M1	/ GPIO3 A5 d				
LCDC D13	/ VOP BT1120 D5	/ GMAC1 TXCLK M0	/ I2S3 SD1 M0	/ SDMMC2 CLK M1	/ GPIO3 A6 d				
LCDC D14	/ VOP BT1120 D6	/ GMAC1 RXCLK M0		/ SDMMC2 DEN M1	/ GPIO3 A7 d				
LCDC D15	/ VOP BT1120 D6	/ ETH1 REPCIO 25M M0		/ SDMMC2 PWREN M1	/ GPIO3 B0 d				
LCDC D16	/ VOP BT1120 D7	/ GMAC1 RXD0 M0	/ UART4 RX M1	/ PWM8 M0	/ GPIO3 B1 d				
LCDC D17	/ VOP BT1120 D8	/ GMAC1 RXD1 M0	/ UART4 TX M1	/ PWM9 M0	/ GPIO3 B2 d				
LCDC D18	/ VOP BT1120 D9	/ GMAC1 RXD0 CHS M0	/ I2C5 SCL M0	/ PWM SDIO M2	/ GPIO3 B3 d				
LCDC D19	/ VOP BT1120 D10	/ GMAC1 RXD1 M0	/ I2C5 SDA M0	/ PWM SDIO M2	/ GPIO3 B4 d				
LCDC D20	/ VOP BT1120 D11	/ GMAC1 TXD0 M0	/ I2C5 SCL M1	/ PWM10 M0	/ GPIO3 B5 d				
LCDC D21	/ VOP BT1120 D12	/ GMAC1 TXD1 M0	/ I2C3 SDA M1	/ PWM11 IR M0	/ GPIO3 B6 d				
LCDC D22	/ PWM12 M0	/ GMAC1 TXEN M0	/ UART3 TX M1	/ PDM SD12 M2	/ GPIO3 B7 d				
LCDC D23	/ PWM13 M0	/ GMAC1 MCLKINOUT M0	/ UART3 RX M1	/ PDM SD13 M2	/ GPIO3 C0 d				
LCDC HSYNC	/ VOP BT1120 D13	/ SPI1 MOSI M1	/ PCIE20 PERSTn M1	/ I2S1 SDIO2 M2	/ GPIO3 C1 d				
LCDC VSYNC	/ VOP BT1120 D14	/ SPI1 MISO M1	/ UART5 TX M1	/ I2S1 SDIO3 M2	/ GPIO3 C2 d				
LCDC DEN	/ VOP BT1120 D15	/ SPI1 CLK M1	/ UART5 RX M1	/ I2S1 SCLK RX M2	/ GPIO3 C3 d				
PWM14 M0	/ VOP PWM M1	/ GMAC1 NDC M0	/ UART7 TX M1	/ PDM CLK1 M2	/ GPIO3 C4 d				
PWM15 TX M0	/ SP015 TX M1	/ GMAC1 MDIO M0	/ UART7 RX M1	/ I2S1 LCLK RX M2	/ GPIO3 C5 d				
					VCCIO5_1				
					VCCIO5_2				

RK3568
bga636_19r00x19r00x120



Note:

Caps of between dashed green lines and U3 should be placed under the U3 package.
Other caps should be placed close to the U3 package

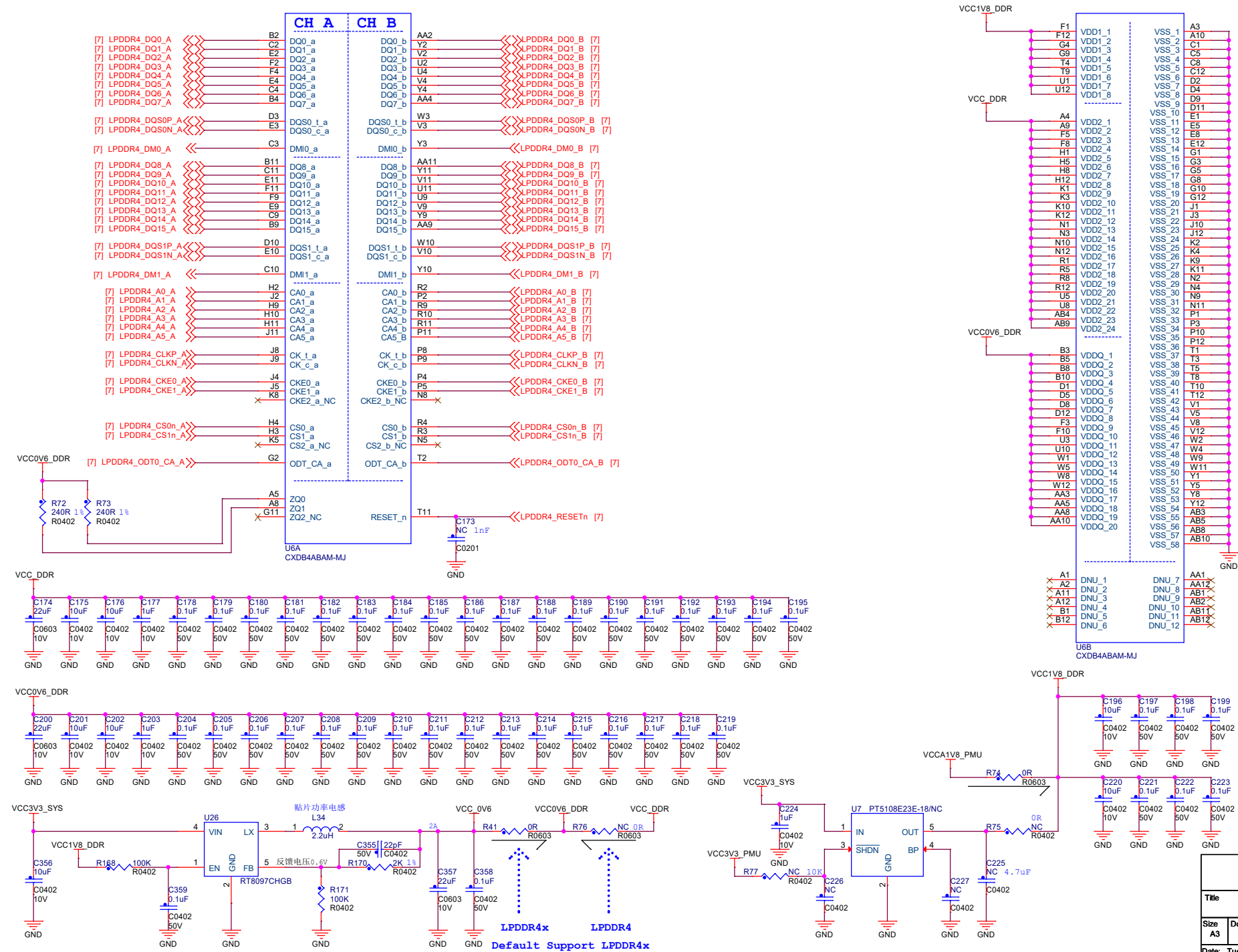
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U5H

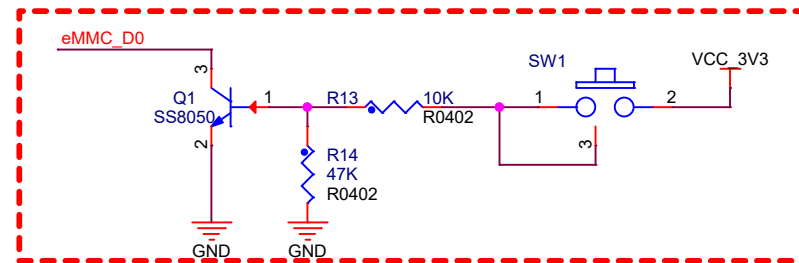
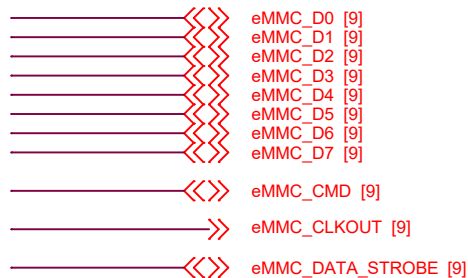
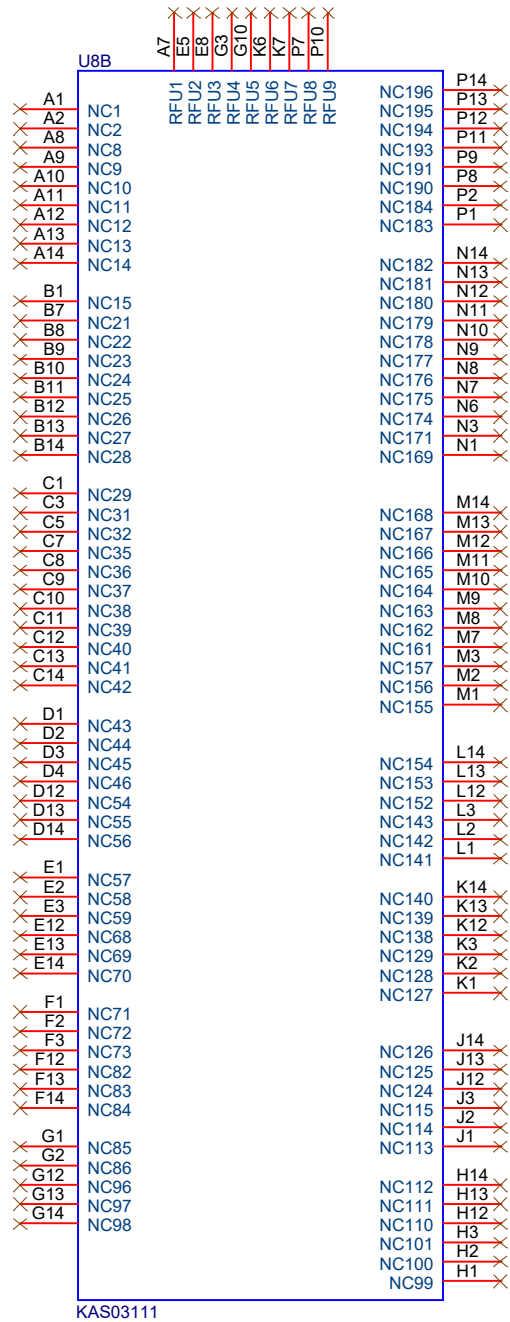
Operating Voltage=1.8V/3.3V

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LPDDR4/4x



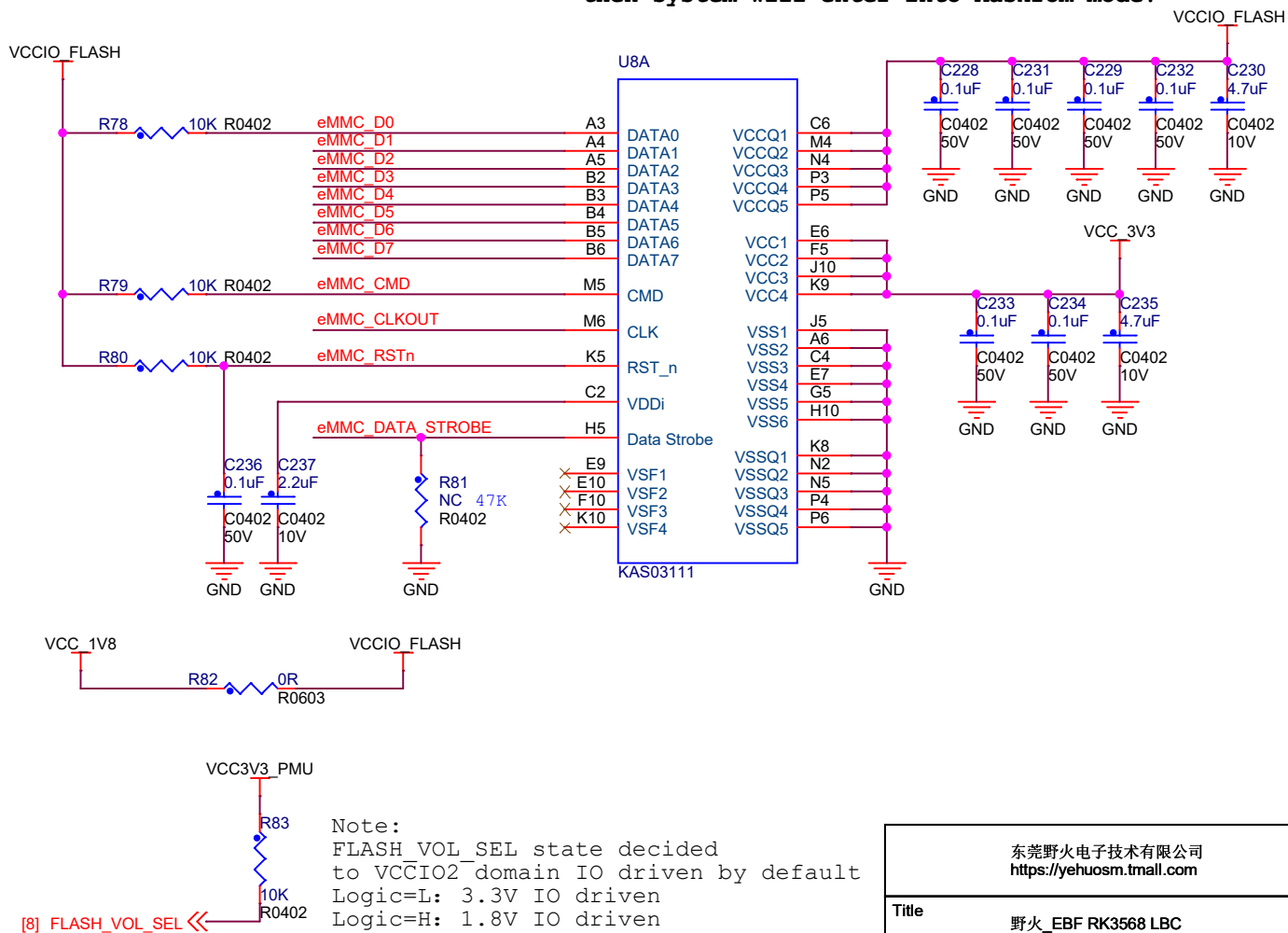
eMMC Flash



Note:

For eMMC or Nand Flash:

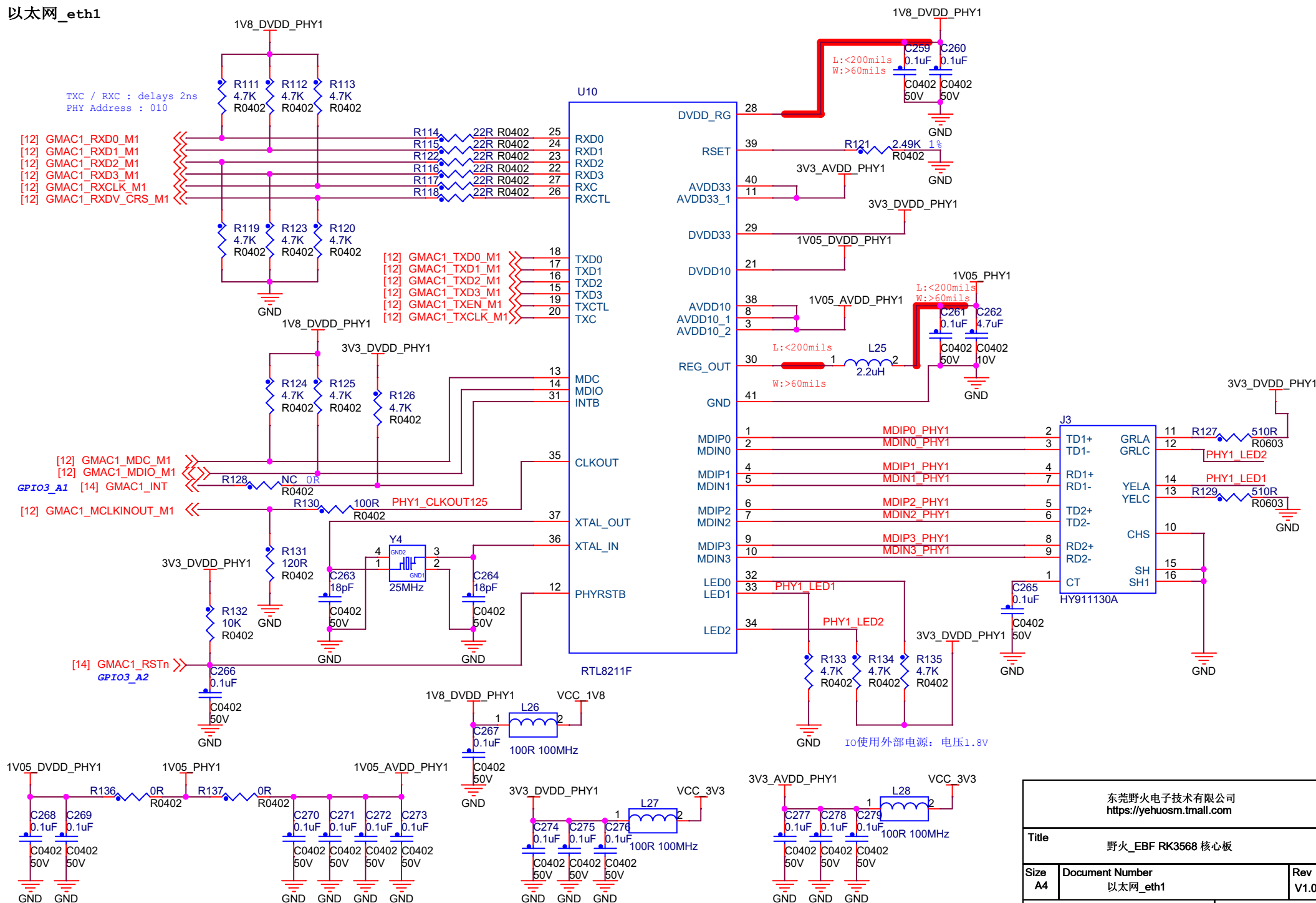
If eMMC_D0/FLASH_D0=0V at after power on and reset,
then system will enter into Maskrom mode.



```
Note:
FLASH VOL_SEL state decided
to VCCIO2_domain IO driven by default
Logic=L: 3.3V IO driven
Logic=H: 1.8V IO driven
```

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以太网_eth1



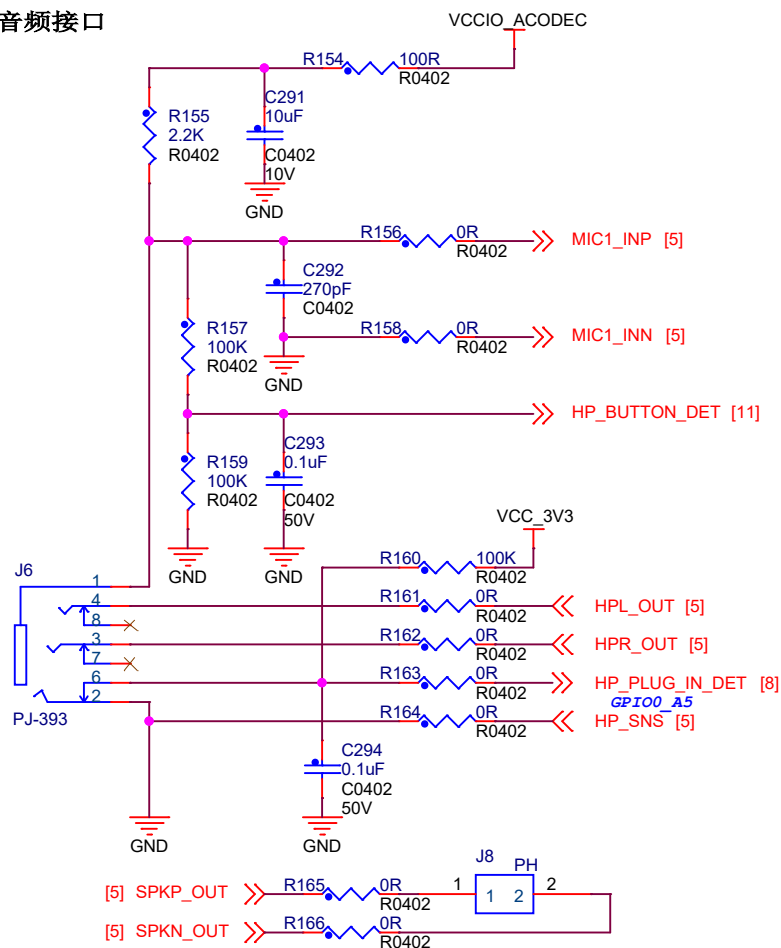
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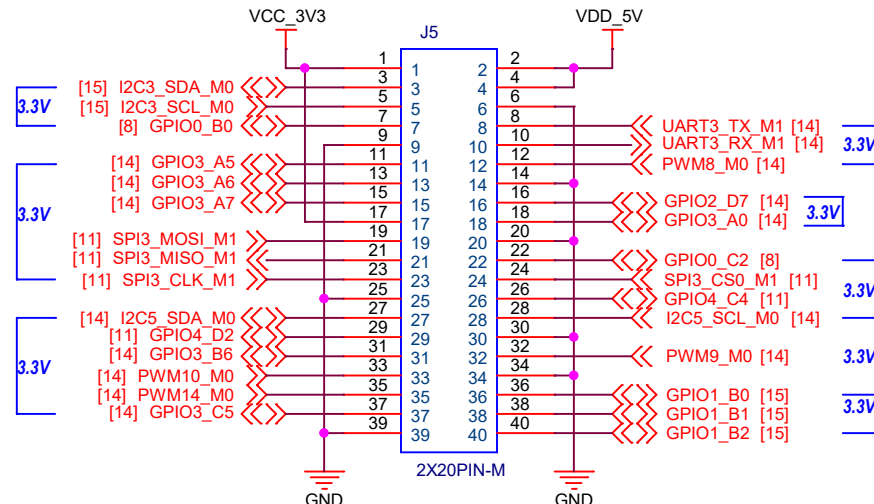
Size A4	Document Number 以太网_eth1	Rev V1.0
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音频接口

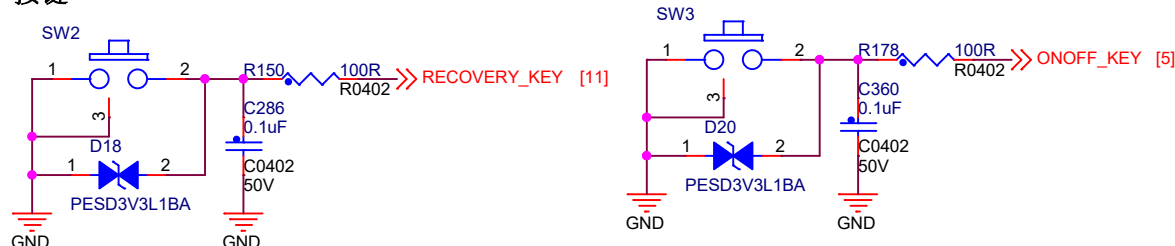


IO 引出

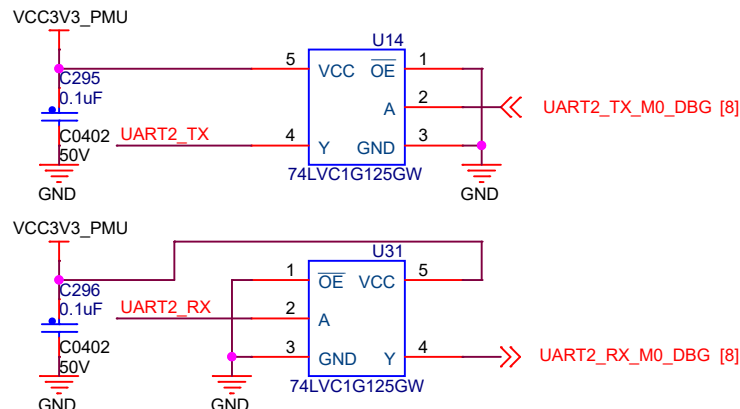
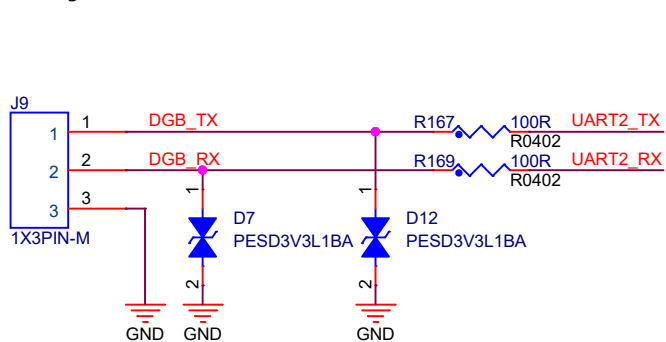


注：所有引出的GPIO只兼容3.3V

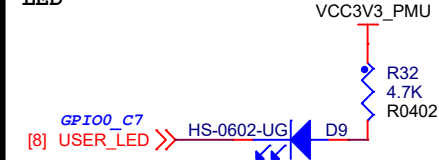
按键



Debug接口



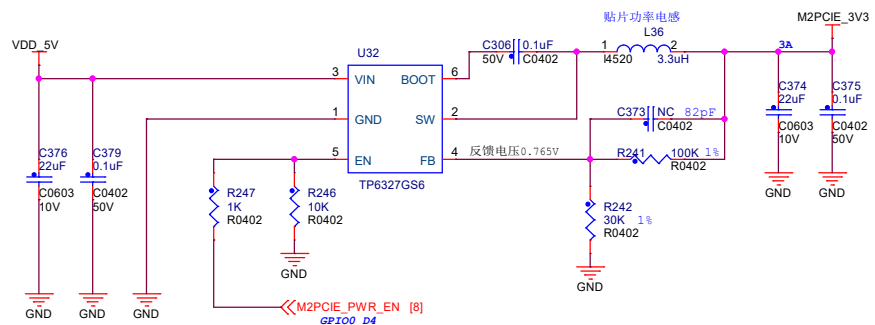
LED



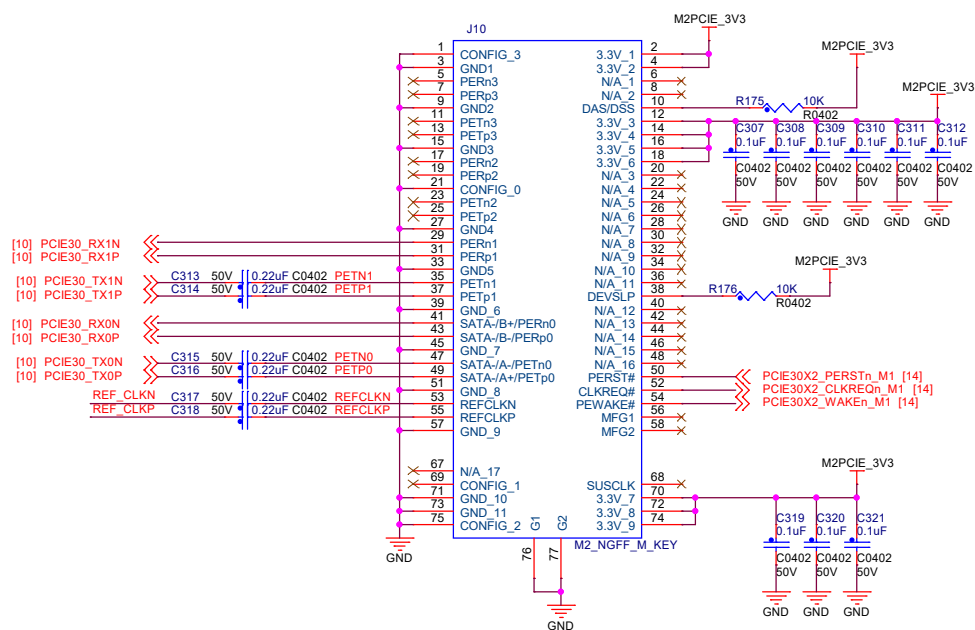
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Title		
野火_EBF RK3568 LBC		
Size	Document Number	Rev
A4	音频/DEBUG/IO引出/按键/LED	V1.0
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M.2 PCIE POWER 3.3V



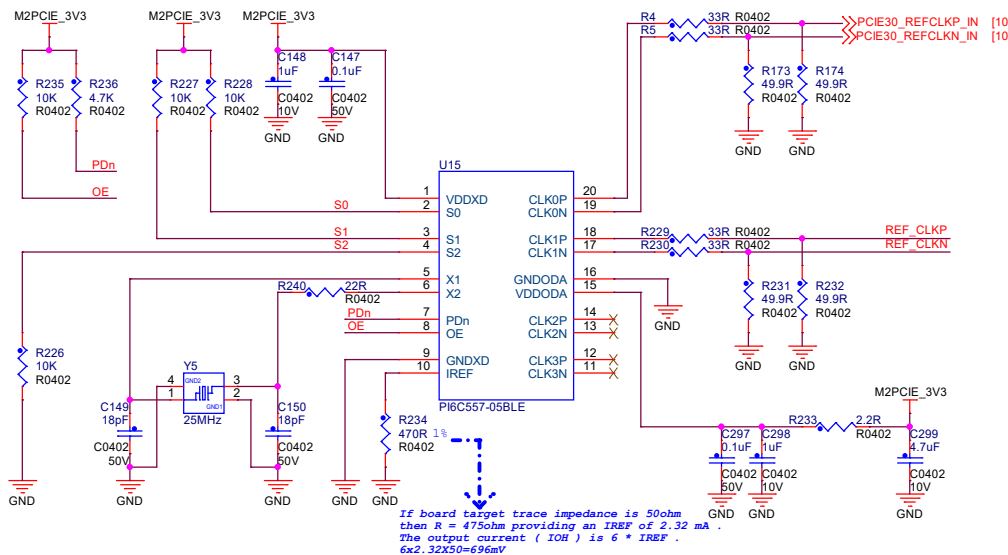
M.2 PCIE3.0 x 2Lanes



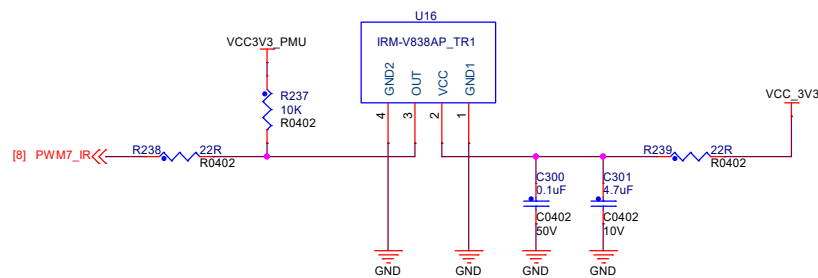
贴片螺母



```
M.2 PCIE REF CLKP 100MHz
```



IR 接收器



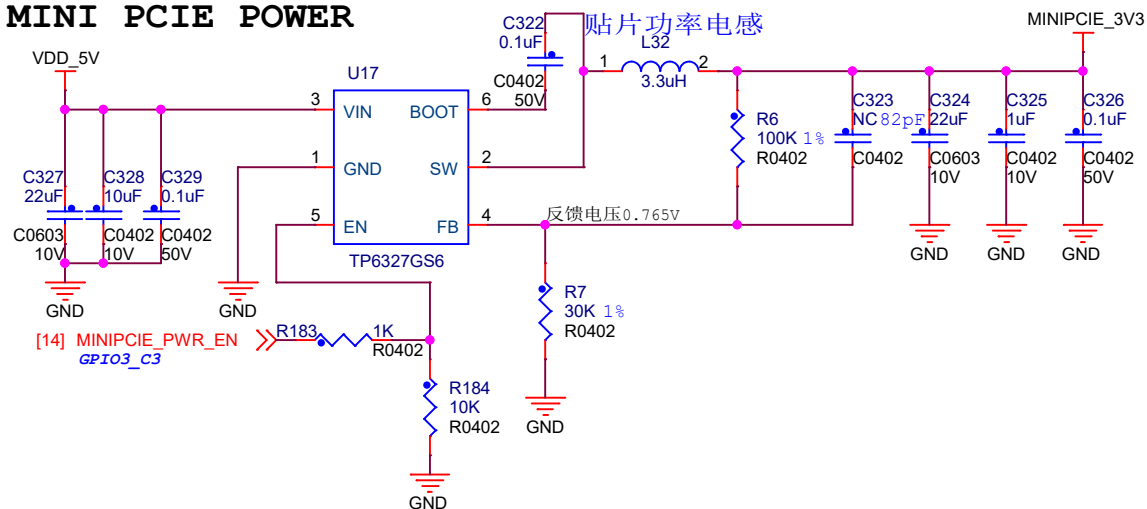
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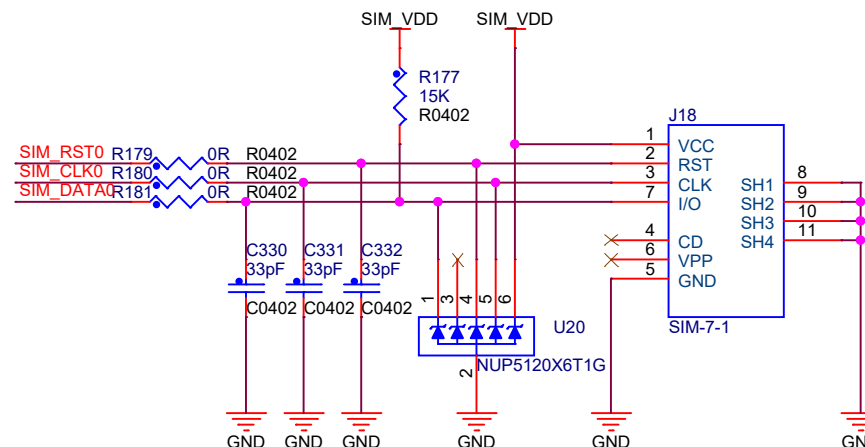
Size	Document Number	Rev
A3	M.2 PCIE3.0接口/IR接收器	V1.0

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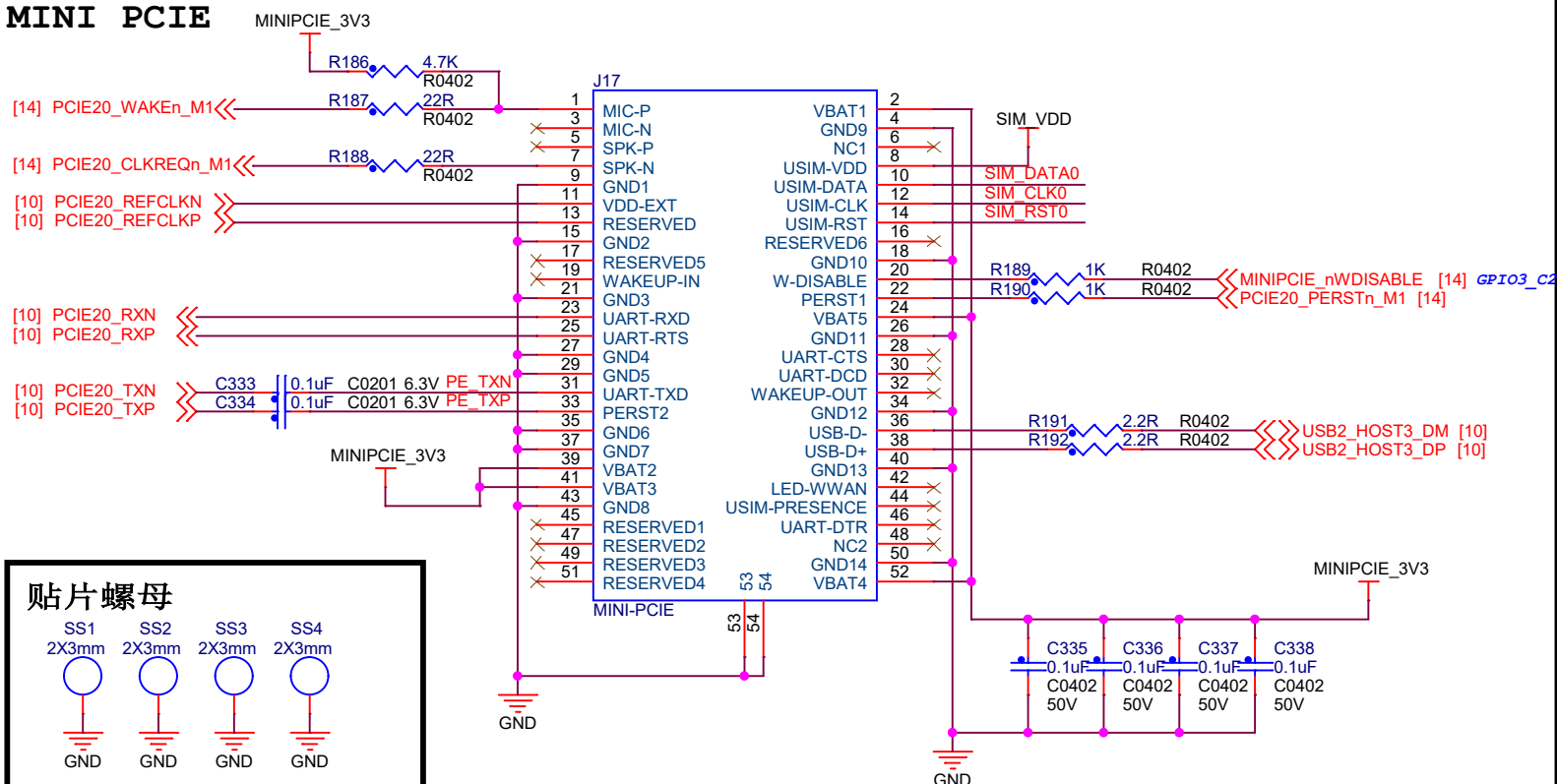
MINI PCIE POWER



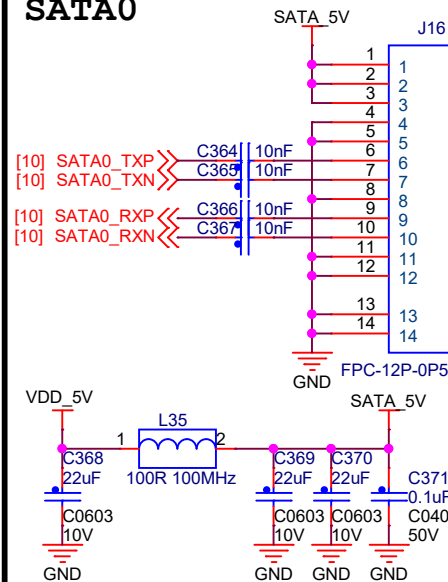
SIM CARD



MINI PCIE



SATA0



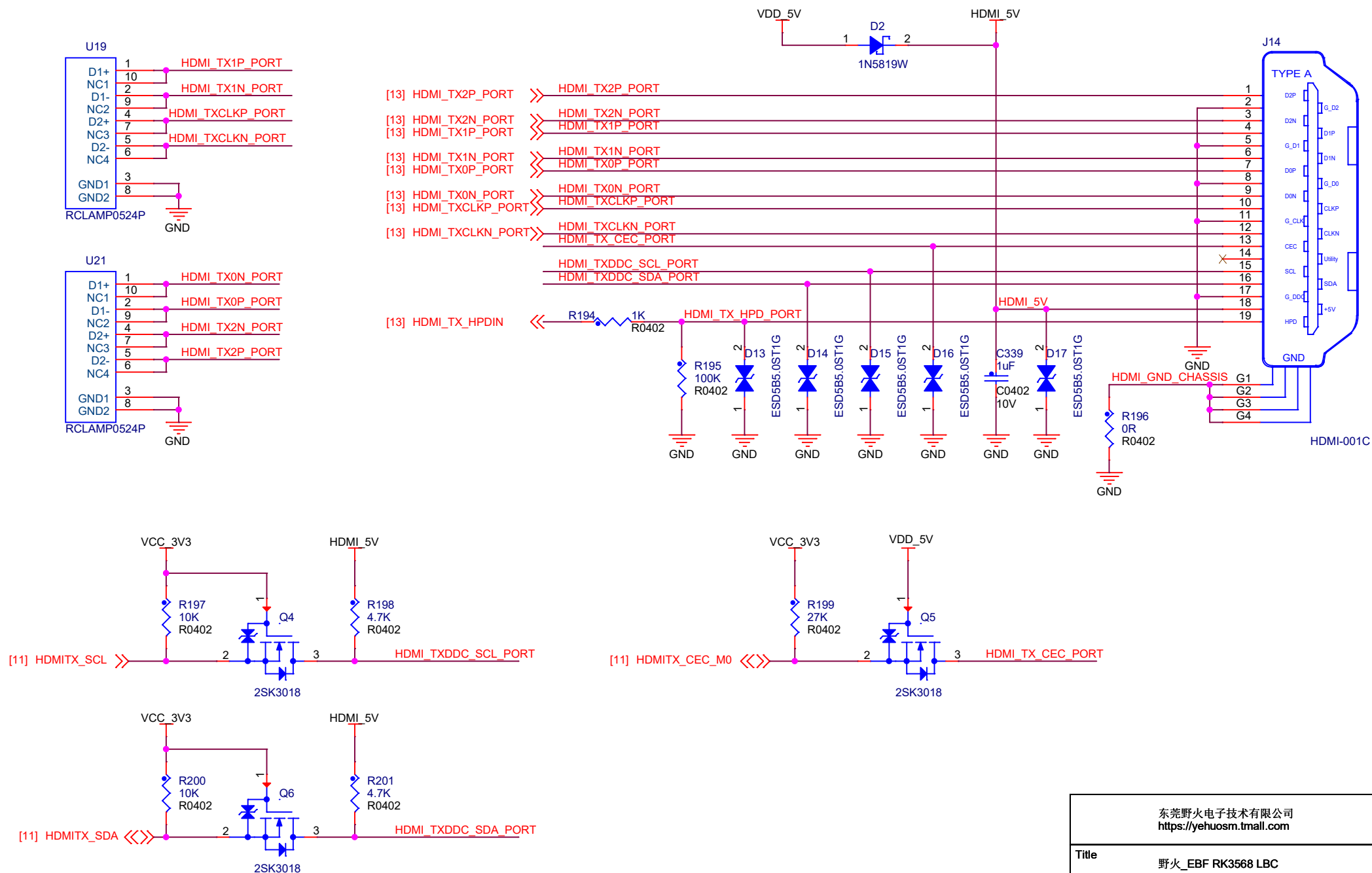
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A4	M.2 PCIE3.0接口/SATA	V1.0

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HDMI 2.0接口



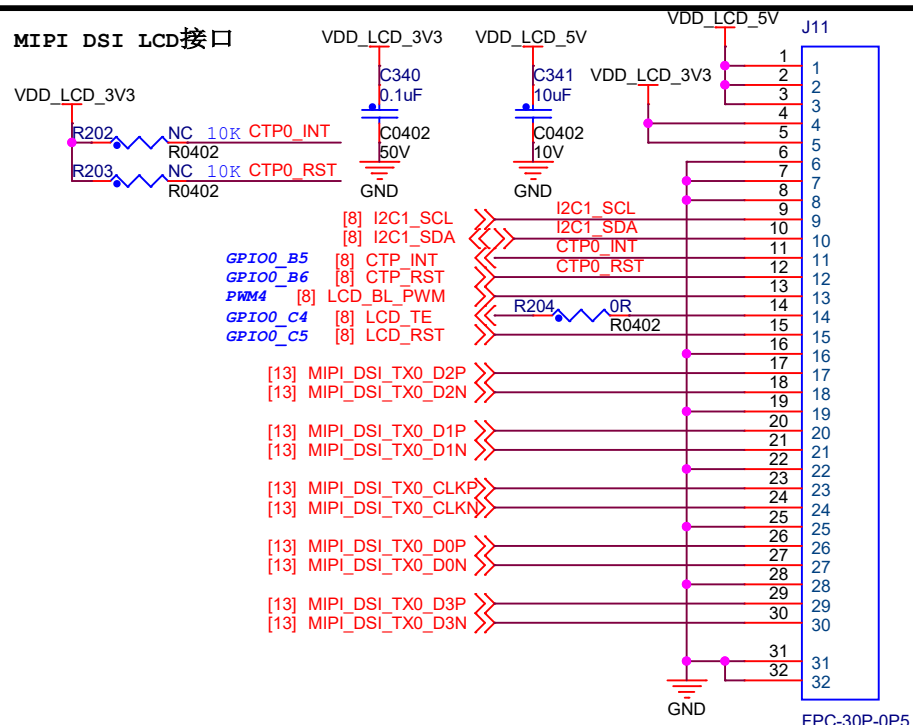
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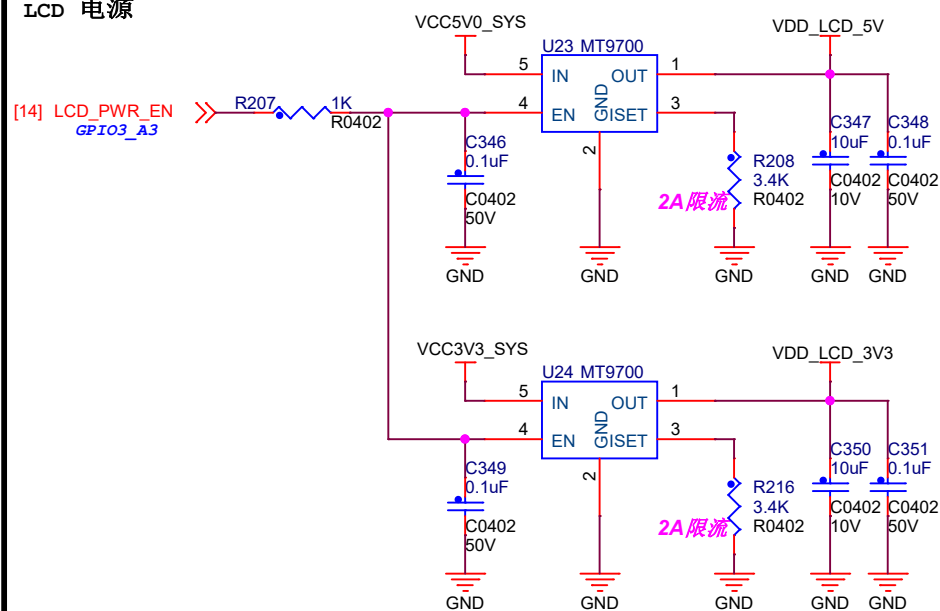
Size A4	Document Number M.2 PCIE3.0接口	Rev V1.0
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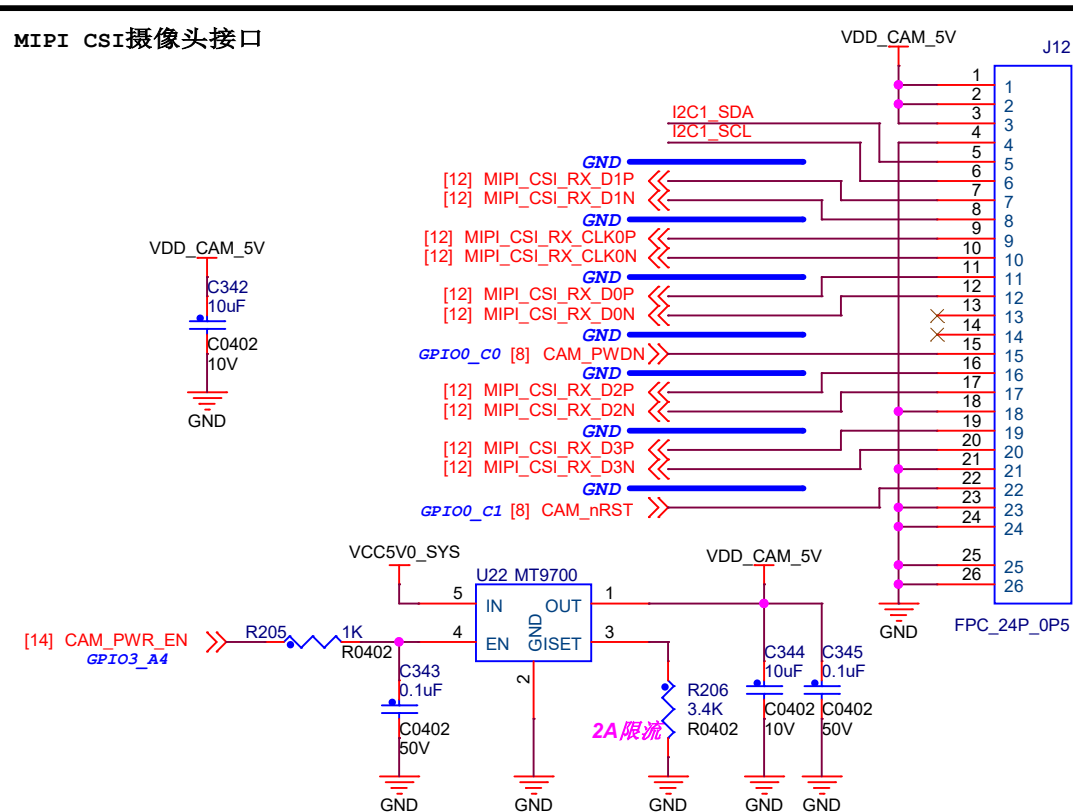
MIPI DSI LCD接口



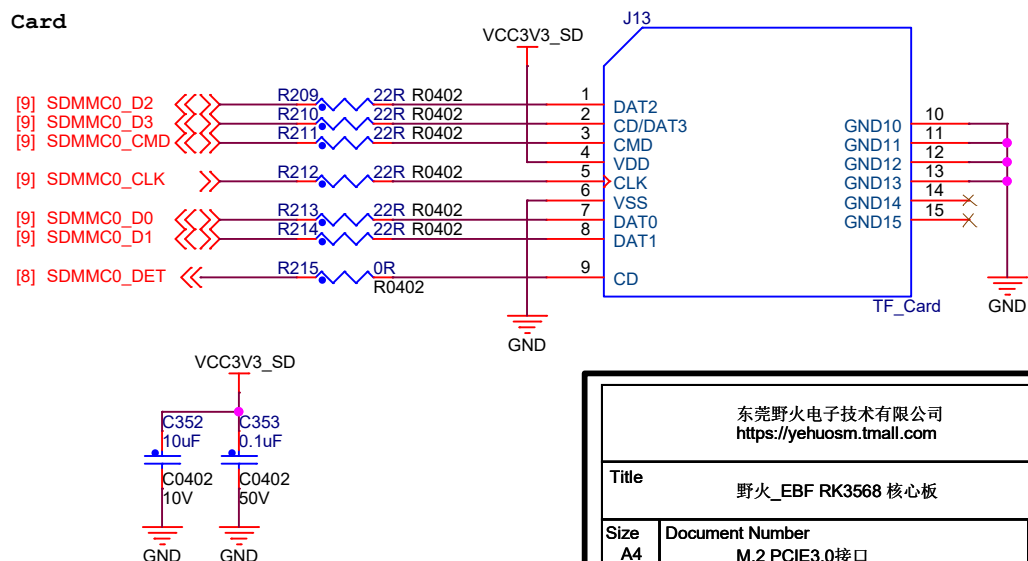
LCD 电源



MIPI CSI摄像头接口



SD Card



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