

野火_RK3566_LUBANCAT_ZERO_WIFI_原理图

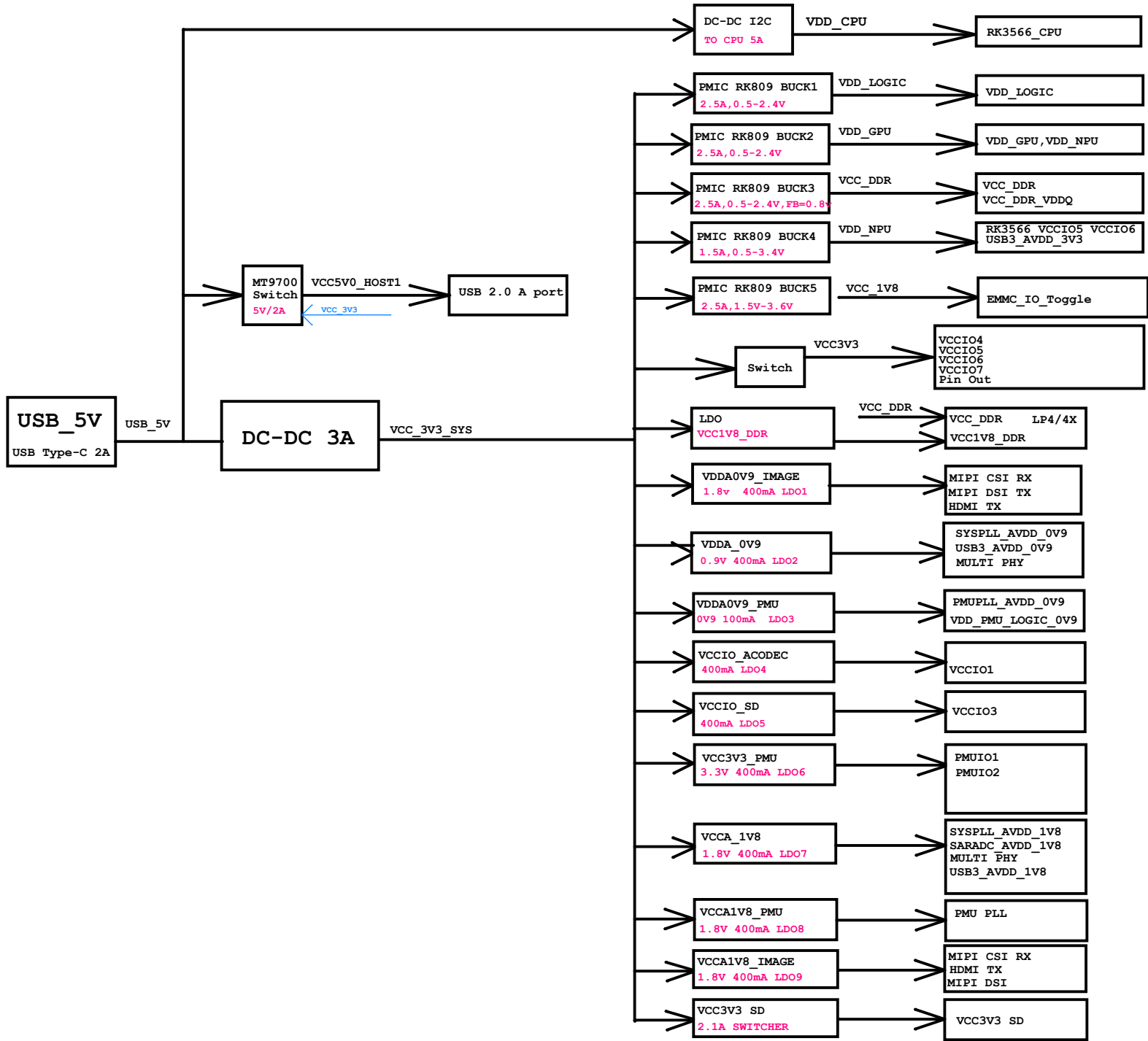
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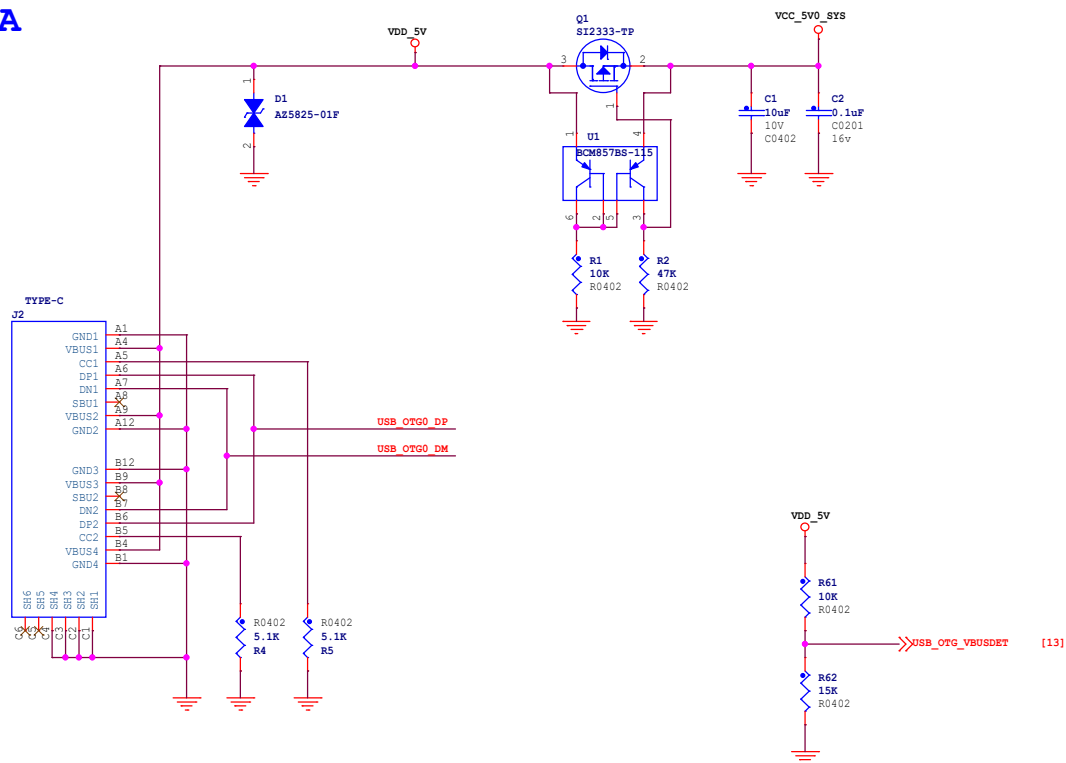
历史版本

版本号	日期	设计	描述
V0.1	2022-04-18	ghs	初始版本
V0.2	2022-07-19	ghs	修正mini HDMI引脚定义，mipi csi添加两对数据线

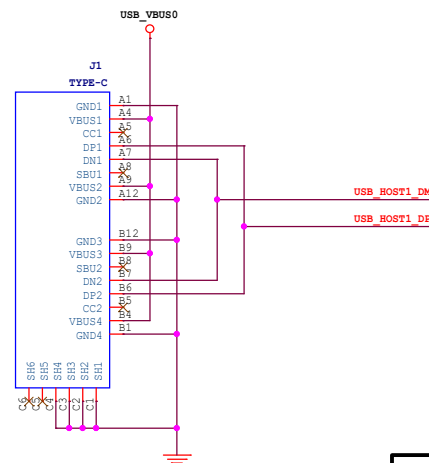
POWER DIAGRAM



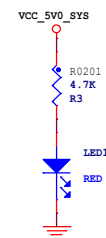
POWER IN OTG Download 5V 3A



USB 2.0 HOST x1



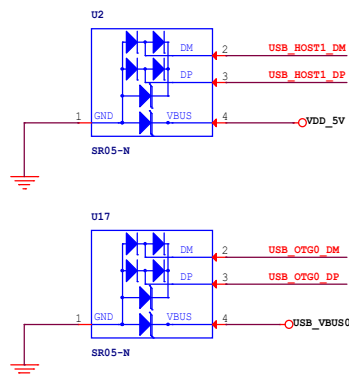
PWR_LED



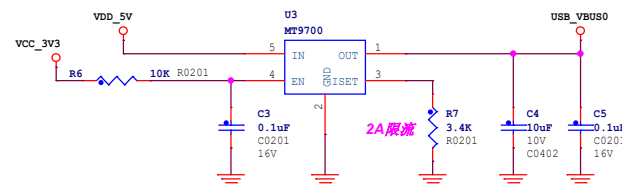
机械定位孔 4*M2.5



USB ESD



USB POWER

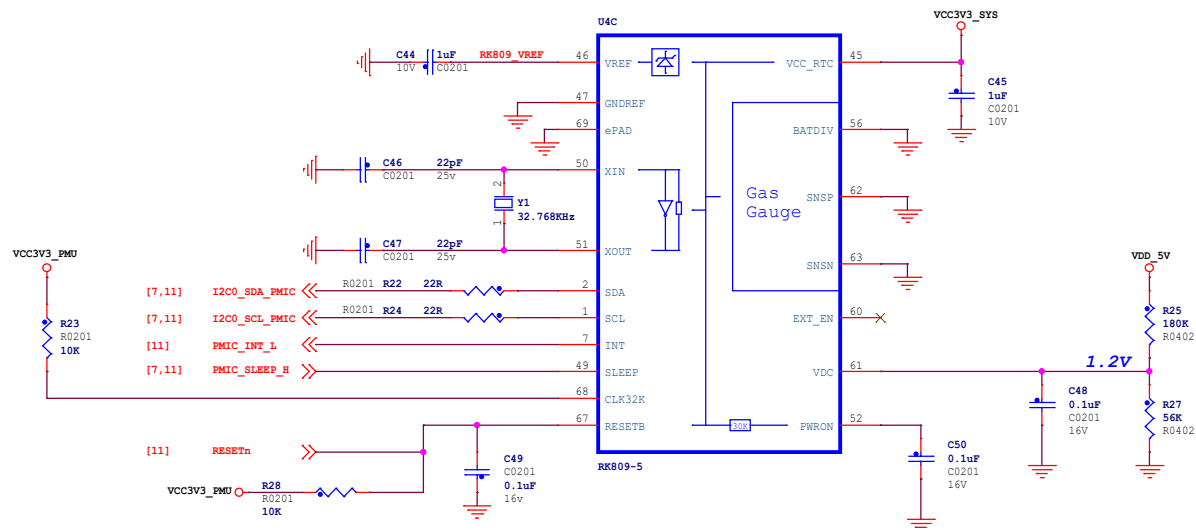


USB_HOST1_DM >>> USB_HOST1_DM [13]
 USB_HOST1_DP >>> USB_HOST1_DP [13]
 USB_OTG0_DP >>> USB_OTG0_DP [13]
 USB_OTG0_DM >>> USB_OTG0_DM [13]

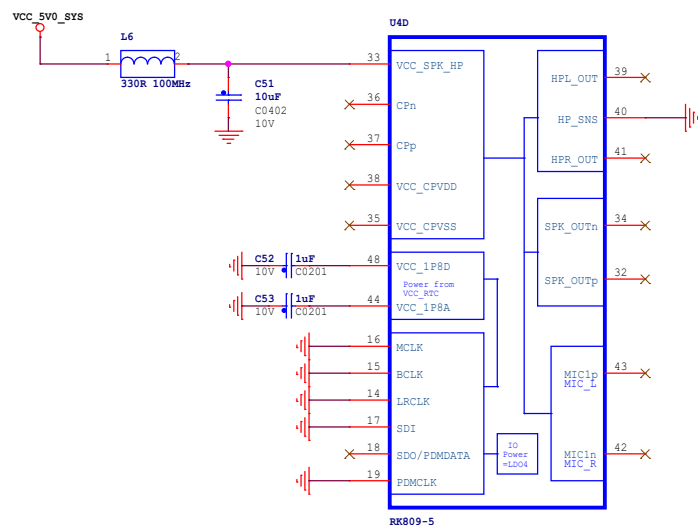
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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A3	Document Number TYPE_C_POWER/OTG	Rev V0.2
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PMIC RK809 Managerment

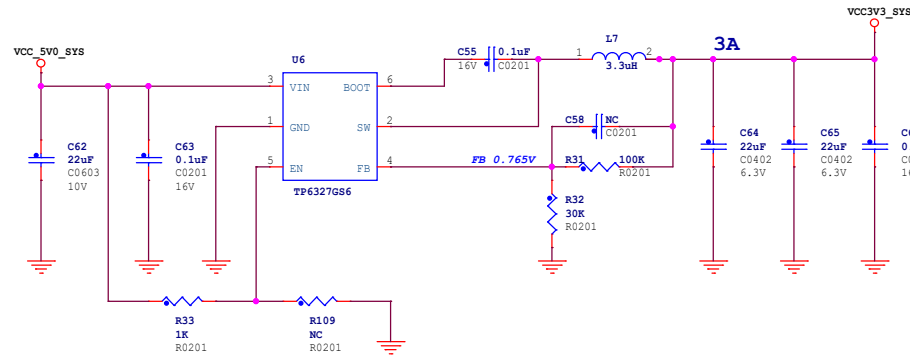


PMIC RK809 CODEC

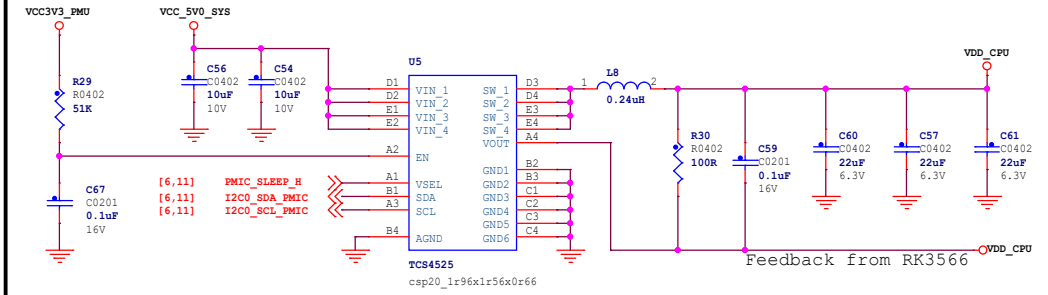


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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
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A3	PMIC_Managerment/CODE	V0.2
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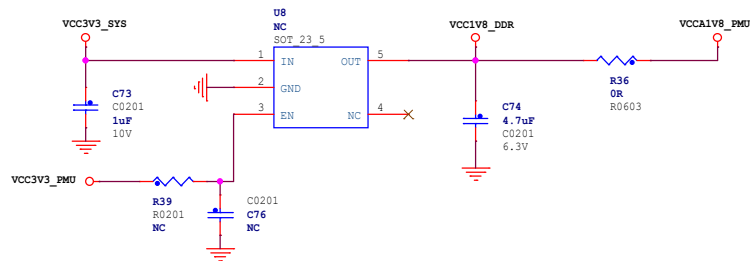
SYS_Power



RK3566 CPU Power VDD_CPU_EXT

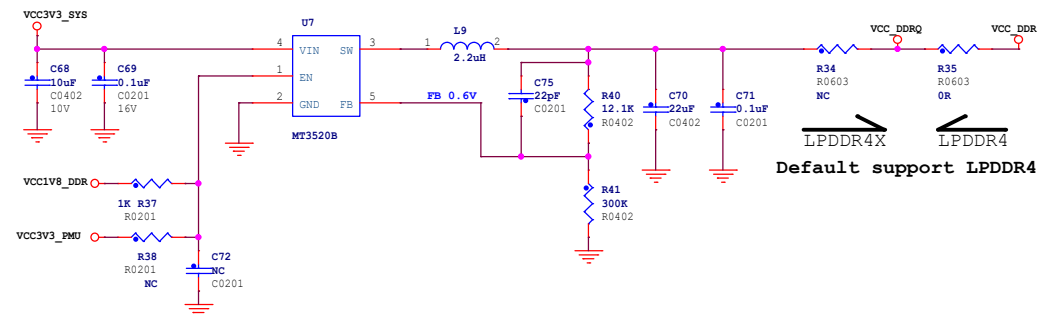


Auxiliary power supply LPDDR4_VCC1V8_DDR



LPDDR4X Power

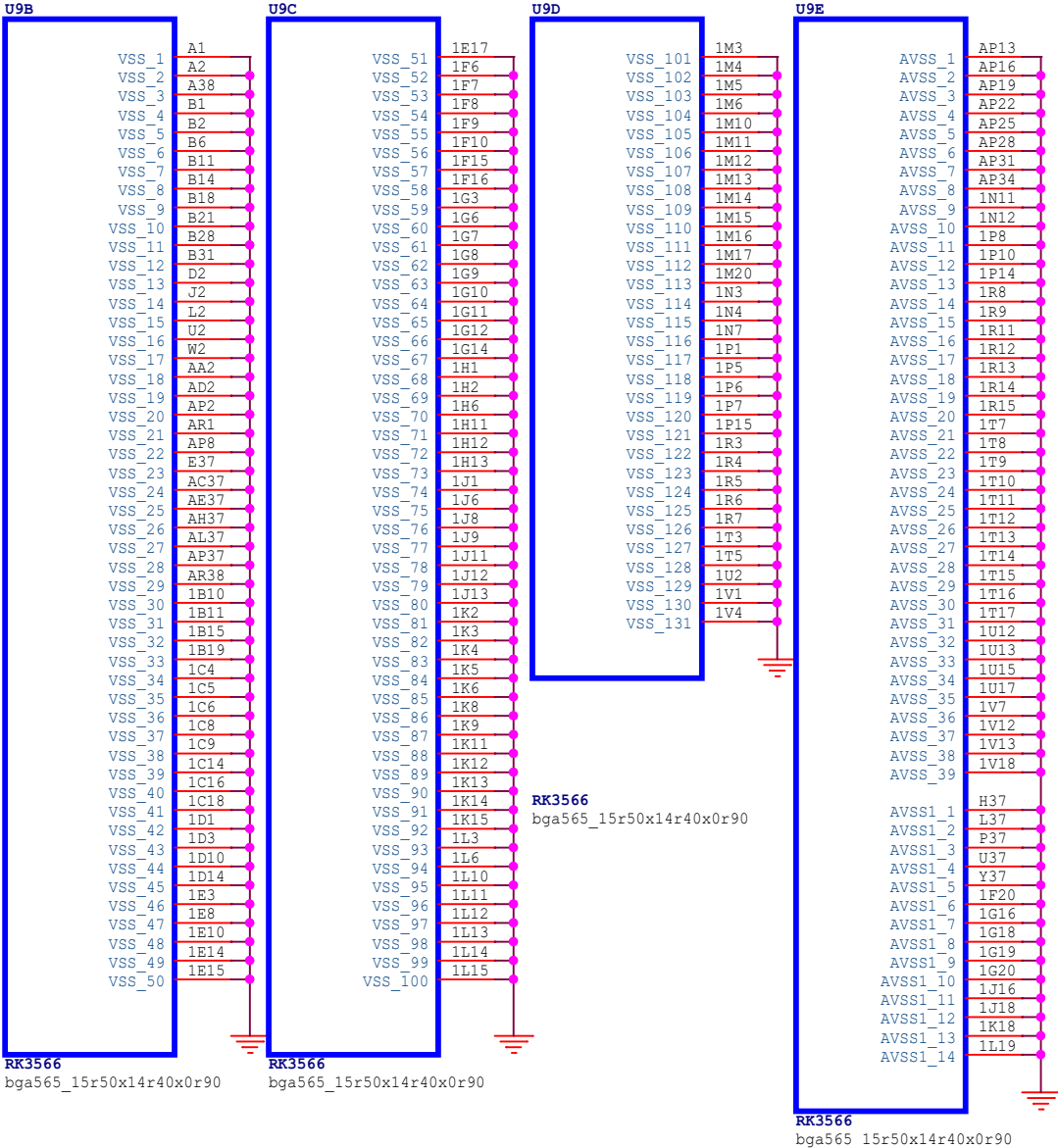
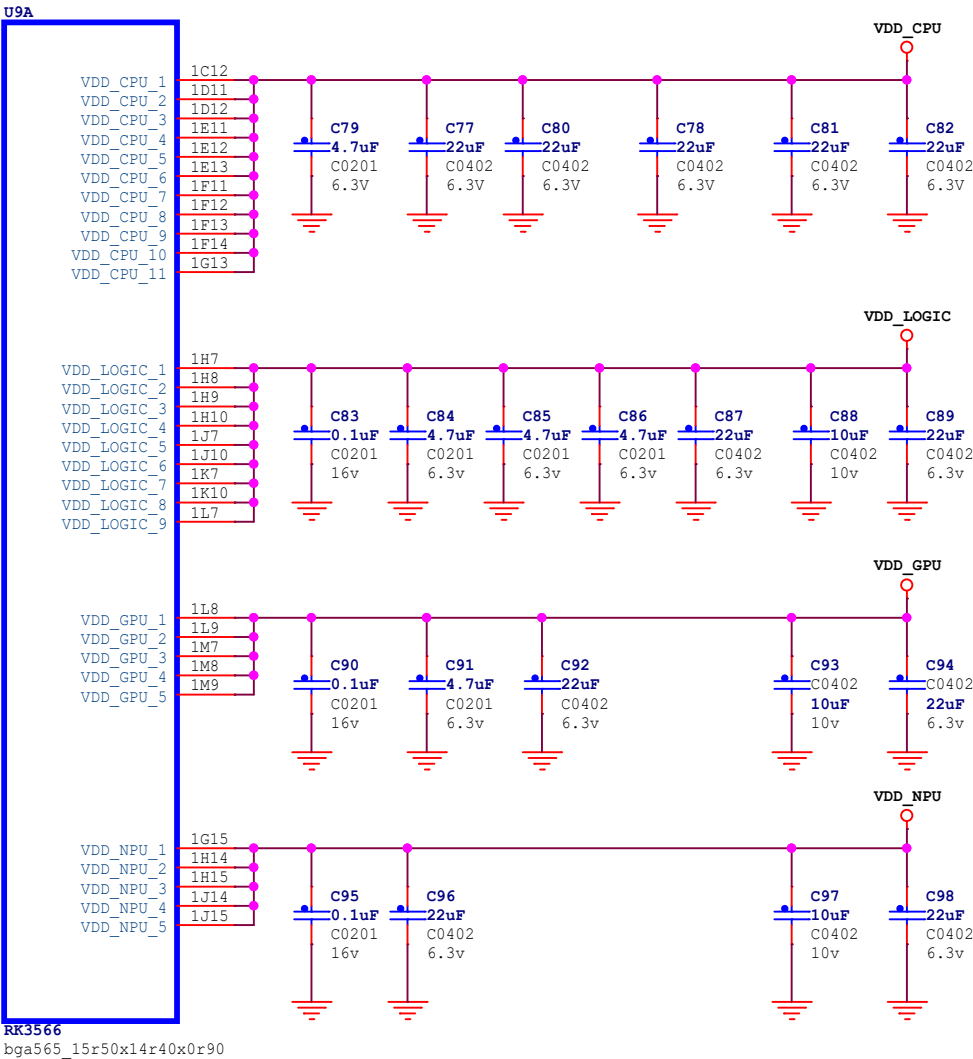
output: 0.6V



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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A3	Document Number Power_other	Rev V0.2
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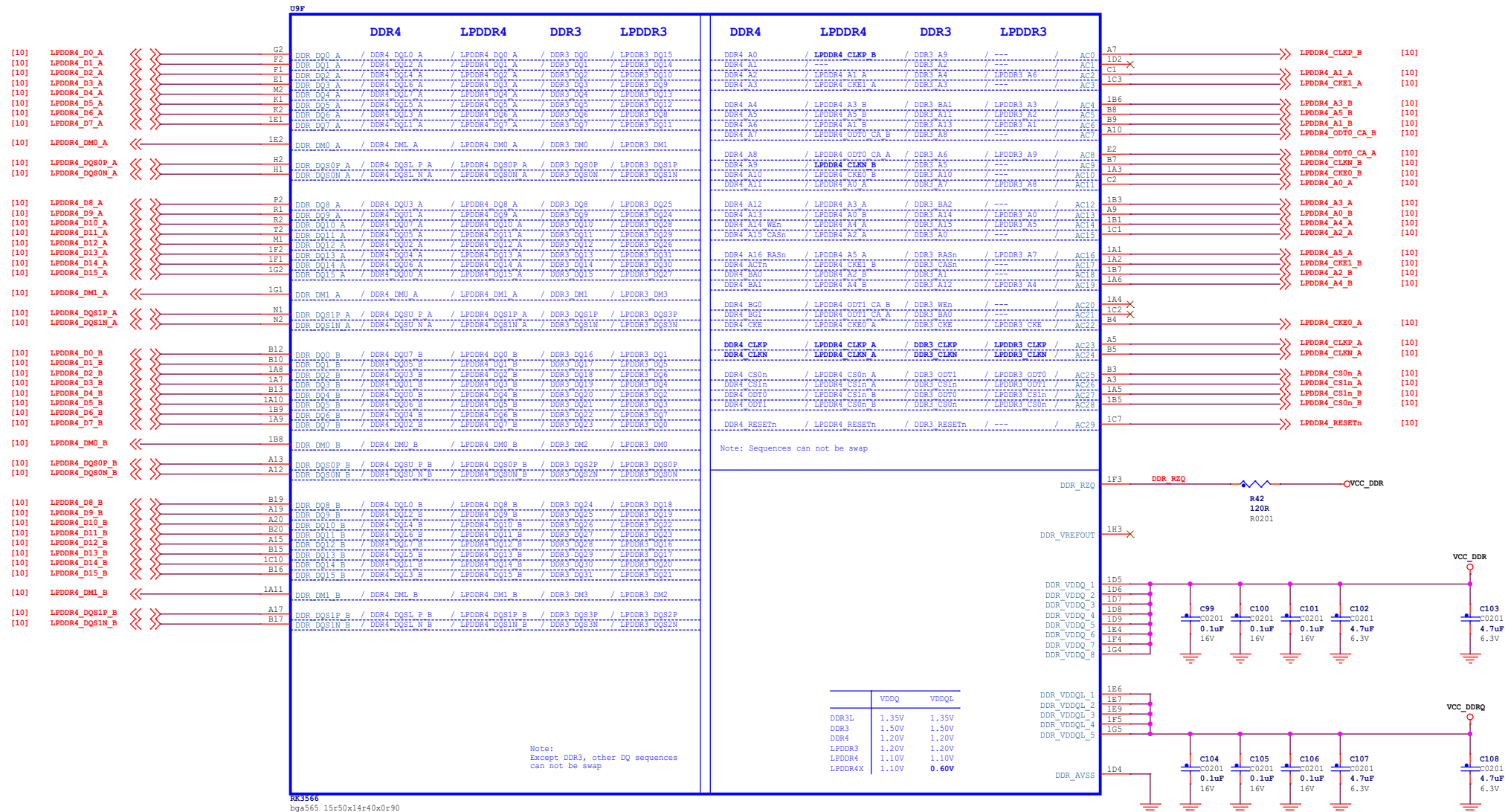
RK3566_ABCDE

(Power&GND)

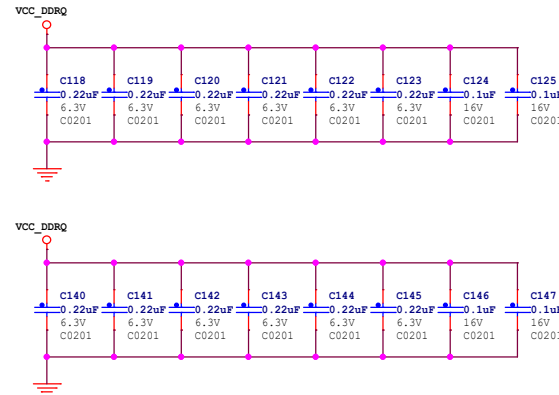
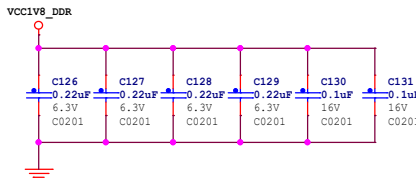
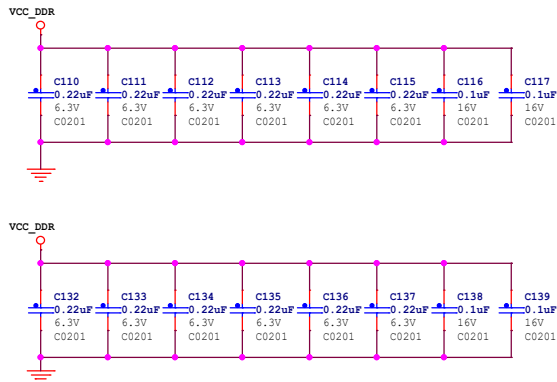
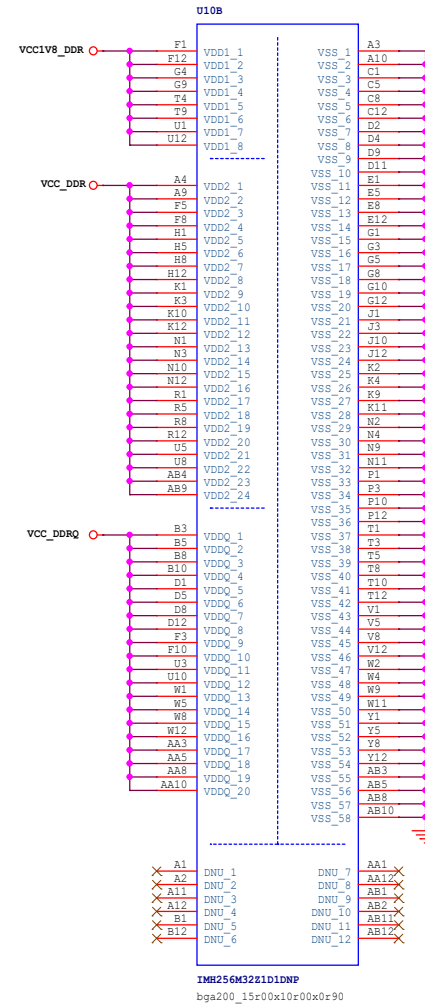
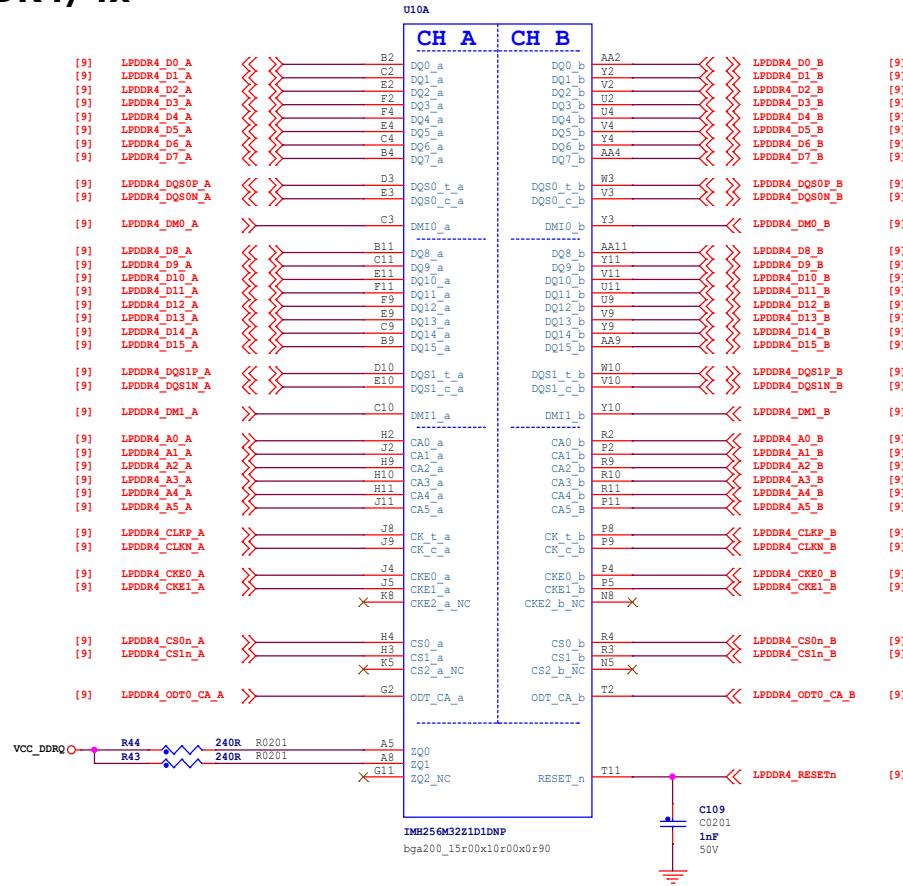


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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A4	Document Number RK3566_Power/GND	Rev V0.2
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RK3566 F (DDR PHY)

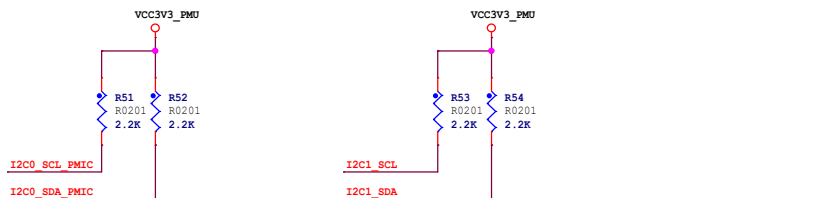


LPDDR4/4x

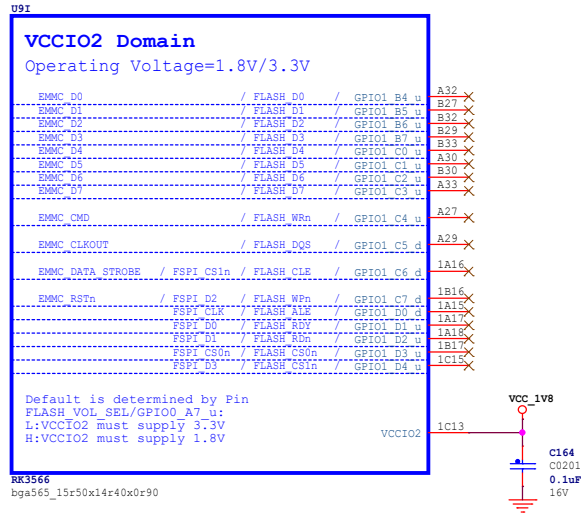


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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
Size	Document Number	Rev
A3	LPDDR4/4x	V0.2
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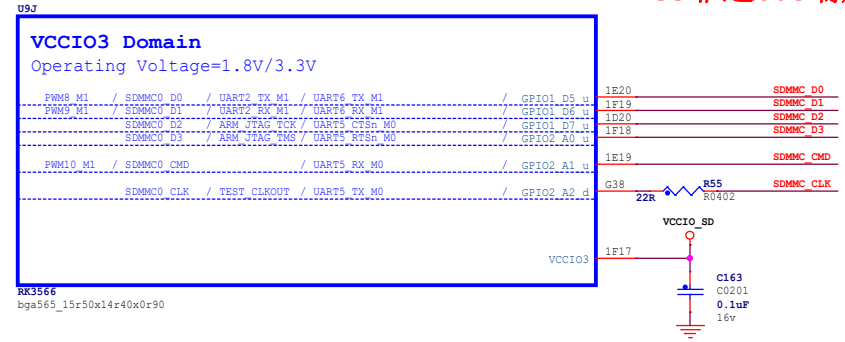
RK3566 G (OSC/PLL/PMUIO1/2)



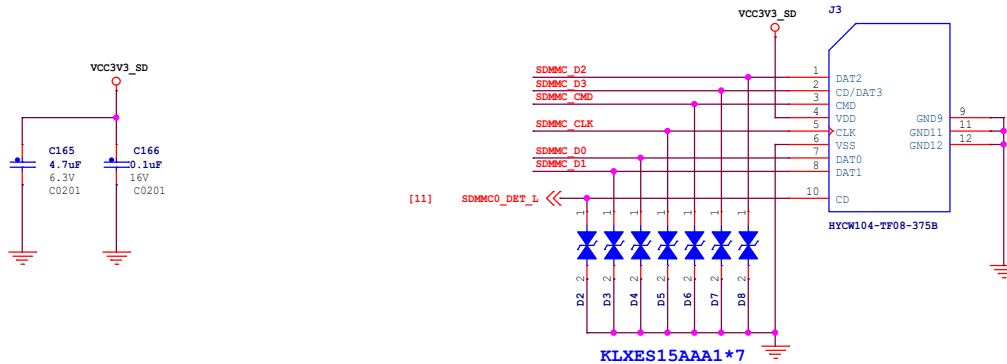
RK3566_I (VCCIO2 Domain) EMMC_IO



RK3566_J (VCCIO3 Domain) SDMMC_IO

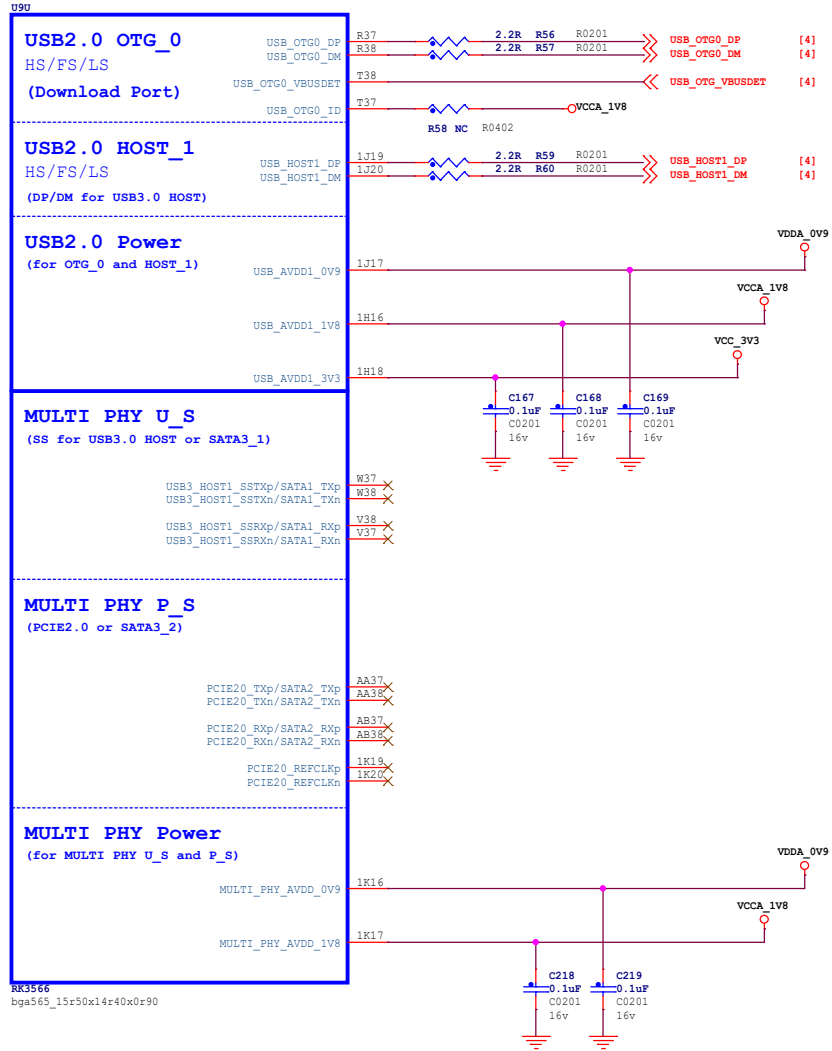


TF_CARD

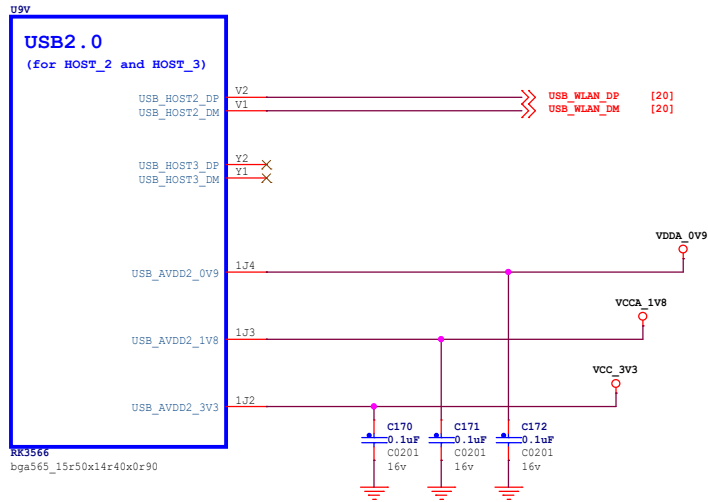


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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A3	Document Number RK3566_EMMC/SD Controller	Rev V0.2
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RK3566_U (USB3.0/SATA/PCIE2.0 x1)



RK3566_V (USB2.0 HOST)



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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
Size	Document Number	Rev
A3	RK3566_USB/PCIE/SATA_PHY	V0.2
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RK3566_R (MIPI_DSI_TX0/LVDS_TX0)

MIPI DSI TX0/LVDS TX0

MIPI_DSI_TX0_D0p/LVDS_TX0_D0p
 MIPI_DSI_TX0_D0n/LVDS_TX0_D0n
 MIPI_DSI_TX0_D1p/LVDS_TX0_D1p
 MIPI_DSI_TX0_D1n/LVDS_TX0_D1n
 MIPI_DSI_TX0_D2p/LVDS_TX0_D2p
 MIPI_DSI_TX0_D2n/LVDS_TX0_D2n
 MIPI_DSI_TX0_D3p/LVDS_TX0_D3p
 MIPI_DSI_TX0_D3n/LVDS_TX0_D3n
 MIPI_DSI_TX0_CLKp/LVDS_TX0_CLKp
 MIPI_DSI_TX0_CLKn/LVDS_TX0_CLKn

AP30
 AR30
 AR29
 AP29
 AP27
 AR27
 AR26
 AP26
 1V16
 1V15

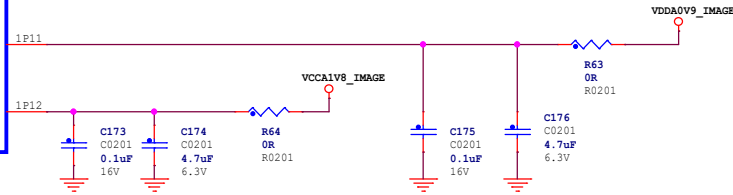
MIPI_DSI_TX0_D0p
 MIPI_DSI_TX0_D0n
 MIPI_DSI_TX0_D1p
 MIPI_DSI_TX0_D1n
 MIPI_DSI_TX0_D2p
 MIPI_DSI_TX0_D2n
 MIPI_DSI_TX0_D3p
 MIPI_DSI_TX0_D3n
 MIPI_DSI_TX0_CLKp
 MIPI_DSI_TX0_CLKn

(21)
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 (21)
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 (21)
 (21)
 (21)
 (21)
 (21)
 (21)

MIPI_DSI_TX0/LVDS_TX0_AVDD_OV9

MIPI_DSI_TX0/LVDS_TX0_AVDD_1V8

RK3566
 bga565_15r50x14r40x0r90



RK3566_S (MIPI_DSI_TX1)

MIPI DSI TX1

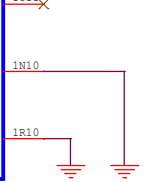
MIPI_DSI_TX1_D0p
 MIPI_DSI_TX1_D0n
 MIPI_DSI_TX1_D1p
 MIPI_DSI_TX1_D1n
 MIPI_DSI_TX1_D2p
 MIPI_DSI_TX1_D2n
 MIPI_DSI_TX1_D3p
 MIPI_DSI_TX1_D3n
 MIPI_DSI_TX1_CLKp
 MIPI_DSI_TX1_CLKn

AP24
 AR24
 AR23
 AP23
 AP21
 AR21
 AR20
 AP20
 1V11
 1U11

MIPI_DSI_TX1_AVDD_OV9

MIPI_DSI_TX1_AVDD_1V8

RK3566
 bga565_15r50x14r40x0r90



RK3566_T (eDP/DP TX)

eDP_TX

EDP_TX_D0p
 EDP_TX_D0n
 EDP_TX_D1p
 EDP_TX_D1n
 EDP_TX_D2p
 EDP_TX_D2n
 EDP_TX_D3p
 EDP_TX_D3n
 EDP_TX_AUXp
 EDP_TX_AUXn

EDP_TX_AVDD_OV9

EDP_TX_AVDD_1V8

RK3566
 bga565_15r50x14r40x0r90



RK3566_Q (HDMI2.0 TX)

HDMI2.0 TX

HDMI_TX_D2p
 HDMI_TX_D2n
 HDMI_TX_D1p
 HDMI_TX_D1n
 HDMI_TX_D0p
 HDMI_TX_D0n
 HDMI_TX_CLKp
 HDMI_TX_CLKn

AP36
 AR36
 AR35
 AP35
 AP33
 AR33
 AR32
 AP32

2.2R
 2.2R
 2.2R
 2.2R
 2.2R
 2.2R
 2.2R
 2.2R

R65
 R66
 R67
 R68
 R69
 R70
 R71
 R72

R0201
 R0201
 R0201
 R0201
 R0201
 R0201
 R0201
 R0201

HDMI_TX_D2p
 HDMI_TX_D2n
 HDMI_TX_D1p
 HDMI_TX_D1n
 HDMI_TX_D0p
 HDMI_TX_D0n
 HDMI_TX_CLKp
 HDMI_TX_CLKn

(19)
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HDMI_TX_HPDIN

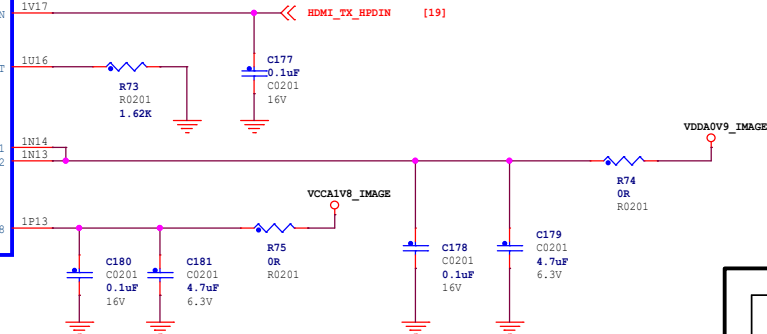
HDMI_TX_REXT

HDMI_TX_AVDD_OV9_1

HDMI_TX_AVDD_OV9_2

HDMI_TX_AVDD_1V8

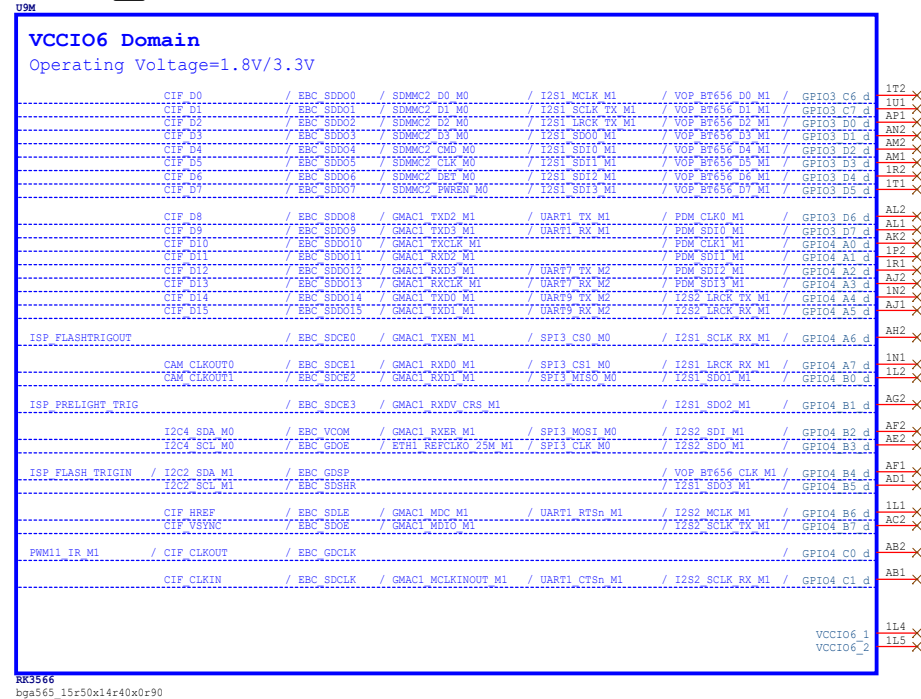
RK3566
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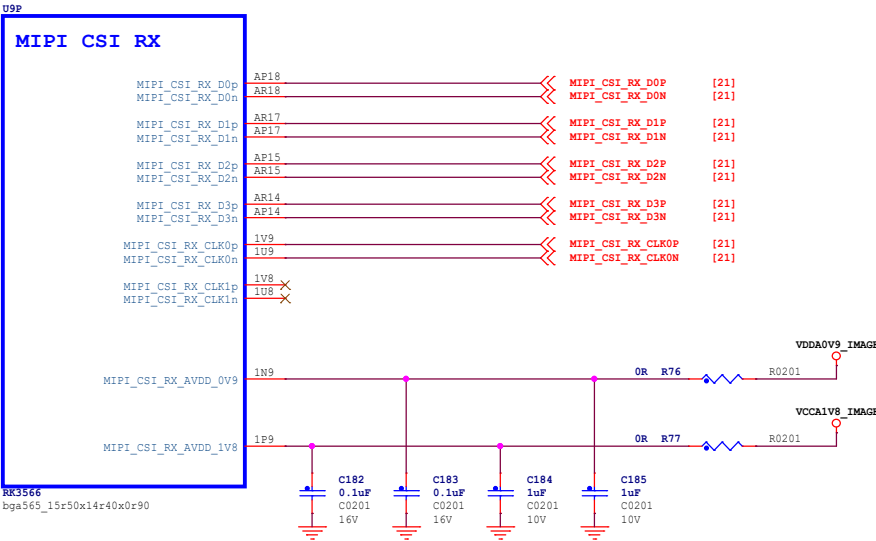
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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A3	Document Number MIPI_DSI/EDP/HDMI_Controller	Rev V0.2
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RK3566_M(VCCIO6 Domain)
GMAC_Controller



RK3566_P(MIPI_CSI_RX)



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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
Size	Document Number	Rev
A3	MIPI_CSI/GMAC_Controller	V0.2
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D

C

4

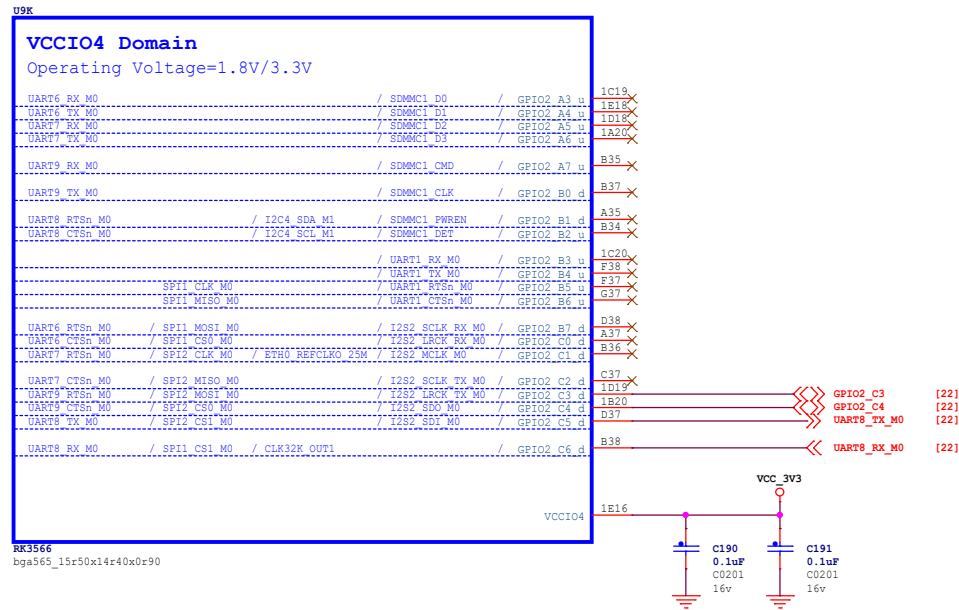
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A

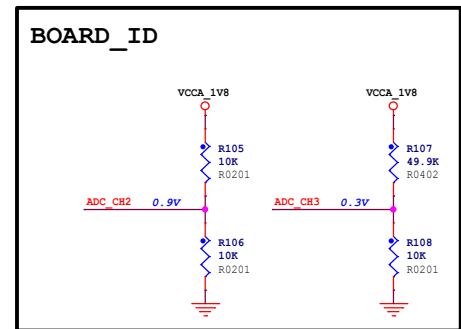
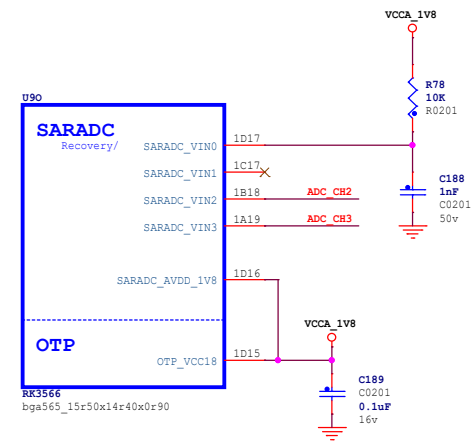


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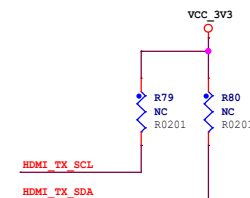
RK3566_K (VCCIO4 Domain)



RK3566_O (SARADC/OTP)

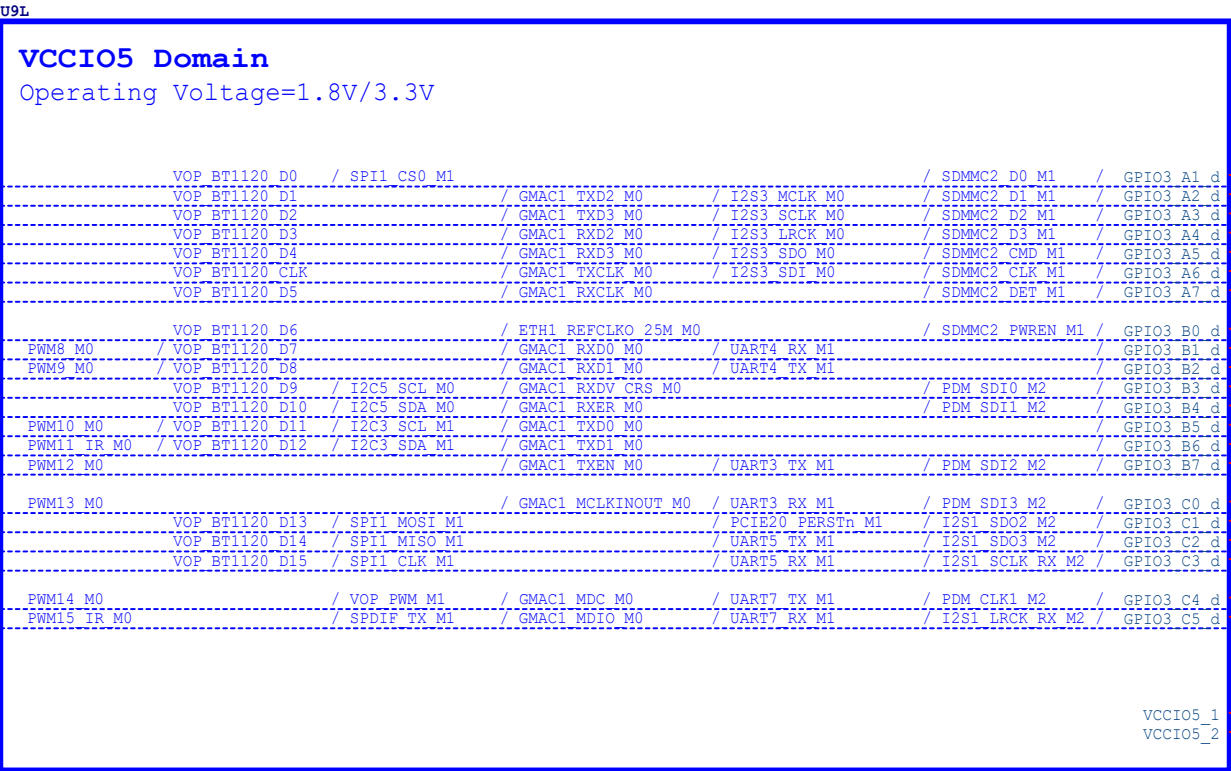


RK3566_N (VCCIO7 Domain)



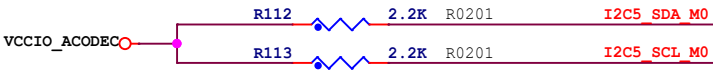
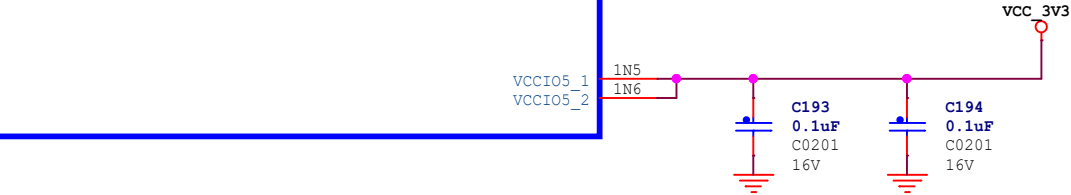
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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图			
Size A3	Document Number RK3566_SARADC/GPIO		Rev V0.2
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RK3566_L (VCCIO5 Domain)



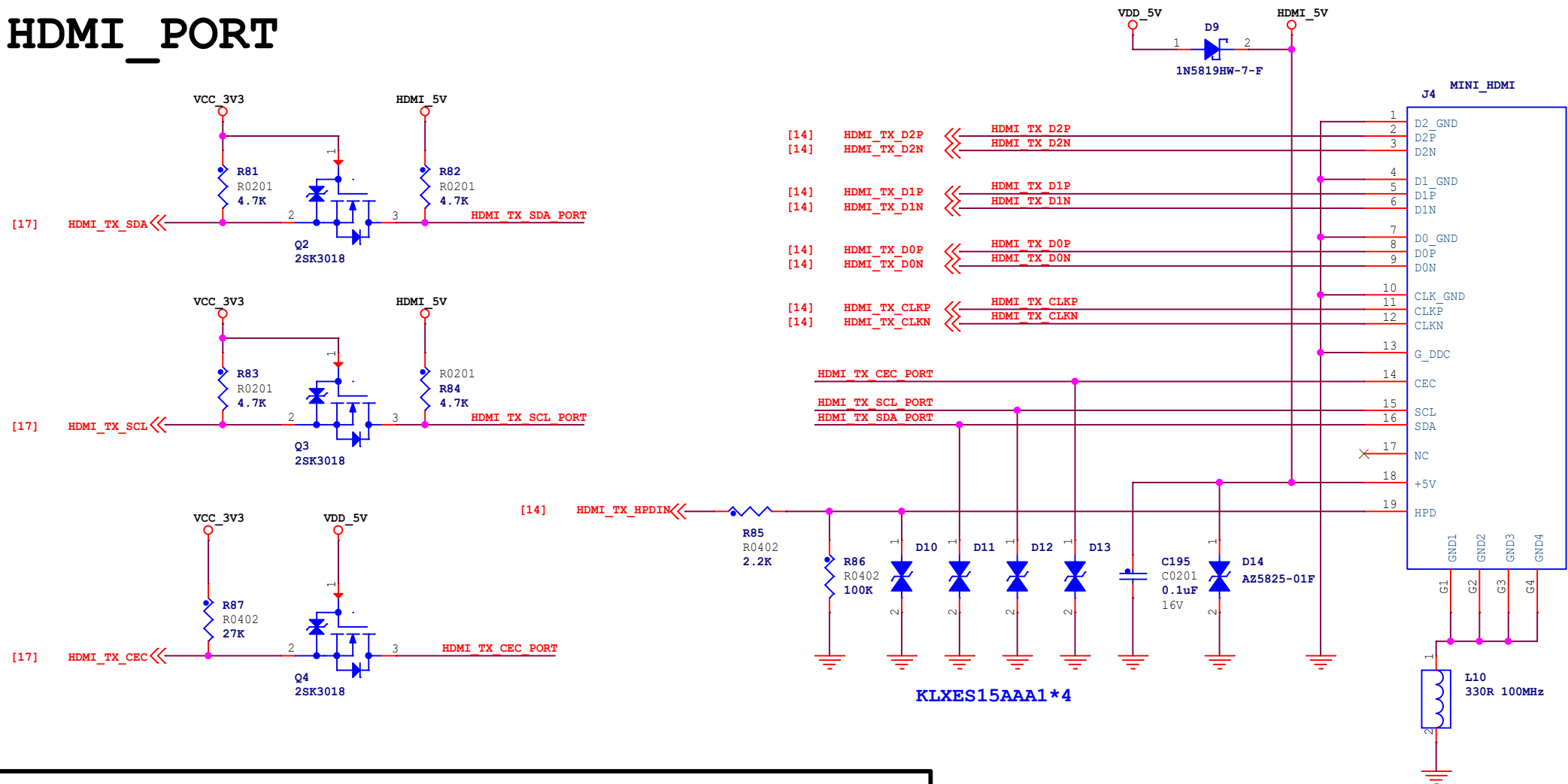
RK3566

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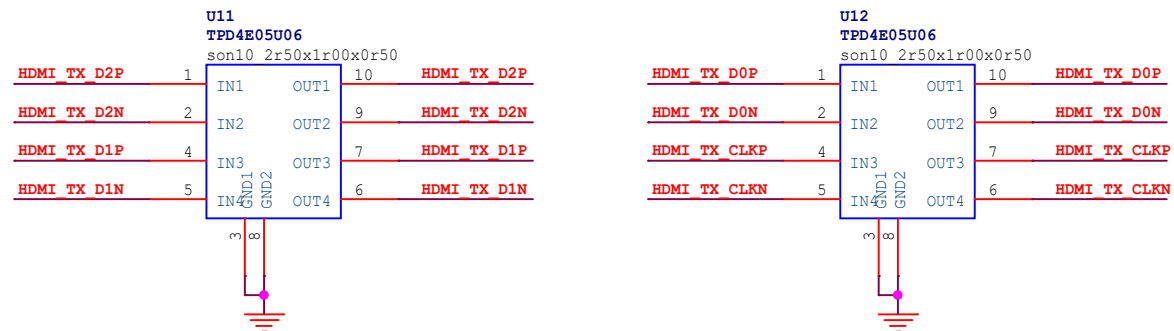


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HDMI PORT



ESD



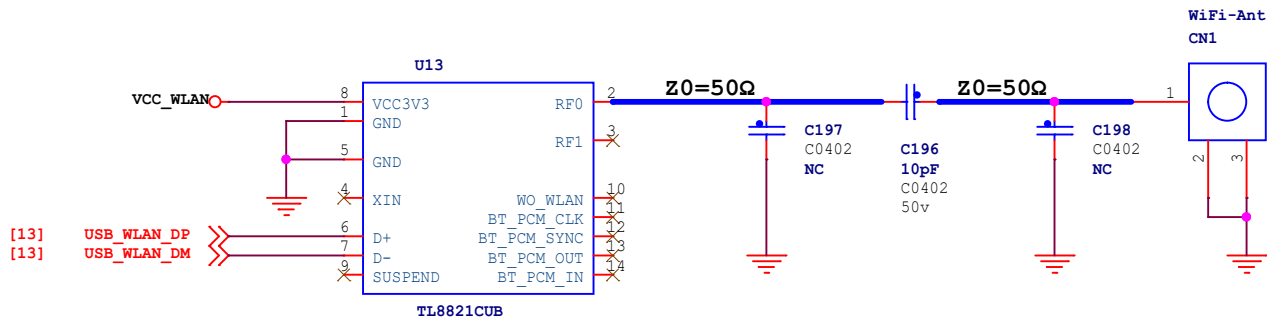
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Title	野火 RK3566 LUBANCAT ZERO WIFI 原理图
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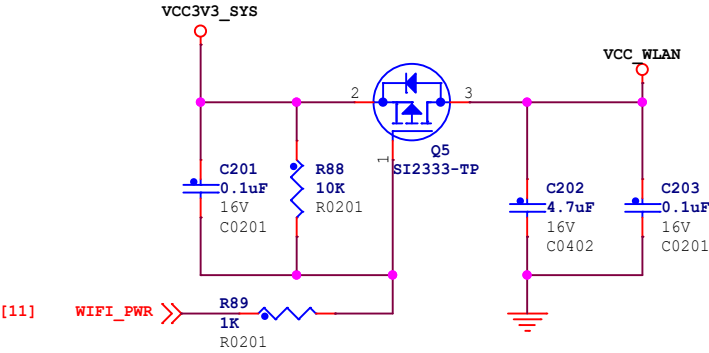
Size	Document Number	Rev
A4	HDMI_PORT	V0.2

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USB_WLAN

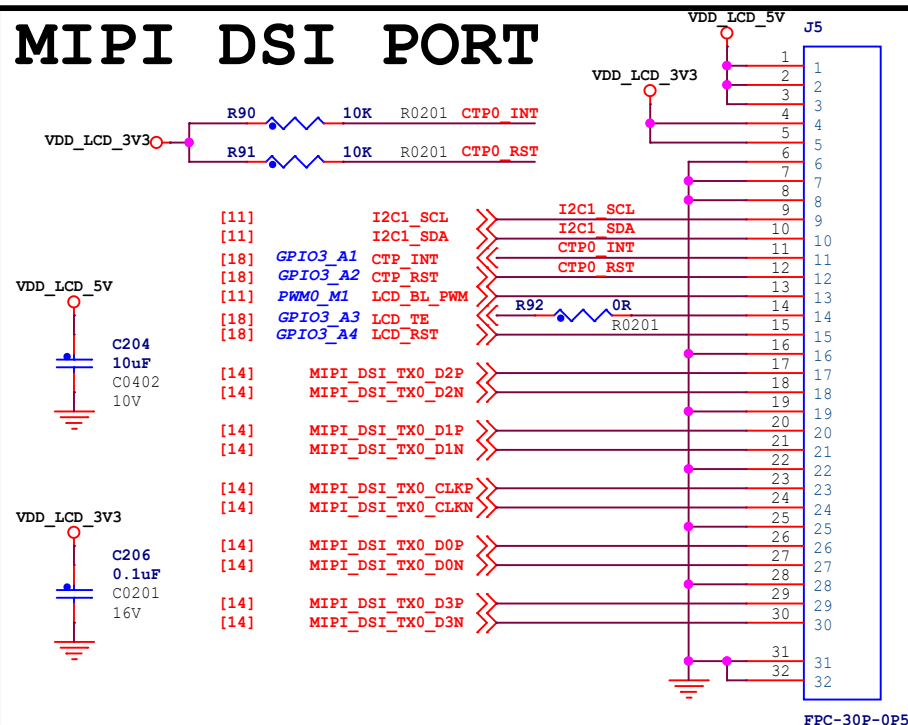


WLAN_Power

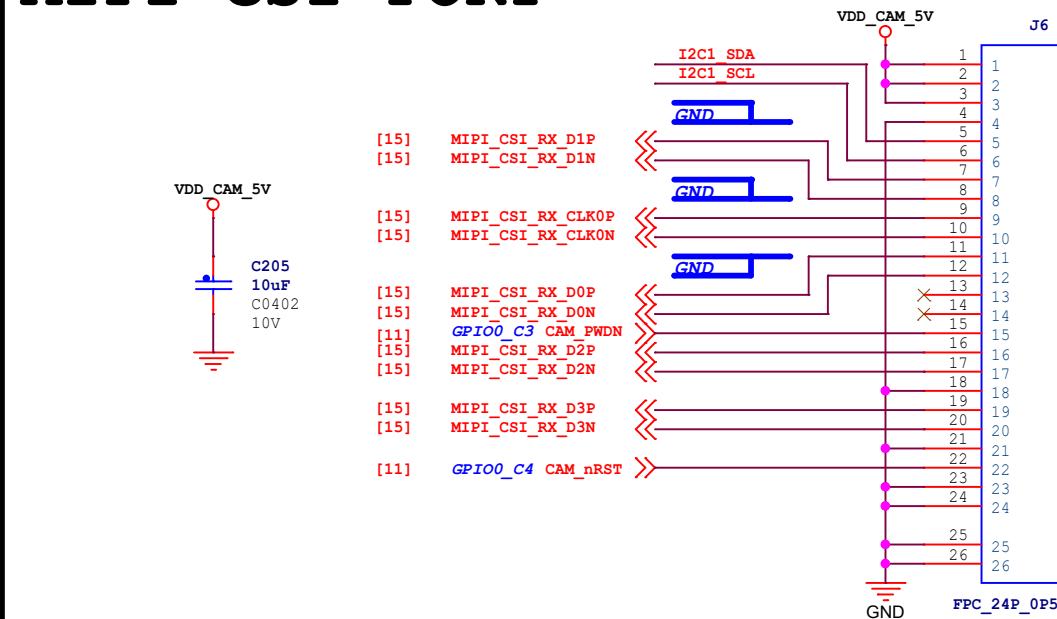


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Size A4	Document Number USB_WLAN	Rev V0.2
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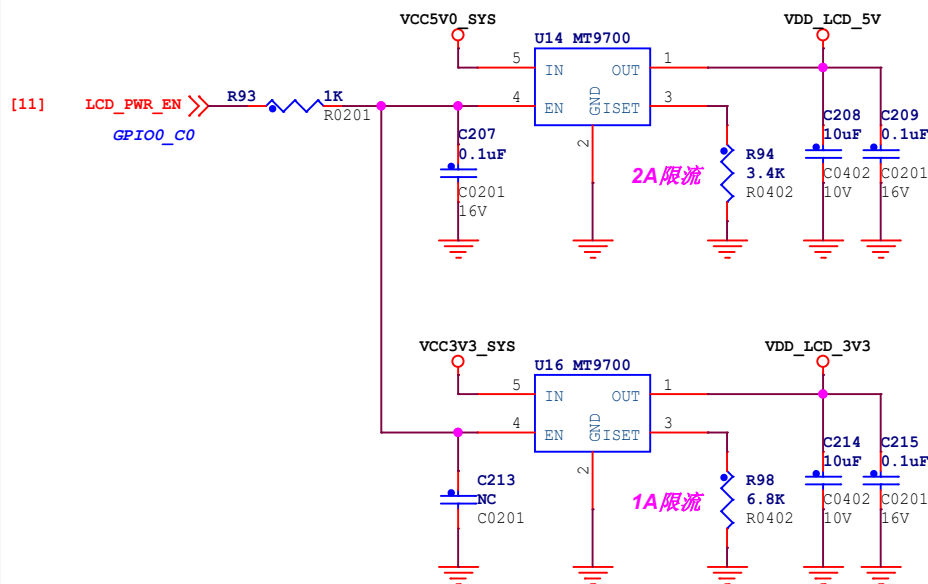
MIPI DSI PORT



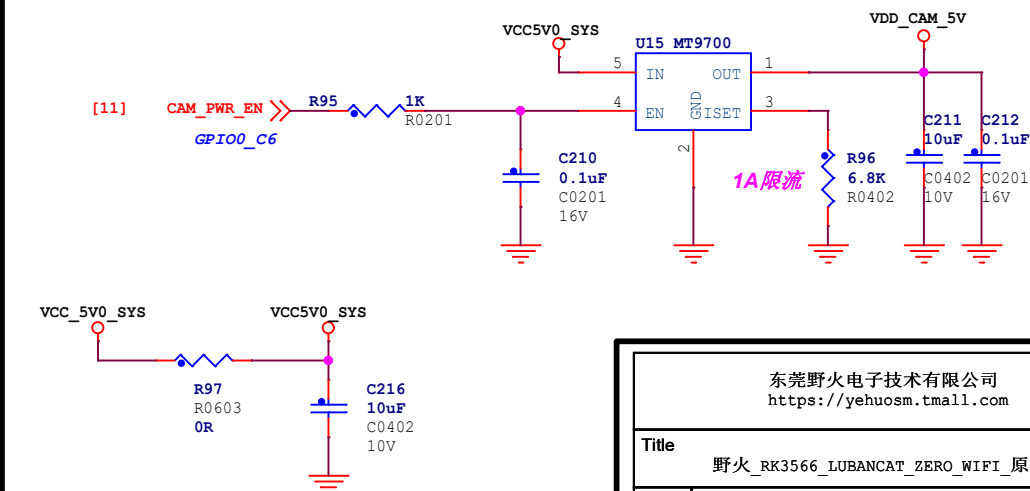
MIPI CSI PORT



LCD Power



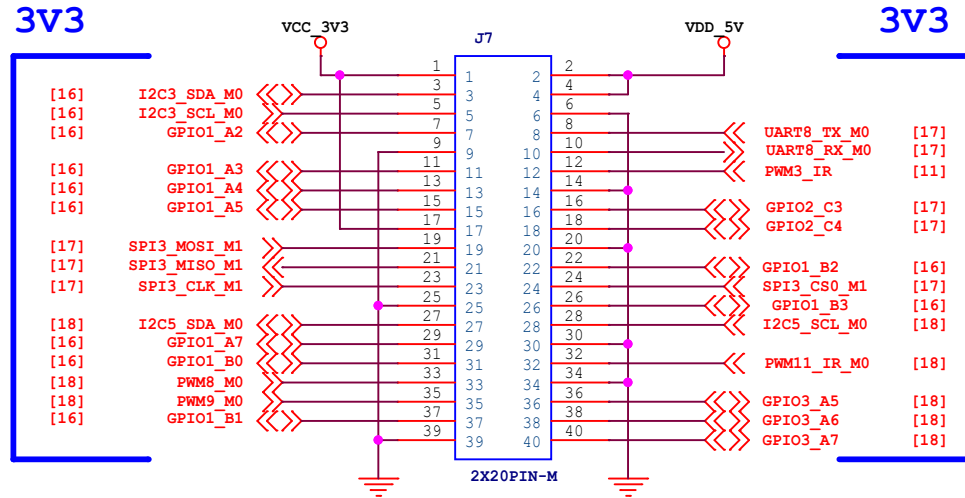
MIPI CSI Power



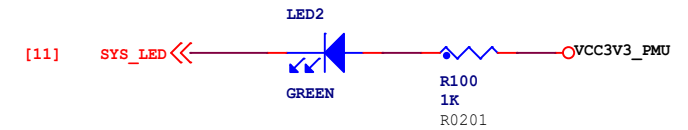
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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
Size A4	Document Number MIPI DSI/MIPI CSI_PORT	Rev V0.2
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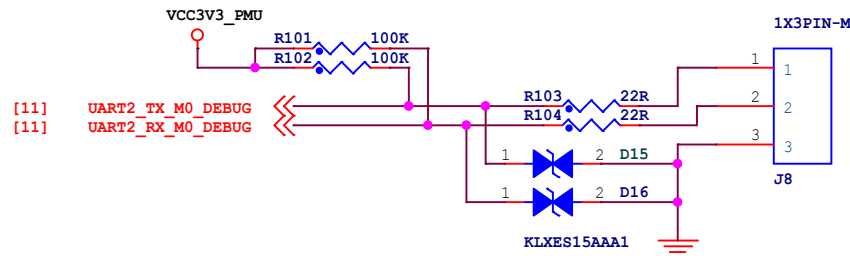
Pin Out



SYS_LED



Debug UART2



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Title 野火_RK3566_LUBANCAT_ZERO_WIFI_原理图		
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