

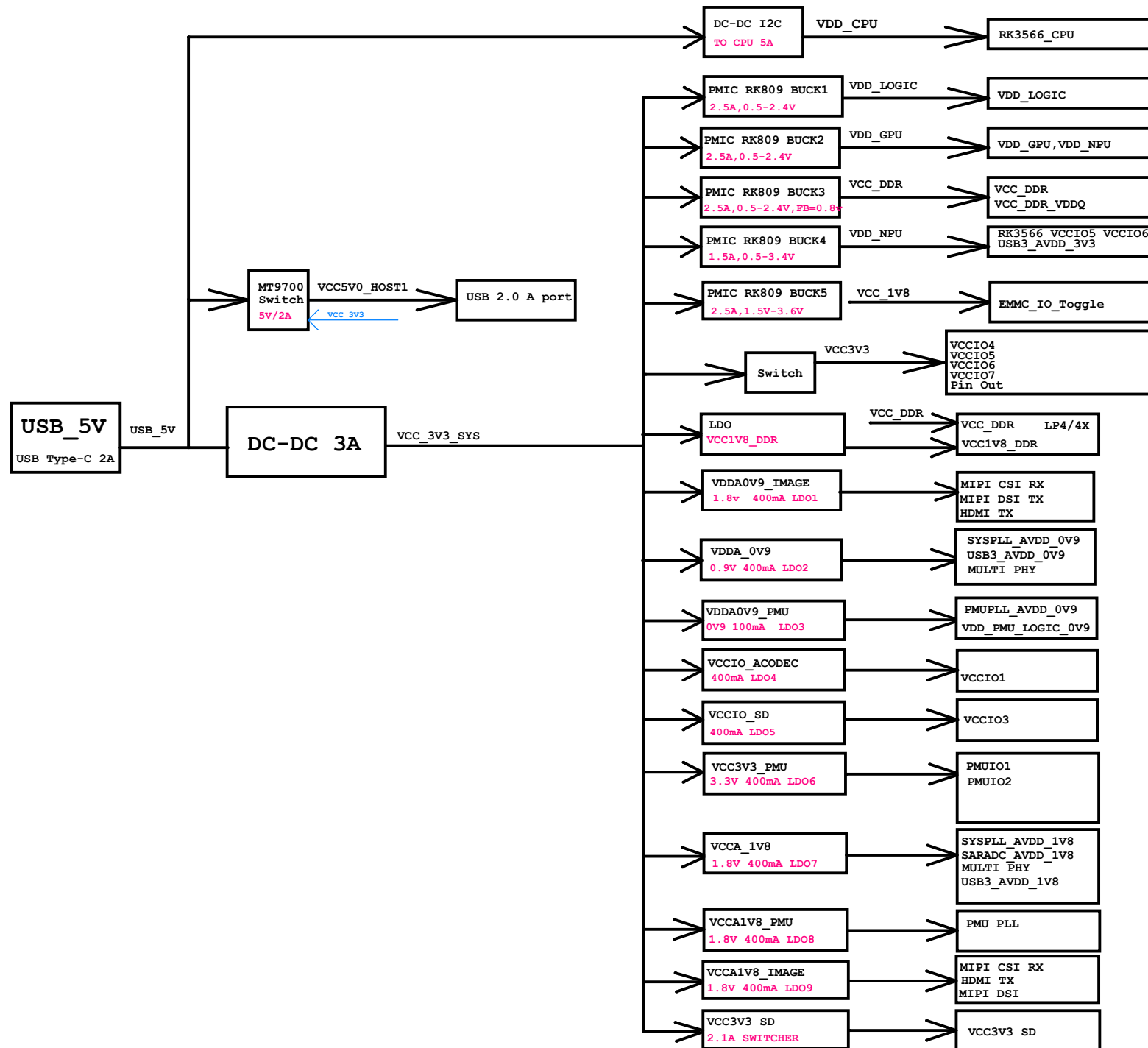
野火_RK3566_LUBANCAT_ZERO_PHY_原理图

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历史版本			
版本号	日期	设计	描述
V0.1	2022-04-18	ghs	初始版本

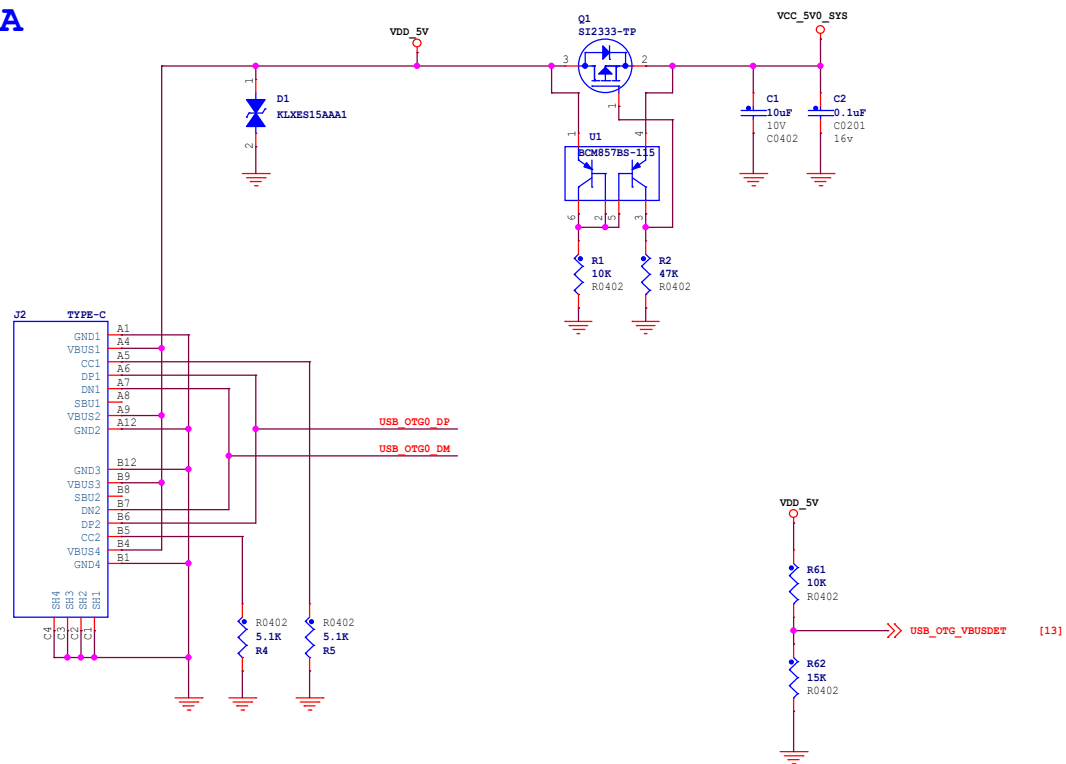
POWER DIAGRAM



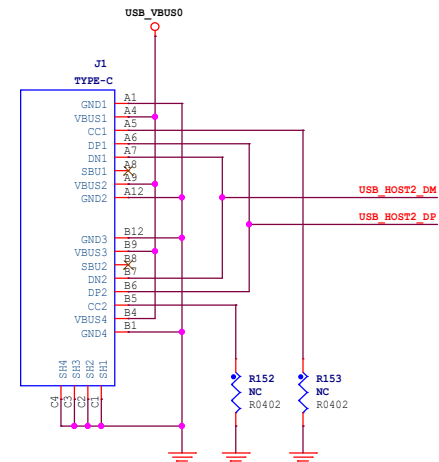
POWER IN

OTG Download

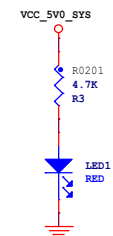
5V 3A



USB 2.0 HOST x1

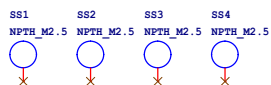


PWR_LED

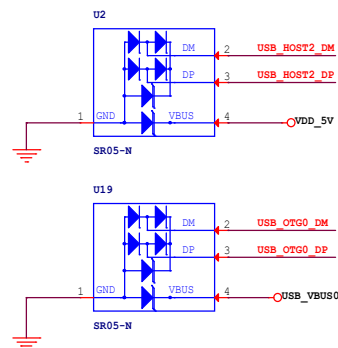


机械定位孔

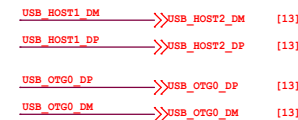
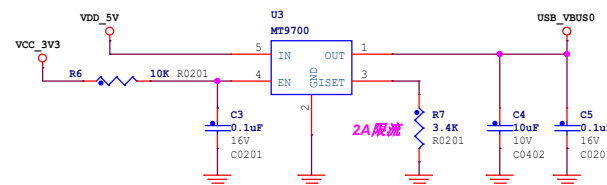
4*M2.5



USB ESD

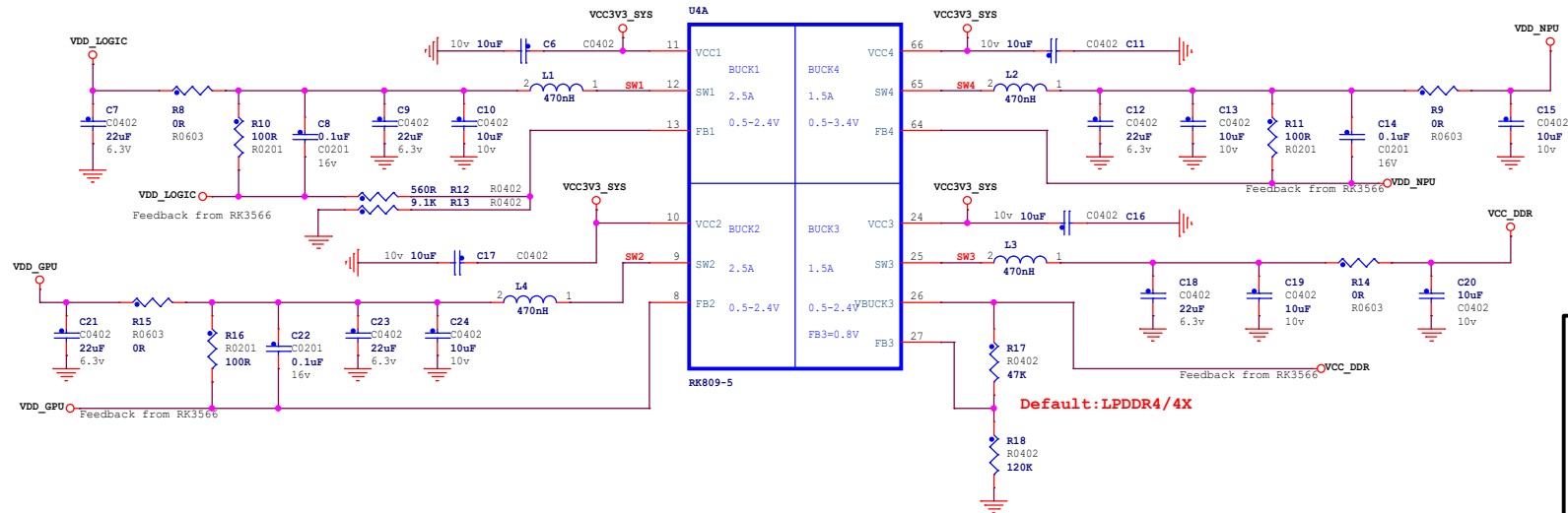


USB POWER



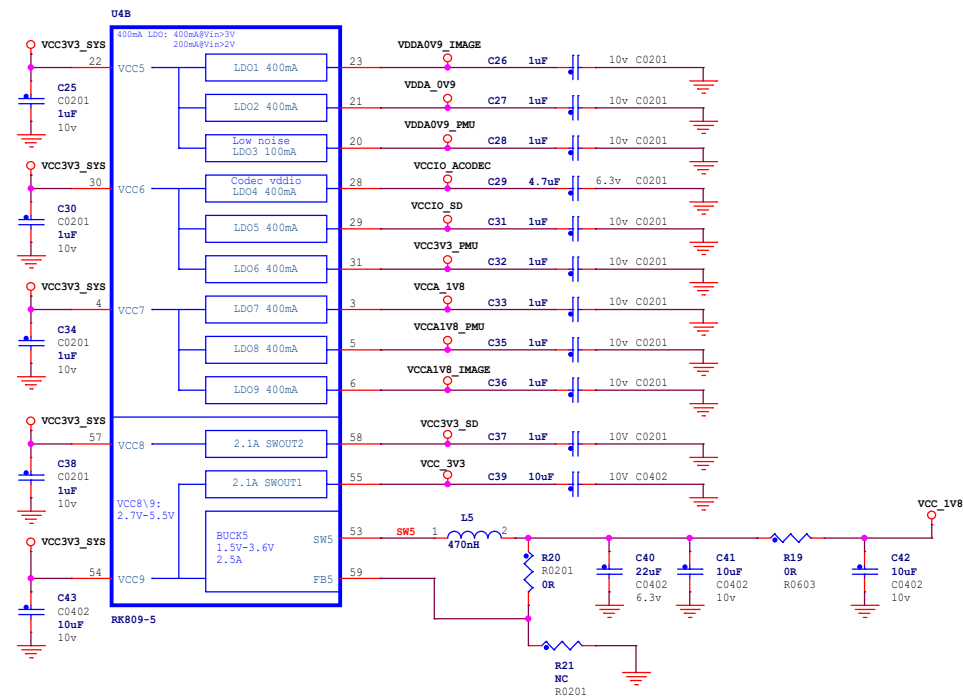
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Title	野火_RK3566_LUBANCAT_ZERO_WIFI_原理图	
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PMIC RK809 DCDC



RK3566 BUCK FB

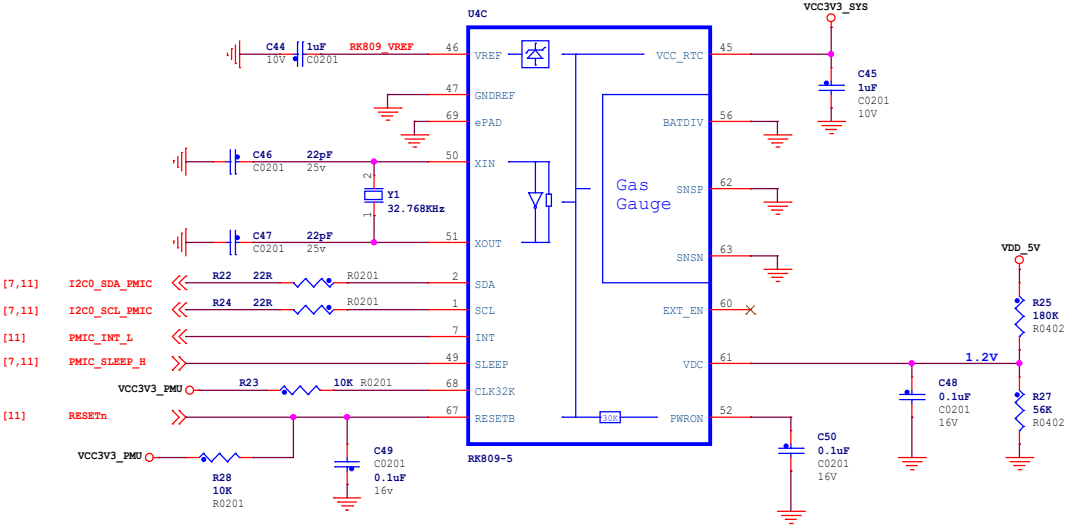
PMIC RK809 LDO



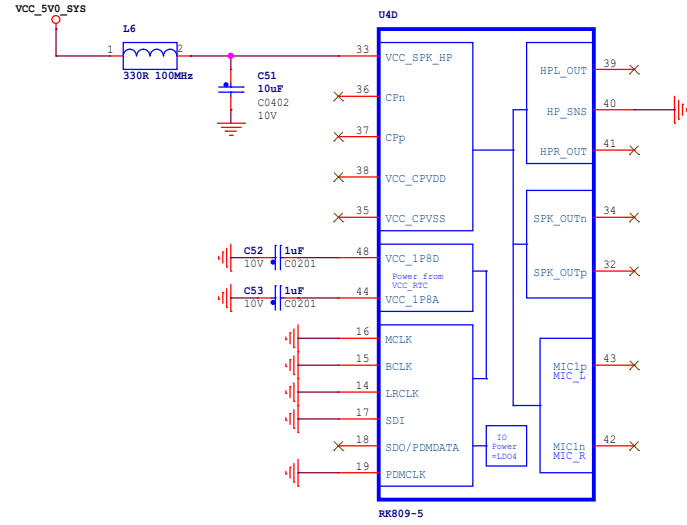
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https://yehuosm.tmall.com

Title		
野火_RK3566_LUBANCAT_ZERO_PHY_原理图		
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PMIC RK809 Managerment

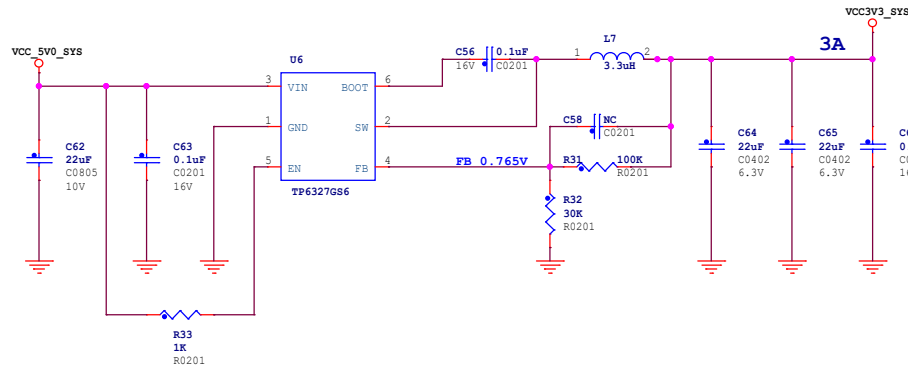


PMIC RK809 CODEC

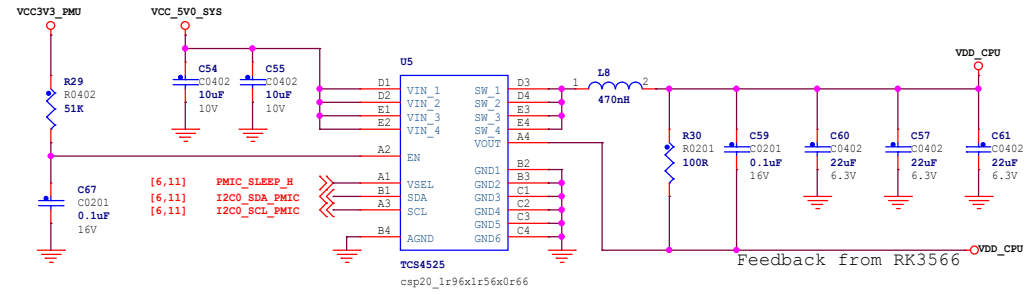


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Title	野火_RK3566_LUBANCAT_ZERO_PHY_原理图	
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A3	PMIC_Managerment/CODE	V0.1
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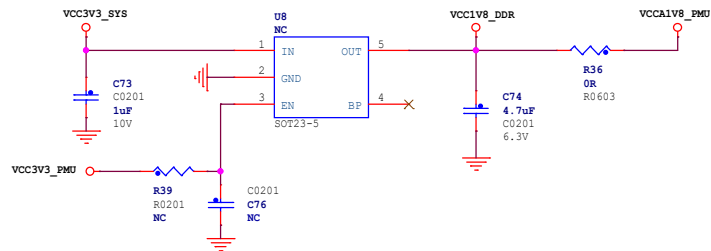
SYS_Power



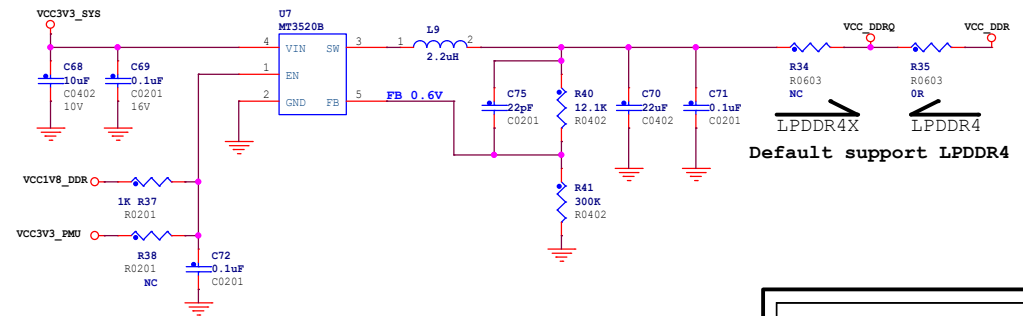
RK3566 CPU Power VDD_CPU_EXT



Auxiliary power supply LPDDR4_VCC1V8_DDR



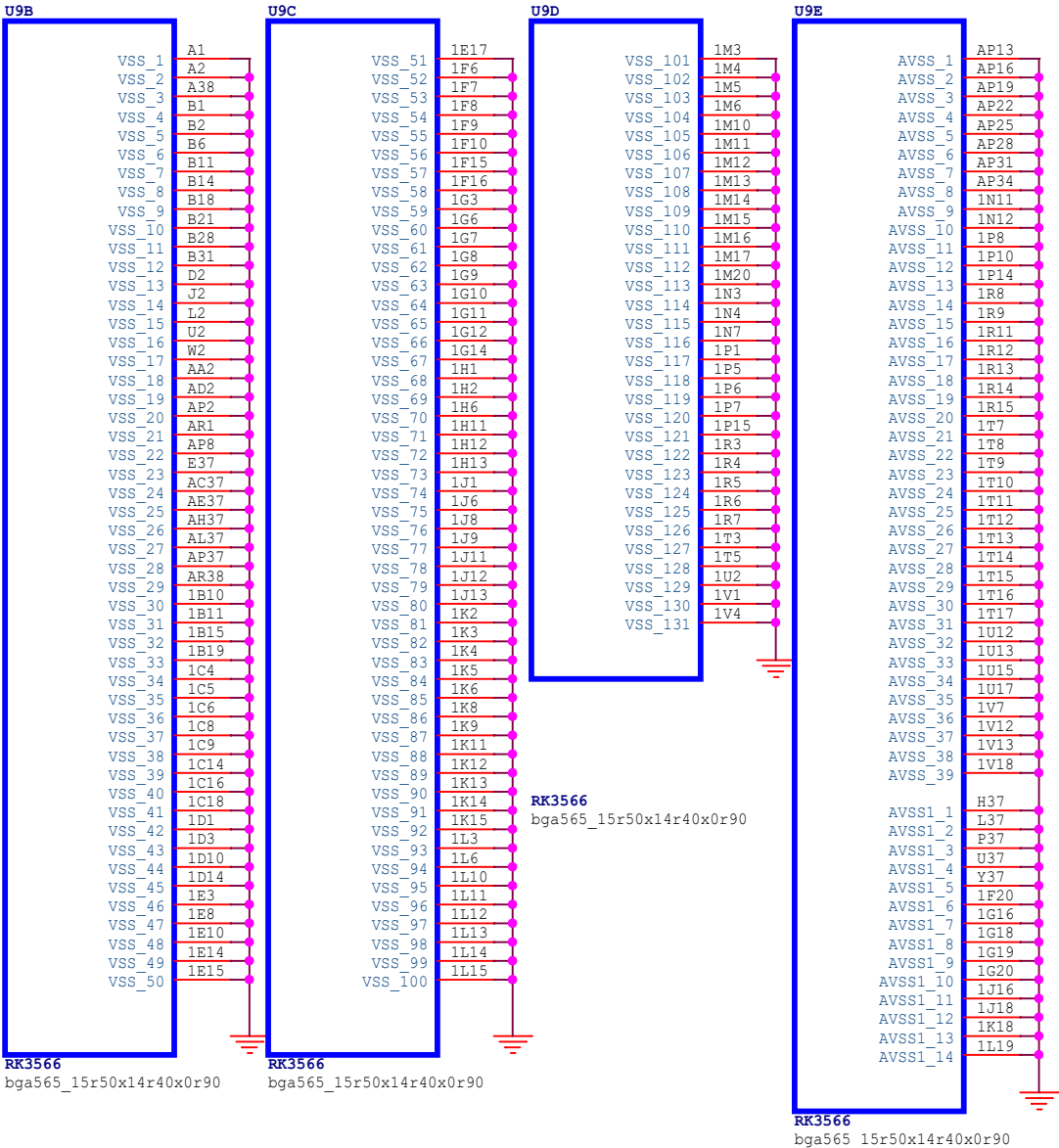
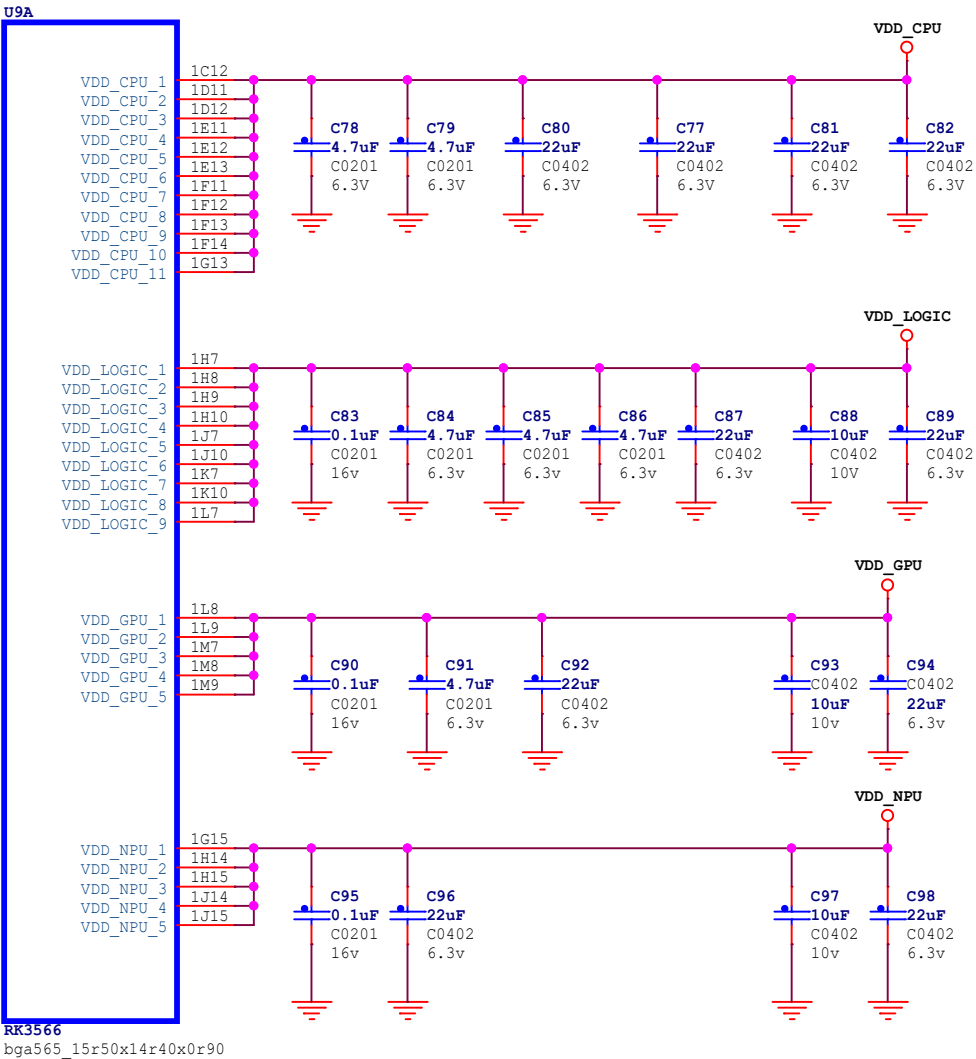
LPDDR4X Power output: 0.6V



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A3	Power_other	V0.1
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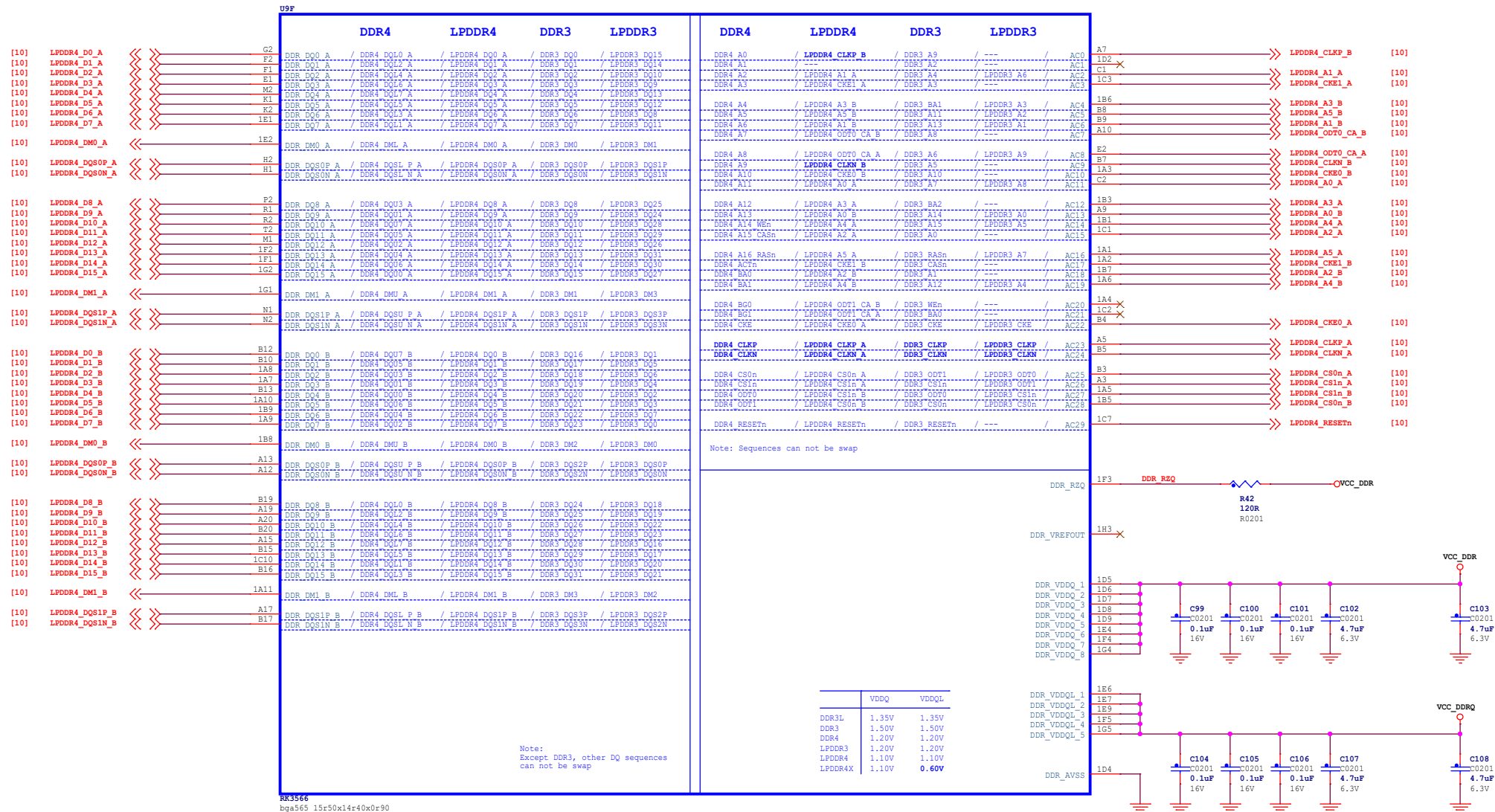
RK3566_ABCDE

(Power&GND)



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Size A4	Document Number RK3566_Power/GND	Rev V0.1
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RK3566_F (DDR PHY)

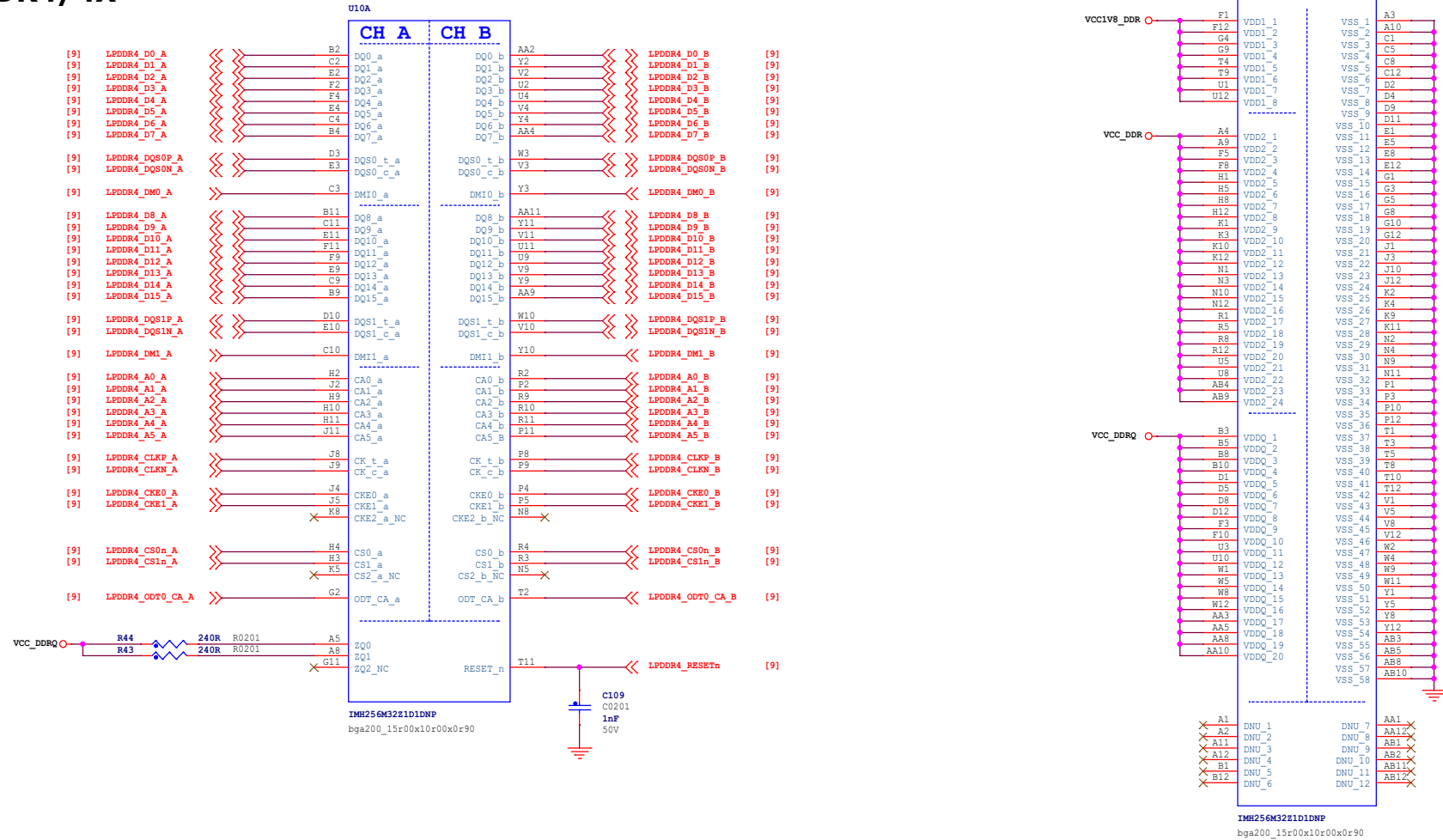


	VDDQ	VDDQL
DDR3L	1.35V	1.35V
DDR3	1.50V	1.50V
DDR4	1.20V	1.20V
LPDDR3	1.20V	1.20V
LPDDR4	1.10V	1.10V
LPDDR4X	1.10V	0.60V

Note:
Except DDR3, other DQ sequences
can not be swap

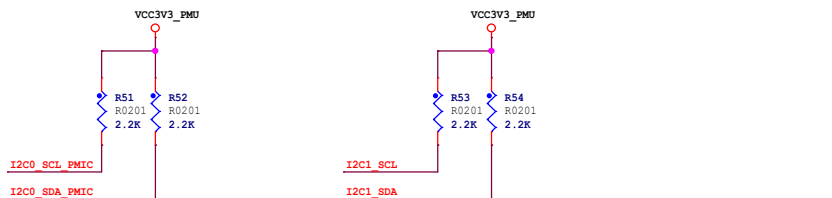
RK3566
bqa565 15r50x14r40x0r90

LPDDR4/4X

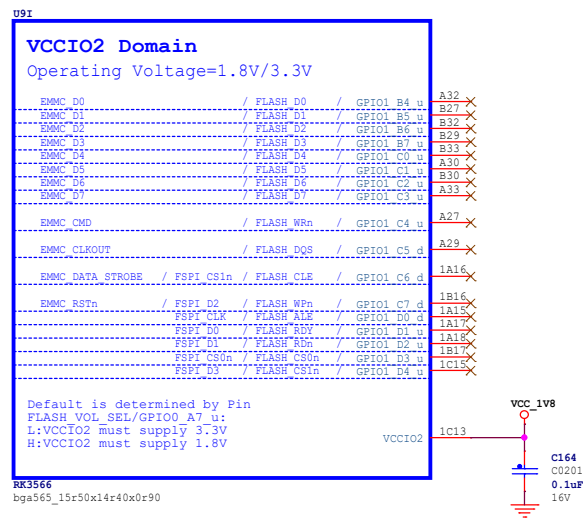


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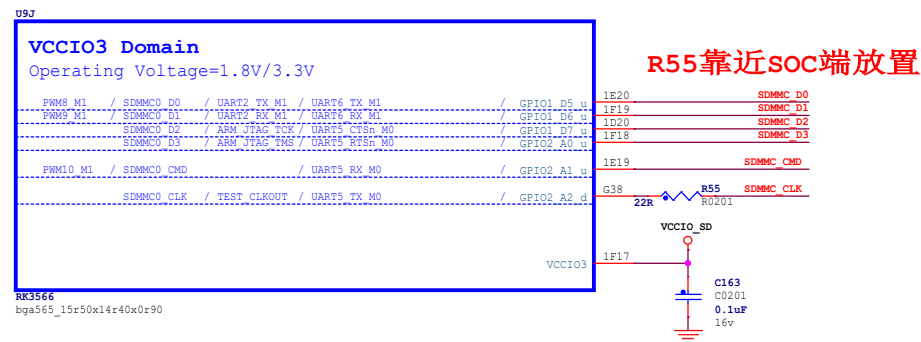
RK3566 G (OSC/PLL/PMUIO1/2)



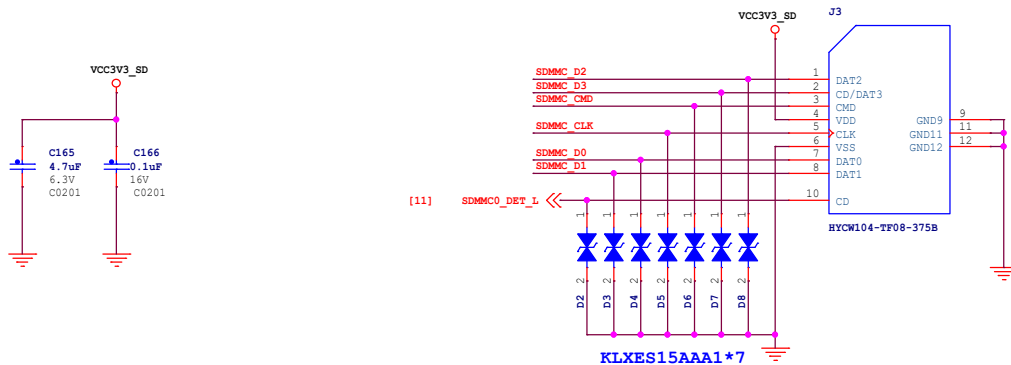
RK3566_I (VCCIO2 Domain) EMMC_IO



RK3566_J (VCCIO3 Domain) SDMMC_IO

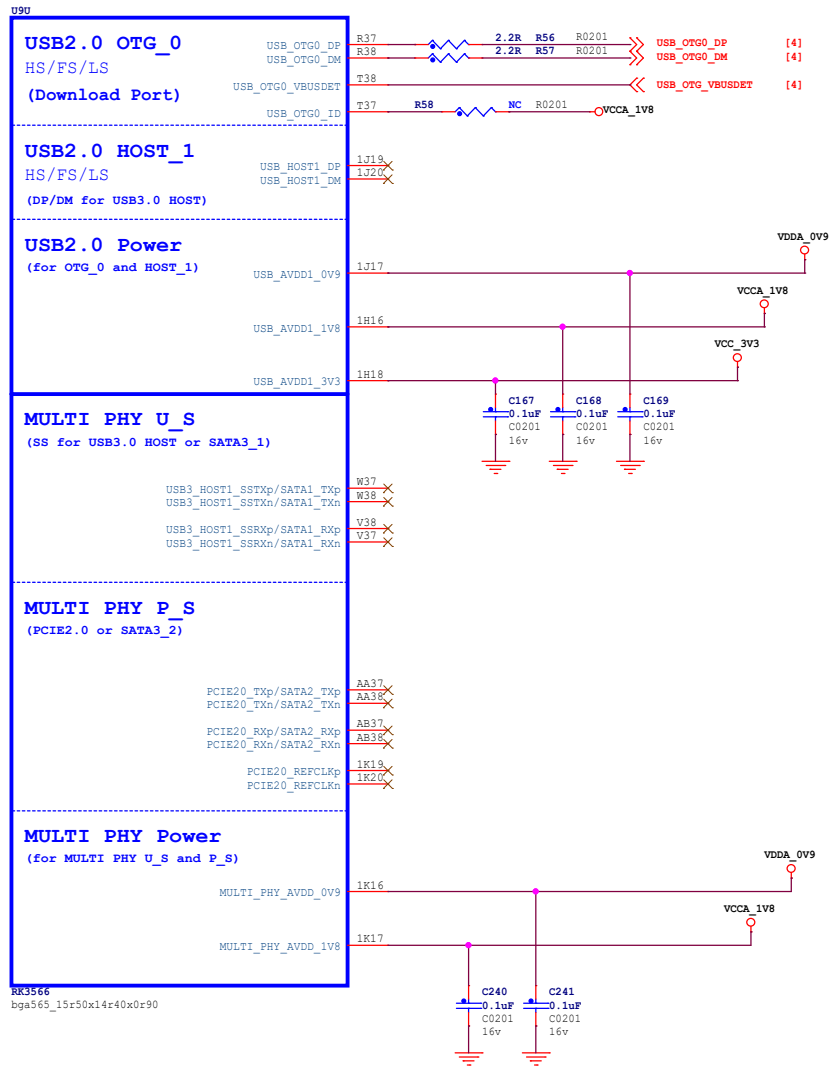


TF_CARD

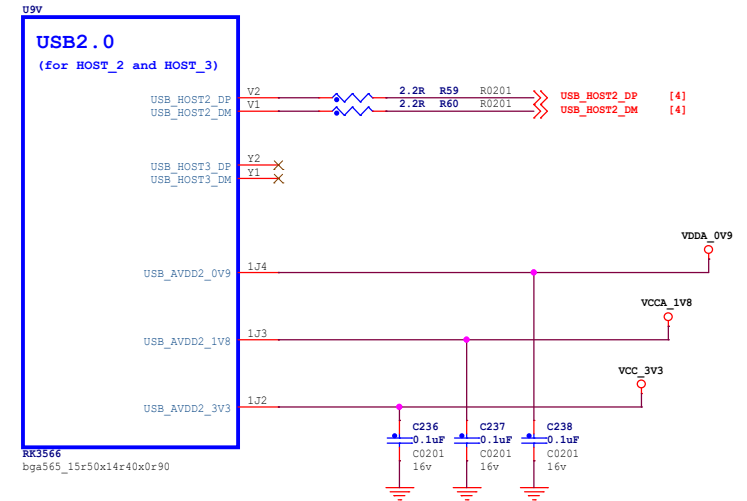


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RK3566_U (USB3.0/SATA/PCIE2.0 x1)



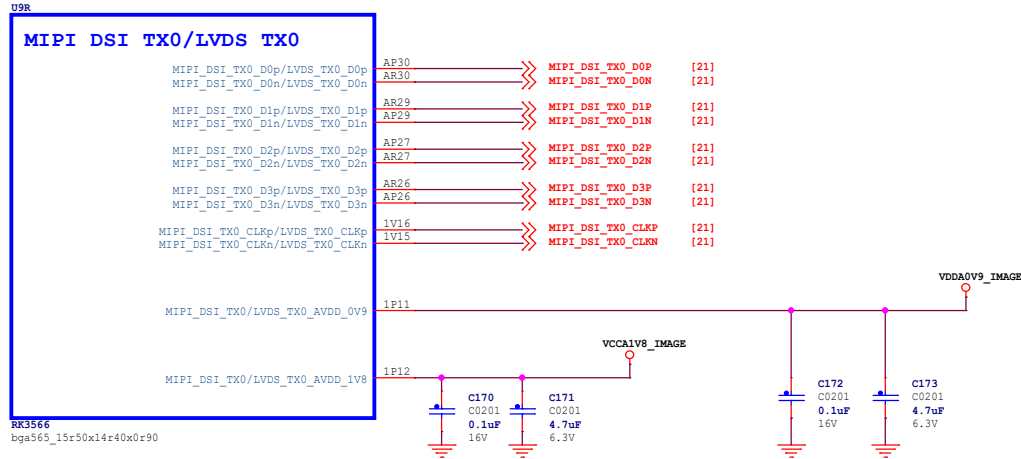
RK3566_V (USB2.0 HOST)



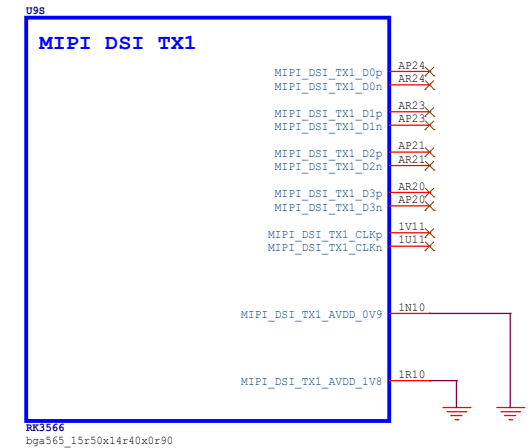
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Title 野火_RK3566_LUBANCAT_ZERO_PHY_原理图		
Size A3	Document Number RK3566_USB/PCIE/SATA_PHY	Rev V0.1
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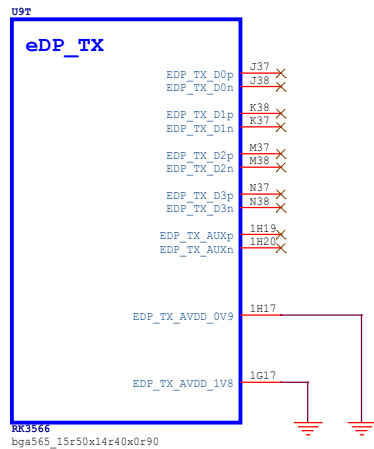
RK3566_R (MIPI_DSI_TX0/LVDS_TX0)



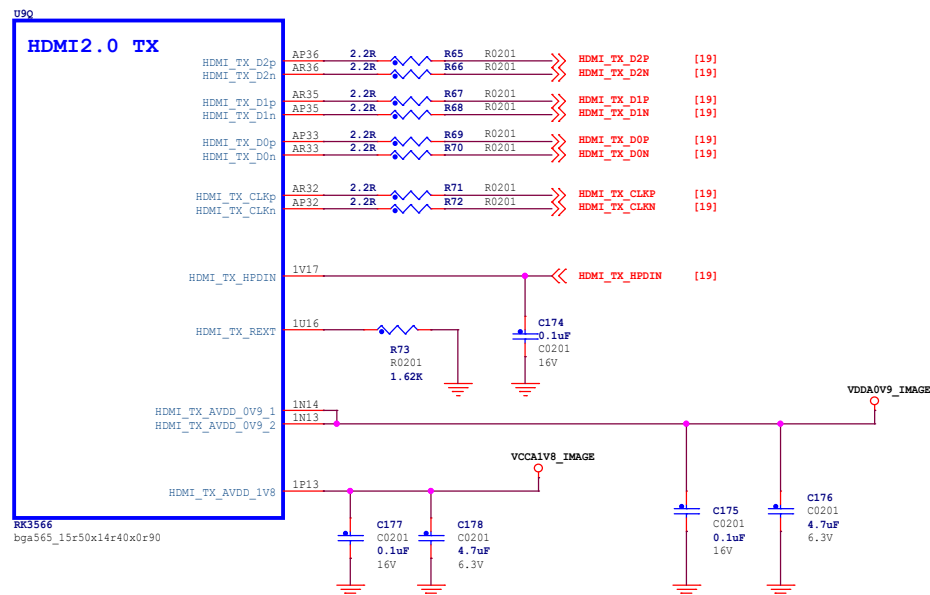
RK3566_S (MIPI_DSI_TX1)



RK3566_T (eDP/DP TX)



RK3566_Q (HDMI2.0 TX)

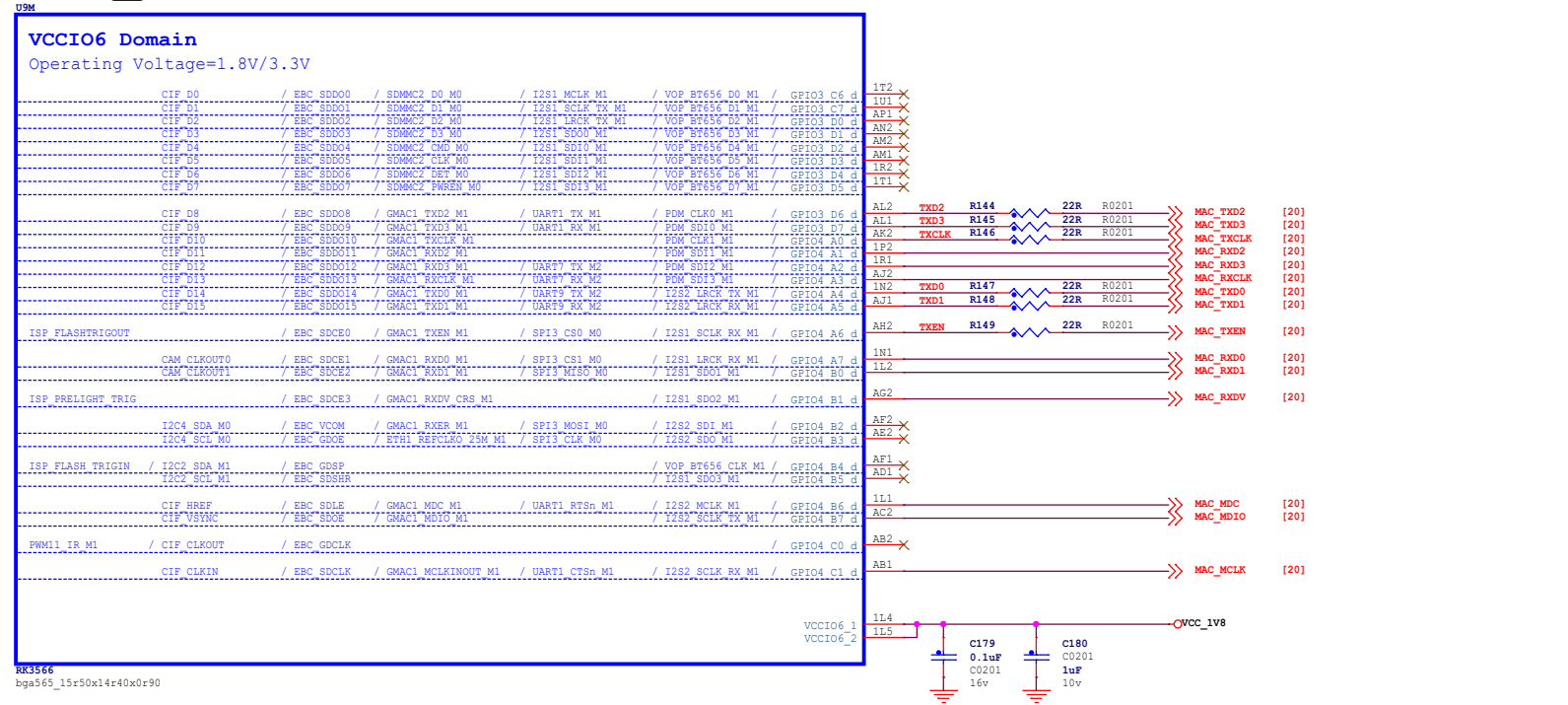


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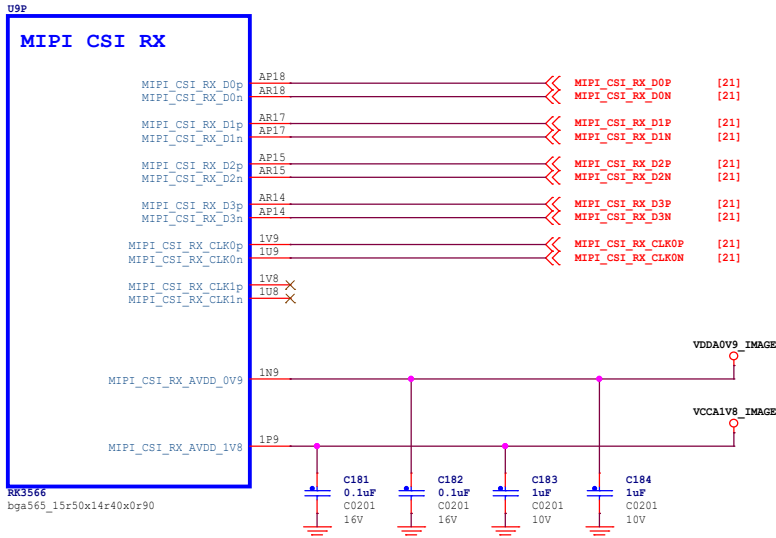
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RK3566_M(VCCIO6 Domain)

GMAC_Controller



RK3566_P(MIPI_CSI_RX)

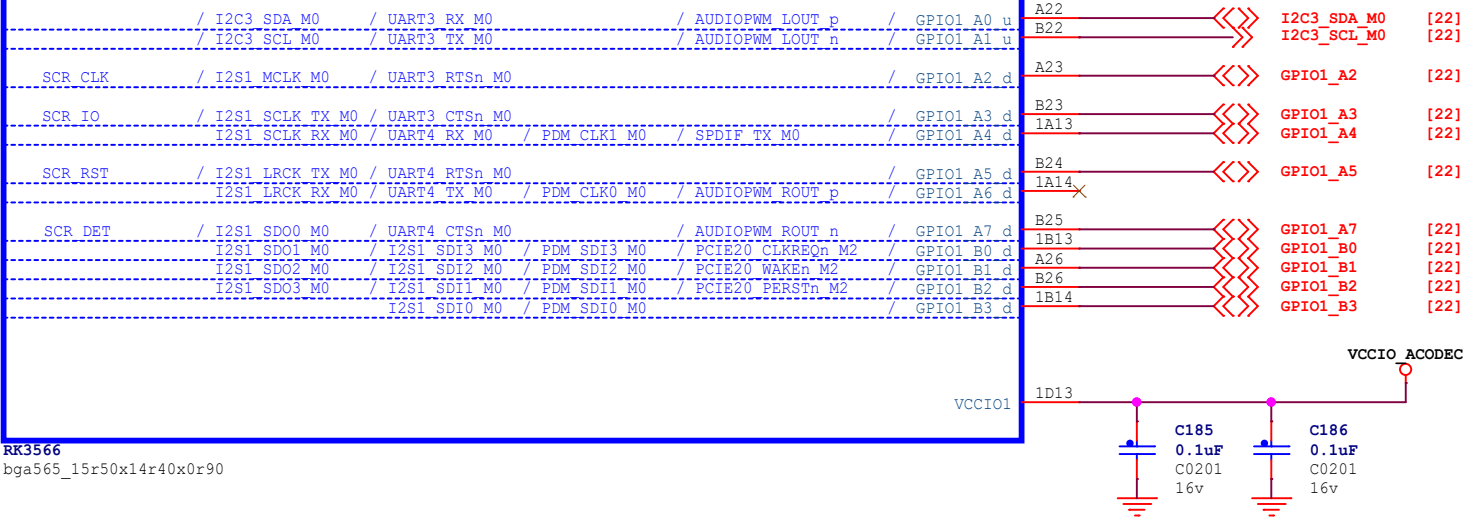


RK3566_H (VCCIO1 Domain)

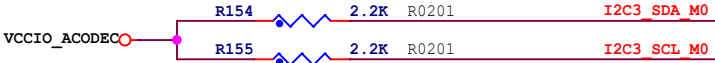
U9H

VCCIO1 Domain

Operating Voltage=1.8V/3.3V



RK3566
bga565_15r50x14r40x0r90



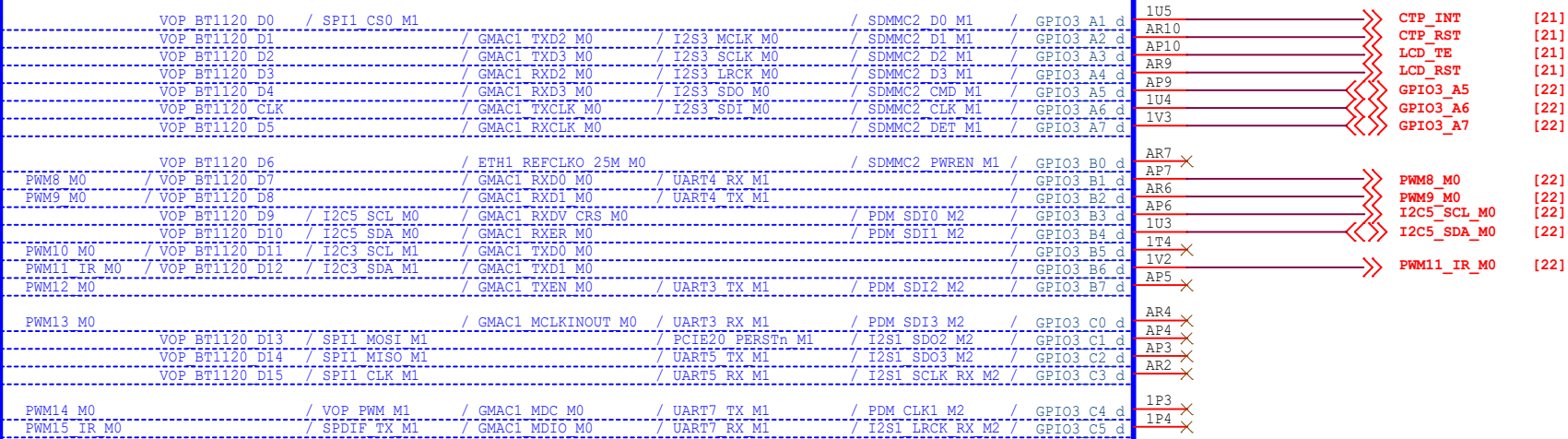
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RK3566_L (VCCIO5 Domain)

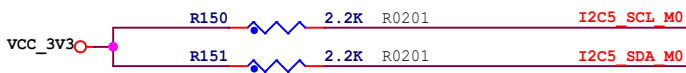
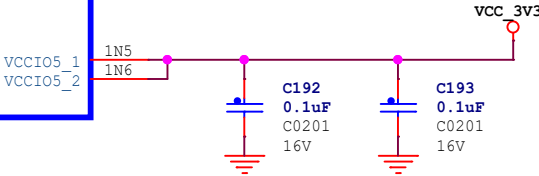
U9L

VCCIO5 Domain

Operating Voltage=1.8V/3.3V

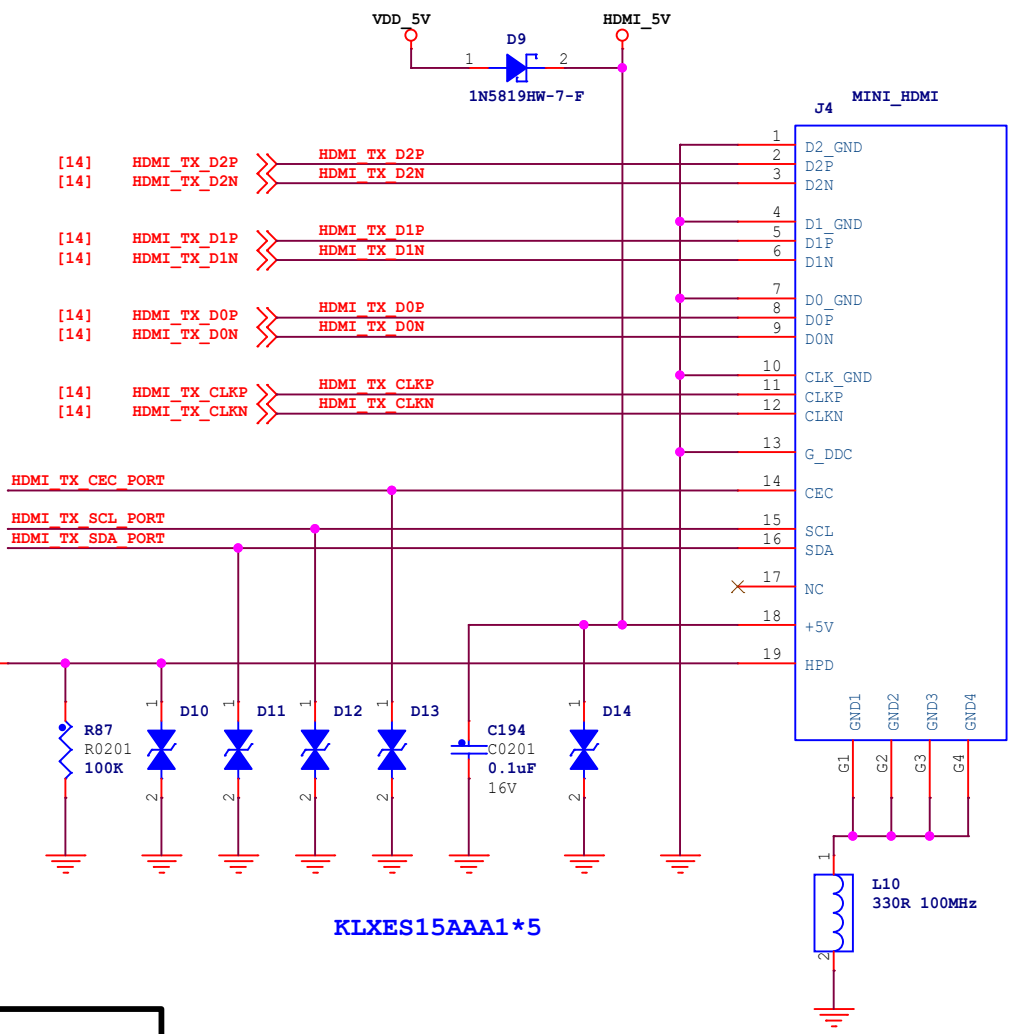
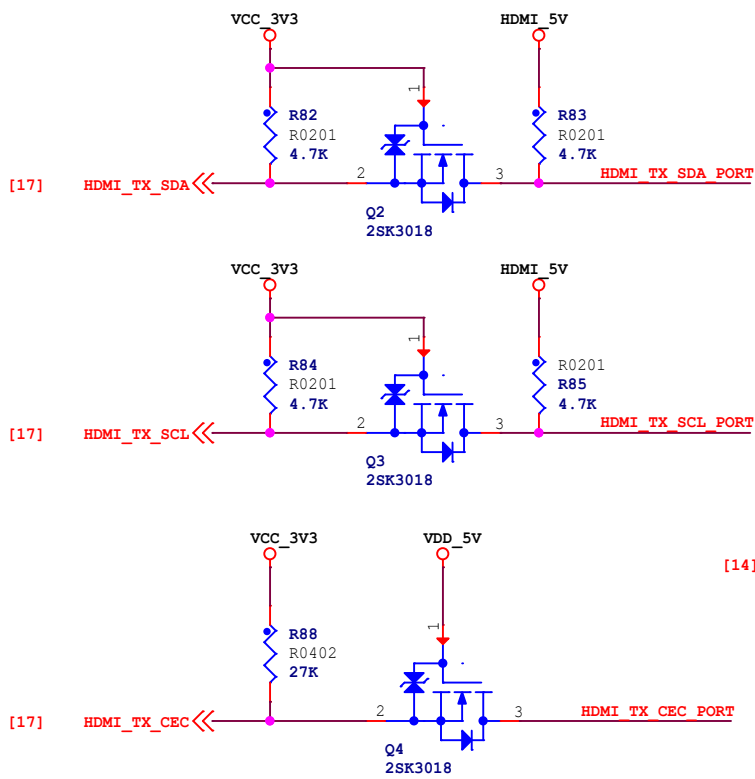


RK3566
bga565_15r50x14r40x0r90

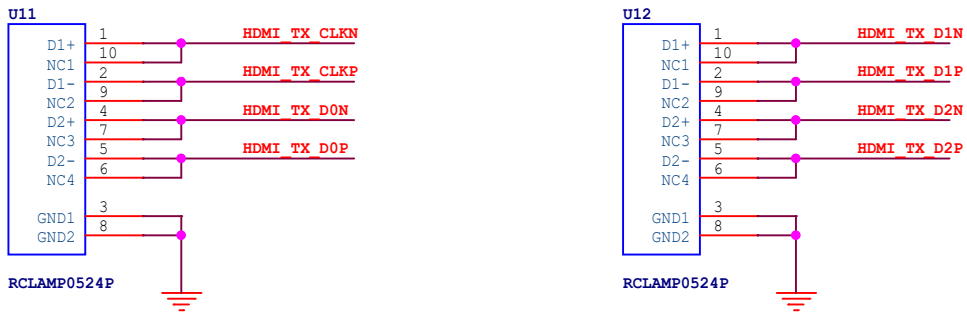


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Size A4	Document Number RK3566_VO Interface	Rev V0.1
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HDMI_PORT



ESD

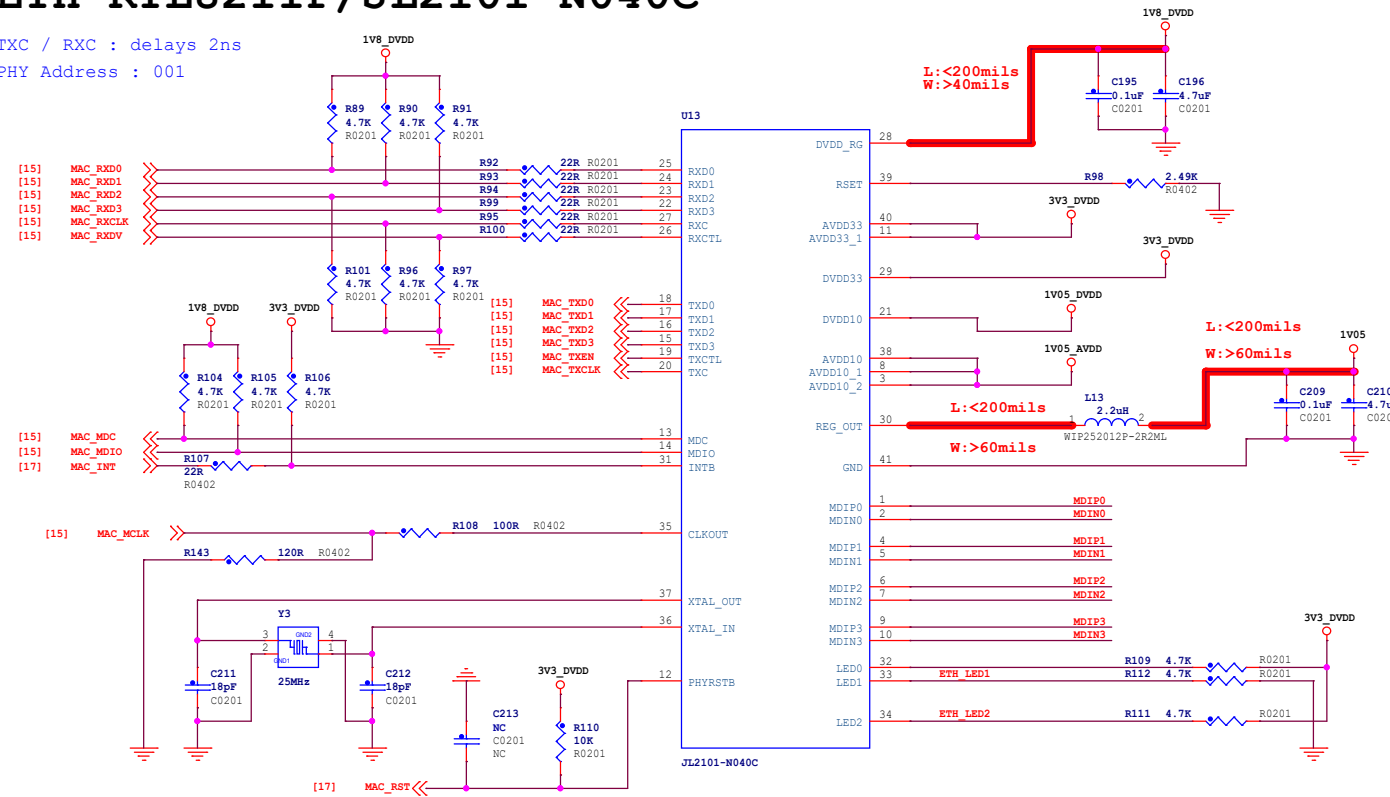


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Size A4	Document Number HDMI_PORT	Rev V0.1
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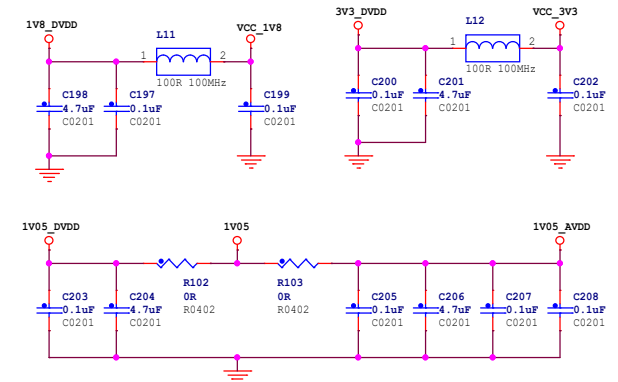
ETH RTL8211F/JL2101-N040C

TXC / RXC : delays 2ns

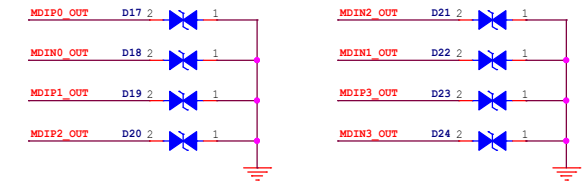
PHY Address : 001



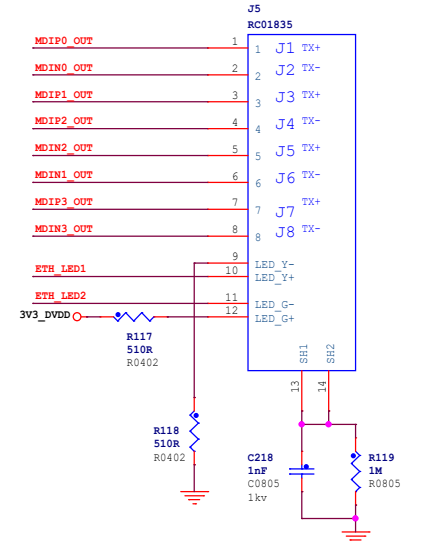
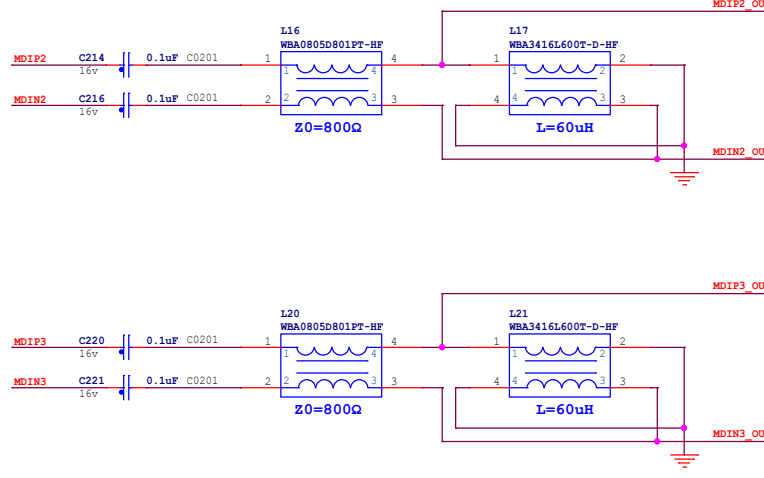
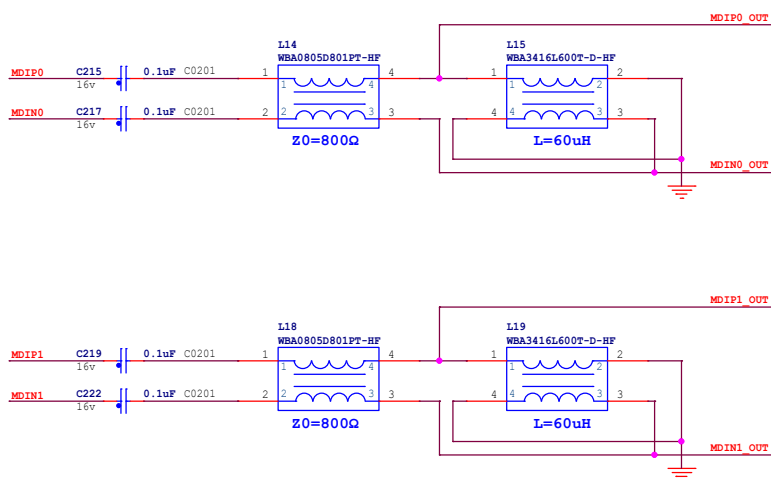
Power Filter



ESD KLXES15AAA1*8

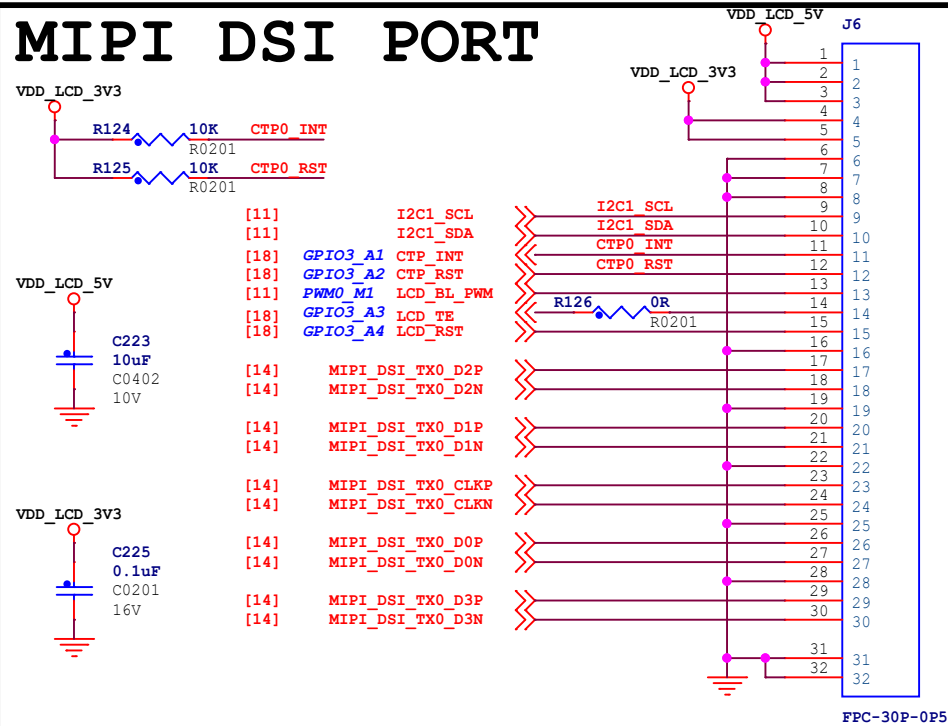


Chip LAN

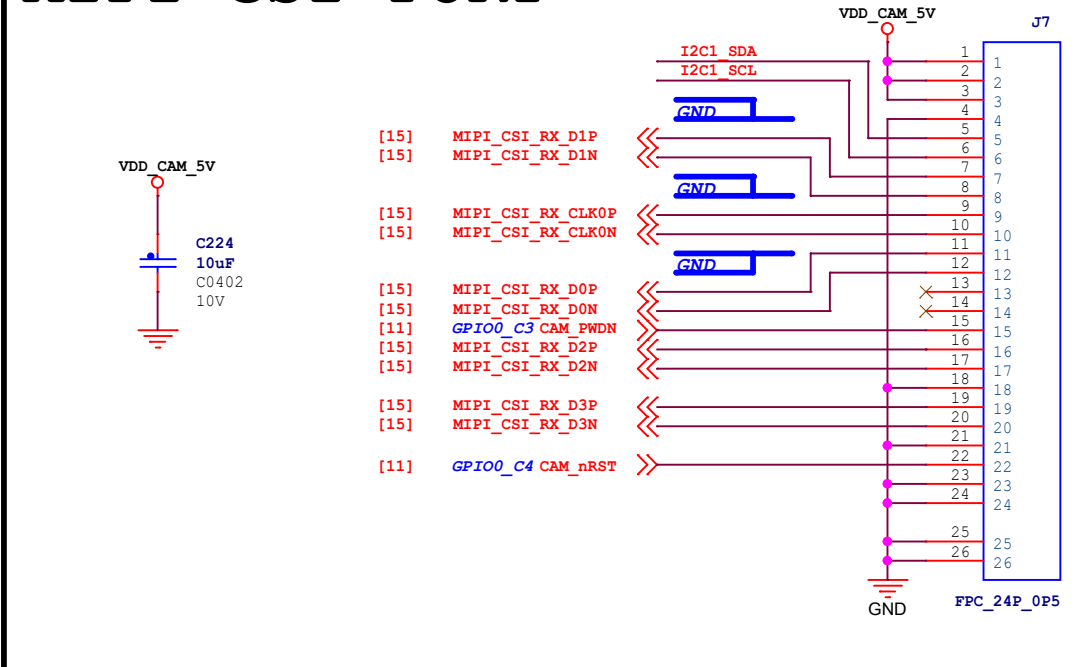


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A3	ETH RTL8211F/JL2101-N040C	V0.1
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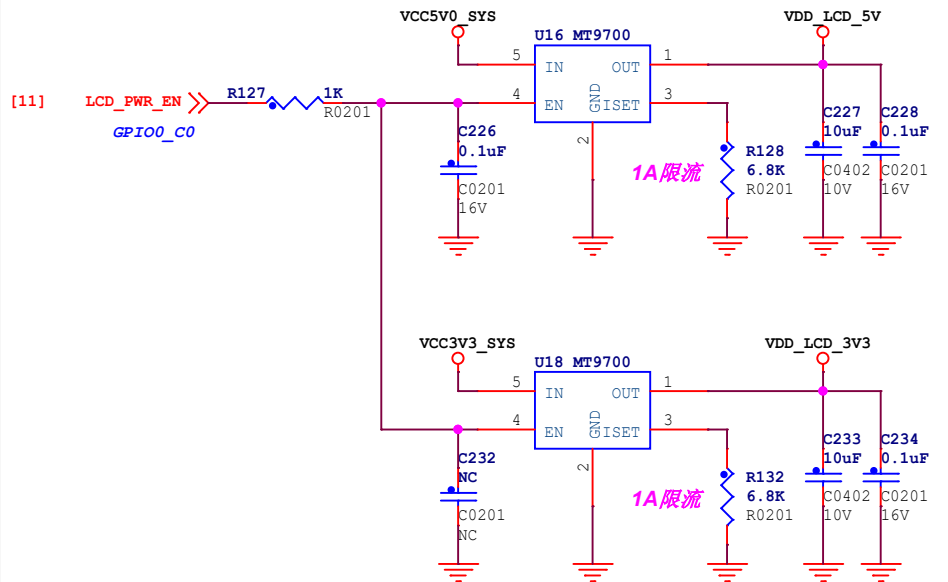
MIPI DSI PORT



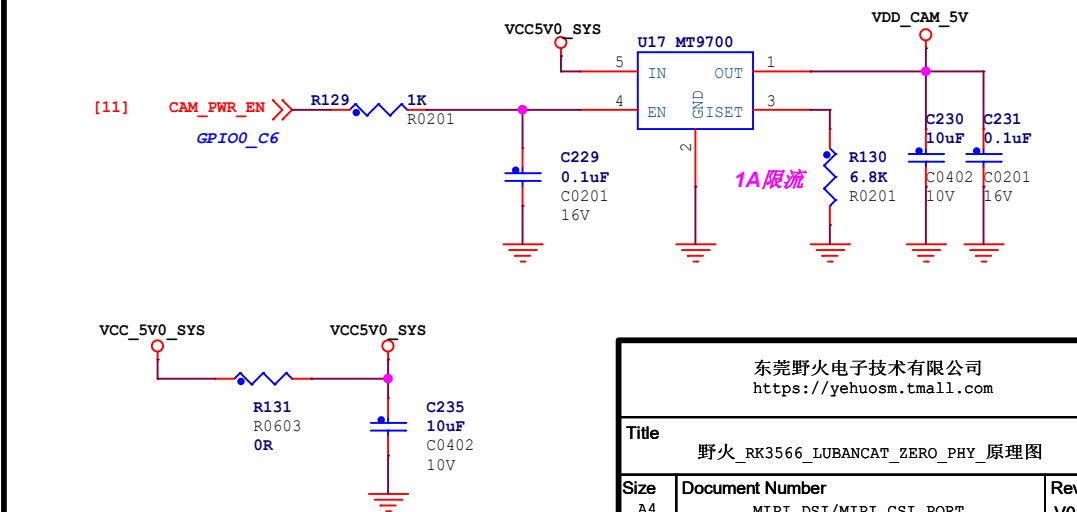
MIPI CSI PORT



LCD Power



MIPI CSI Power



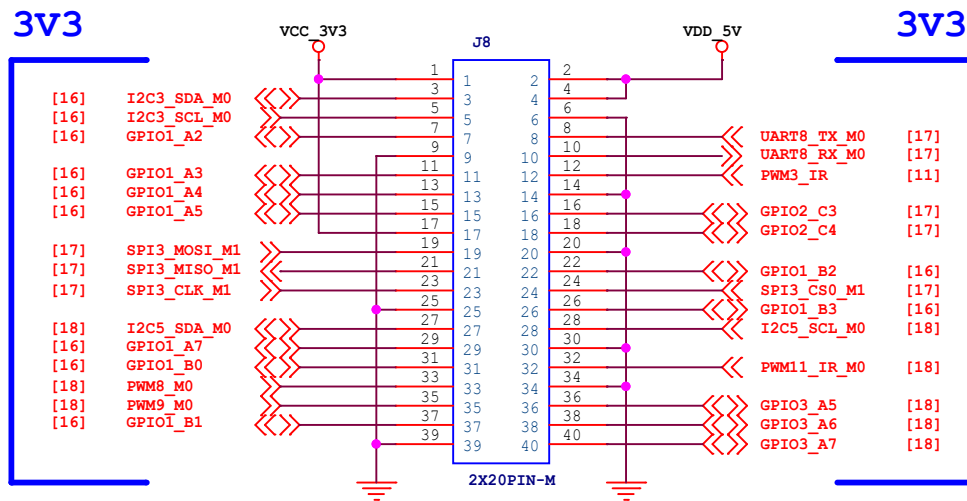
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Title 野火_RK3566_LUBANCAT_ZERO_PHY_原理图

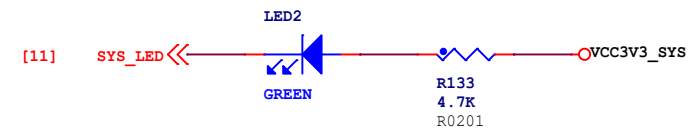
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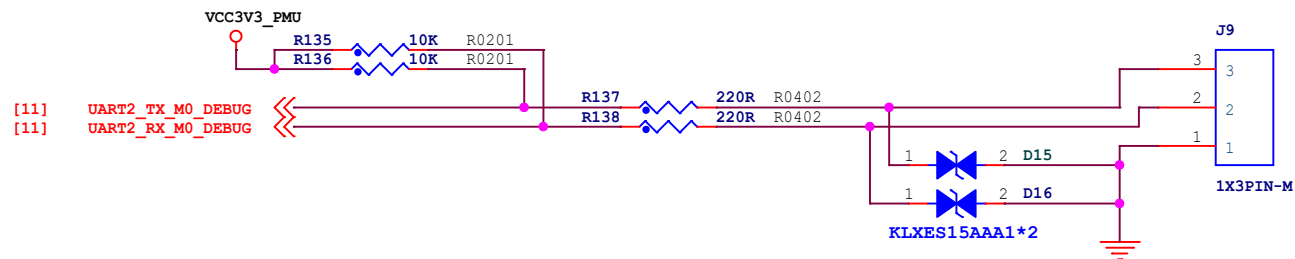
Pin Out



SYS_LED



Debug UART2



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Title 野火_RK3566_LUBANCAT_ZERO_PHY_原理图

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